

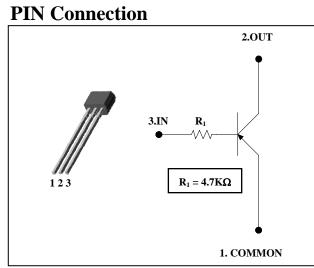
PNP Silicon Transistor

Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density



Ordering Information

Type NO.	Marking	Package Code	
SRA2210M	2210	TO-92M	

Absolute Maximum Ratings

Absolute Maximum Ratings			(Ta=25°C)
Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	VI	-20, 5	V
Output current	Ι _Ο	-100	mA
Power dissipation	P _D	400	mW
Junction temperature	TJ	150	°C
Storage temperature range	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

Electrical Characteristics (7)				(Ta:	=25°C)	
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	V ₀ =-50V, V ₁ =0	-	-	-500	nA
DC current gain	Gı	$V_0 = -5V$, $I_0 = -10mA$	120	-	-	-
Output voltage	V _{O(ON)}	I ₀ =-10mA, I ₁ =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = -0.2V$, $I_0 = -5mA$	-	-0.8	-1.2	V
Input voltage (OFF)	V _{I(OFF)}	$V_0 = -5V$, $I_0 = -0.1mA$	-0.3	-0.55	-	V
Transition frequency	f _T *	V_0 =-10V, I_0 =-5mA, f=1MHz	-	200	-	MHz
Input current	I ₁	V ₁ =-5V, I ₀ =0	-	-	-1.8	mA
Input resistor (Input to base)	R_1	-	3.3	4.7	6.1	KΩ

* : Characteristic of transistor only

Electrical Characteristic Curves

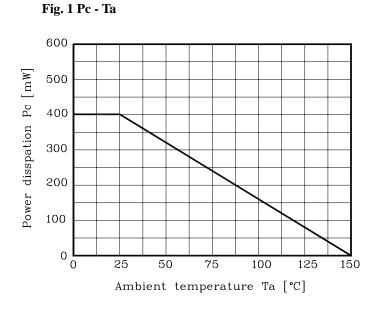


Fig. 3 I_O - $V_{I(OFF)}$

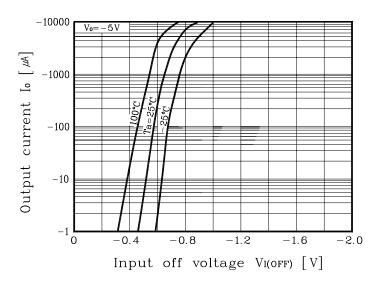
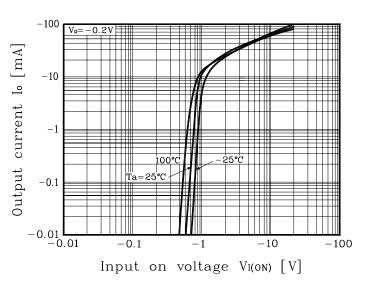
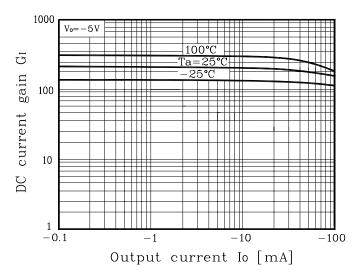


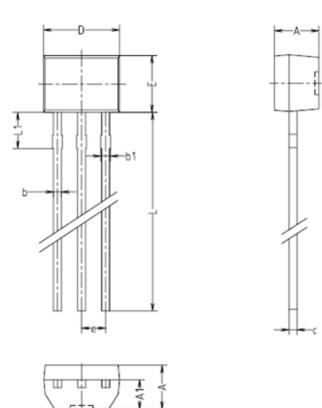
Fig. 2 I_O - $V_{I(ON)}$







Outline Dimension



	TO-92M				
SYMBOL	MINIMUM	NOMINAL	MAXIMUM		
A	2.25	2.30	2.35		
A1	1.50	1.55	1.60		
b	0.40	0.42	0.44		
b1	0.40	-	0.50		
С	0.40	0.42	0.44		
D	3.93	4.00	4.07		
E	2.93	3.00	3.07		
е	1.17	1.27	1.37		
L	14.30	14.50	14.70		
L1	2.05	2.15	2.25		

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