

SRA2212

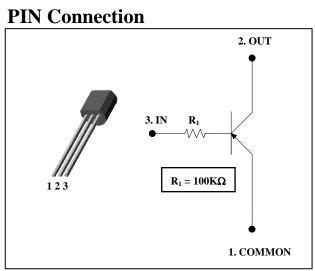
PNP Silicon Transistor

Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density



Ordering Information

Type NO.	Marking	Package Code	
SRA2212	SRA2212	TO-92	

Absolute Maximum Ratings

Absolute Maximum Ratings			(Ta=25°C)	
Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	-50	V	
Input voltage	VI	-40, 5	V	
Output current	Ι _ο	-100	mA	
Power dissipation	P _D	625	mW	
Junction temperature	TJ	150	°C	
Storage temperature range	T _{stg}	-55 ~ 150	°C	

Electrical Characteristics (Ta=25°C						
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = -50V, V_1 = 0$	-	-	-500	nA
DC current gain	Gı	$V_0 = -5V$, $I_0 = -10mA$	120	-	-	-
Output voltage	V _{O(ON)}	I ₀ =-10mA, I _I =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = -0.2V$, $I_0 = -5mA$	-	-	-4.4	V
Input voltage (OFF)	V _{I(OFF)}	V ₀ =-5V, I ₀ =-0.1mA	-0.3	-	-	V
Transition frequency	f_{T}^{*}	V_0 =-10V, I_0 =-5mA, f=1MHz	-	200	-	MHz
Input current	I ₁	V ₁ =-5V, I ₀ =0	-	-	-0.1	mA
Input resistor (Input to base)	R ₁	_	70	100	130	KΩ

* : Characteristic of transistor only

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Electrical Characteristic Curves

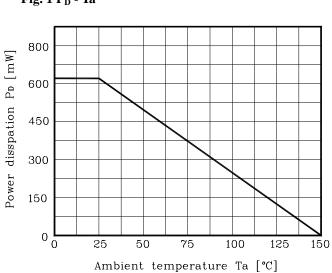


Fig. 1 P_D - Ta

Fig. 3 I_O - $V_{I(OFF)}$

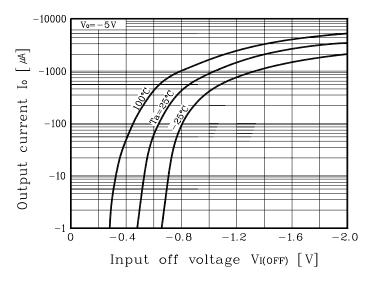
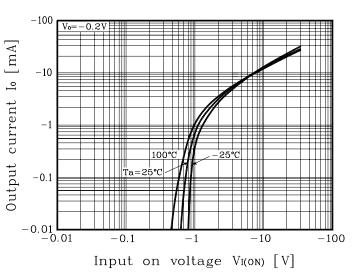
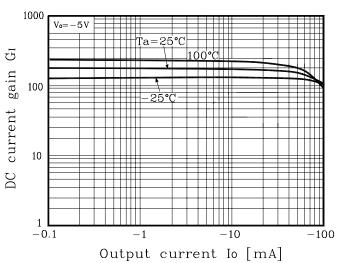


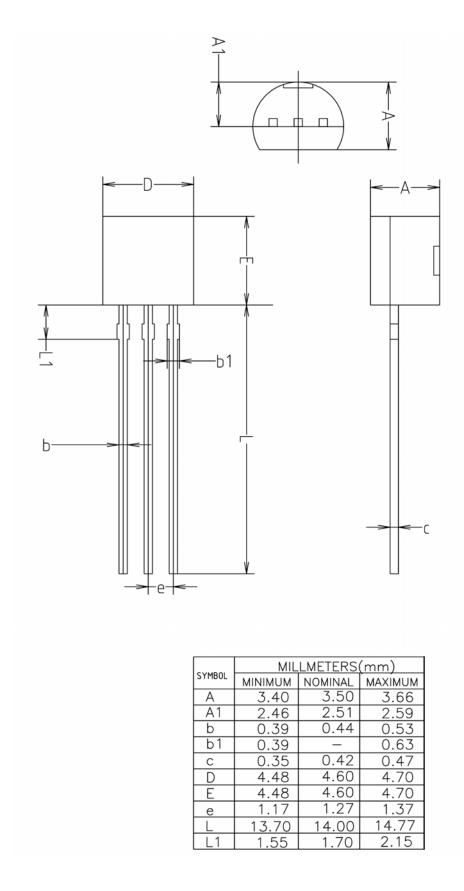
Fig. 2 I_O - $V_{I(ON)}$







Outline Dimension



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