

SRC1207S

NPN Silicon Transistor

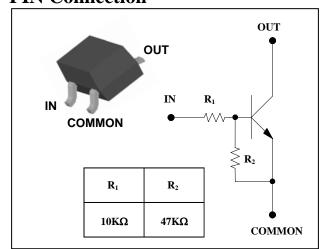
Descriptions

- Switching application
- Interface circuit and driver circuit application

Features

- With built-in bias resistors
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

PIN Connection



Ordering Information

Type NO.	Marking	Package Code	
SRC1207S	<u>RC7</u> □ ②	SOT-23	

①Device Code ②Year&Week Code

Absolute Maximum Ratings

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Out voltage	Vo	50	V
Input voltage	V _I	30,-6	V
Out current	Io	100	mA
Power dissipation	P_{D}	200	mW
Junction temperature	TJ	150	°C
Storage temperature	T _{stg}	-55 ~ 150	°C

Electrical Characteristics

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	$V_0 = 50V, V_1 = 0$	-	-	500	nA
DC current gain	G _I	$V_O=5V$, $I_O=10mA$	80	150	-	-
Output voltage	$V_{O(ON)}$	$I_0 = 10 \text{mA}, I_1 = 0.5 \text{mA}$	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	$V_0 = 0.2V$, $I_0 = 5mA$	-	-	1.8	V
Input voltage (OFF)	$V_{I(OFF)}$	$V_0 = 5V$, $I_0 = 0.1 \text{mA}$	0.5	-	-	V
Transition frequency	f_T^*	$V_0=10V$, $I_0=5$ mA, $f=1$ MHz	-	200	=	MHz
Input current	I ₁	$V_1 = 5V, I_0 = 0$	-	-	0.88	mA
Input resistor (Input to base)	R ₁	-	7	10	13	ΚΩ
Input resistor (Base to common)	R_2	-	33	47	61	ΚΩ

^{* :} Characteristic of transistor only

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Electrical Characteristic Curves

Fig. 1 P_D - Ta

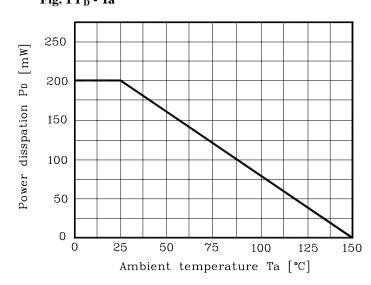


Fig. 2 I_O - $V_{I(ON)}$

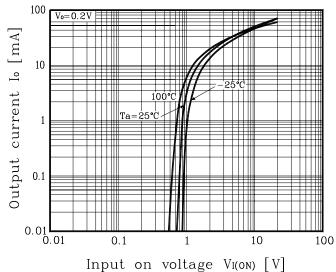


Fig. 3 $I_{\rm O}$ - $V_{I(OFF)}$

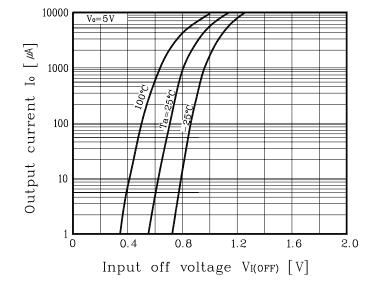
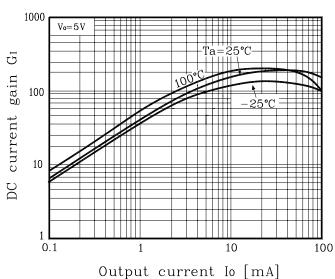
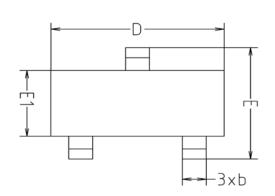


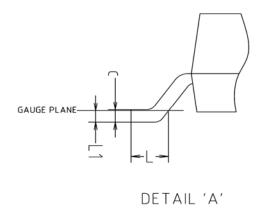
Fig. 4 G_I - I_O

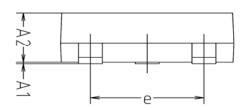


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Outline Dimension



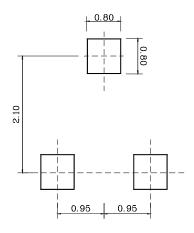






SYMBOL	MILLIMETERS			NOTE
3111000	MINIMUM	NOMINAL	MAXIMUM	11012
A1	0.00	-	0.10	
A2	0.82	-	1.02	
Ь	0.39	0.42	0.45	
С	0.09	0.12	0.15	
D	2.80	2.90	3.00	
E	2.20	2.40	2.60	
E1	1.20	1.30	1.40	
е	1.90BSC			
L	0.20	-	-	
L1	0.12BSC			

*Recommend PCB solder land [Unit: mm]



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