

**NPN Silicon Transistor** 

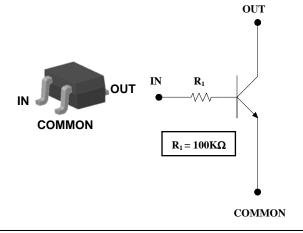
### **Descriptions**

- Switching application
- Interface circuit and driver circuit application

#### **Features**

- With built-in bias resistor
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- High packing density

## **PIN Connection**



### **Ordering Information**

Type NO.	Marking	Package Code
SRC1212E	<u>RB</u> □ ① ②	SOT-523
	Device Cede @ Veer®Week Cede	

①Device Code ② Year&Week Code

### Absolute Maximum Ratings

Absolute Maximum Ratings		(Ta=25°C)		
Characteristic	Symbol	Rating	Unit	
Output voltage	Vo	50	V	
Input voltage	VI	40, -5	V	
Output current	Ι <sub>ο</sub>	100	mA	
Power dissipation	P <sub>D</sub>	150	mW	
Junction temperature	ΤJ	150	°C	
Storage temperature range	T <sub>stg</sub>	-55 ~ 150	°C	

### **Electrical Characteristics**

Lieuti cai chai acteristics (1a=25						=25°C)
Characteristic	Symbol	<b>Test Condition</b>	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	$V_0 = 50V, V_1 = 0$	-	-	500	nA
DC current gain	Gı	$V_0 = 5V, I_0 = 10mA$	120	-	-	-
Output voltage	V <sub>O(ON)</sub>	I <sub>0</sub> =10mA, I <sub>1</sub> =0.5mA	-	0.1	0.3	V
Input voltage (ON)	V <sub>I(ON)</sub>	$V_0=0.2V, I_0=5mA$	-	-	4.4	V
Input voltage (OFF)	V <sub>I (OFF)</sub>	$V_0 = 5V, I_0 = 0.1mA$	0.3	-	-	V
Transition frequency	f <sub>T</sub> *	$V_0=10V$ , $I_0=5mA$ , f=1MHz	-	200	-	MHz
Input current	I <sub>1</sub>	V <sub>1</sub> =5V, I <sub>0</sub> =0	-	-	0.1	mA
Input resistor (Input to base)	$R_1$	-	70	100	130	KΩ

\* : Characteristic of transistor only

 $(T_0-25^{\circ}C)$ 

## **Electrical Characteristic Curves**

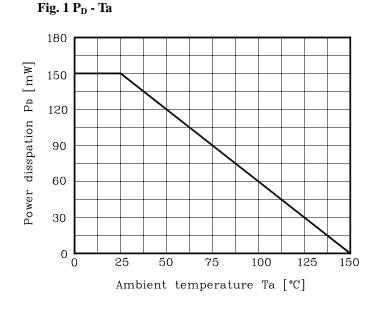


Fig. 3 I<sub>O</sub> - V<sub>I(OFF)</sub>

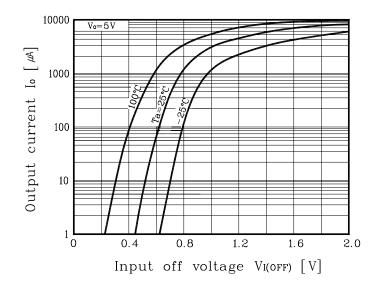


Fig. 2  $I_O$  -  $V_{I(ON)}$ 

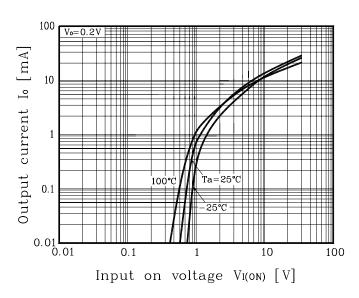
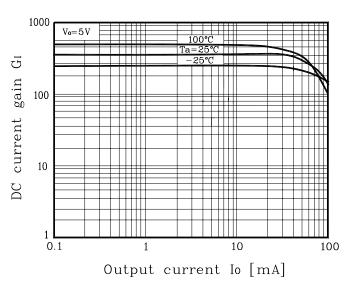
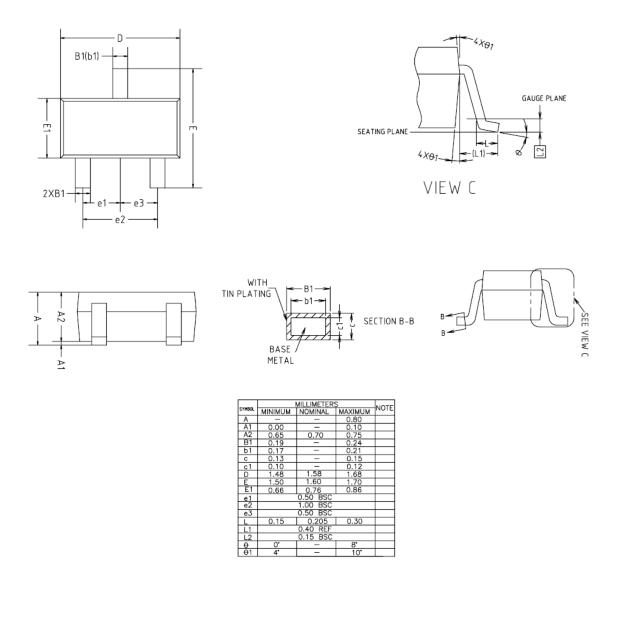


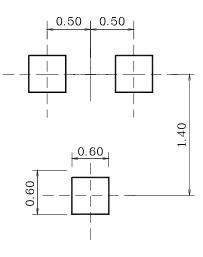
Fig. 4 G<sub>I</sub> - I<sub>O</sub>



## **Outline Dimension**



#### \*Recommend PCB solder land [Unit: mm]



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