

SS12A thru SS110A

SURFACE MOUNT SCHOTTKY BARRIER RECTIFIERS

REVERSE VOLTAGE - 20 to 100 Volts FORWARD CURRENT - 1.0 Ampere

A-SMA

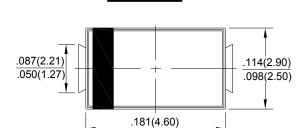
FEATURES

- For surface mounted applications
- Metal-Semiconductor junction with guarding
- Epitaxial construction
- Very low forward voltage drop
- High current capability
- Plastic material has UL flammability classification 94V-0
- For use in low voltage, high frequency inverters, free wheeling, and polarity protection applications.

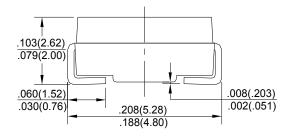
MECHANICAL DATA

●Case: Molded Plastic

Polarity: Indicated by cathode bandWeight: 0.002 ounces,0.053 grams



.157(4.00)



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave ,60Hz, resistive or inductive load.

For capacitive load, derate current by 20%

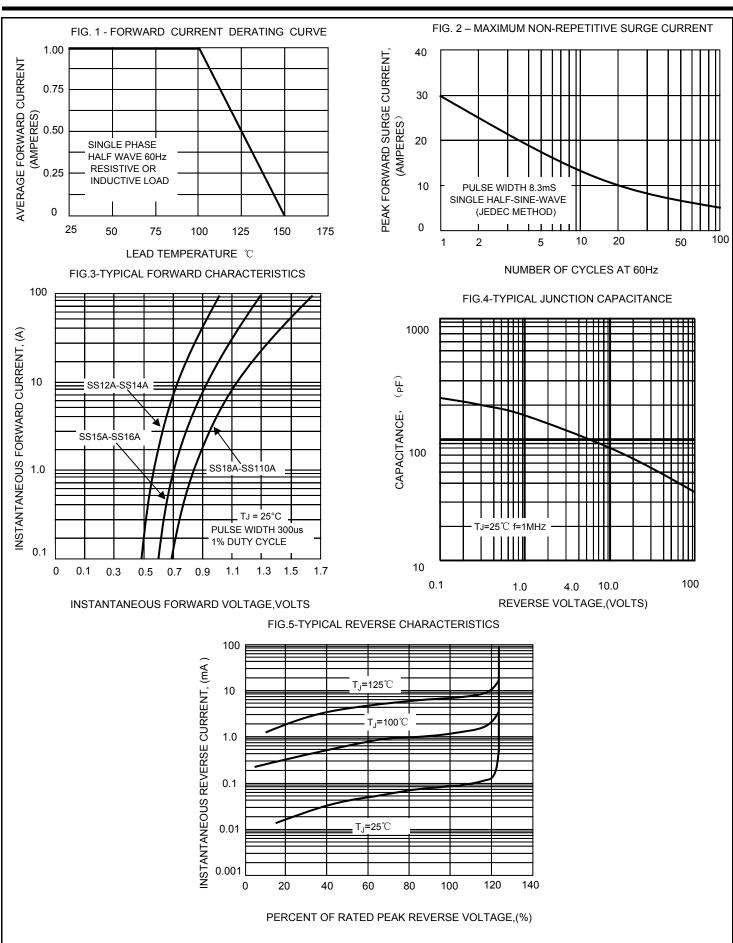
| CHARACTERISTICS | SYMBOL | SS12A | SS13A | SS14A | SS15A | SS16A | SS18A | SS110A | UNIT |
|--|--------|--------------|-------|-------|-------|-------|-------|--------|--------------|
| Maximum Recurrent Peak Reverse Voltage | VRRM | 20 | 30 | 40 | 50 | 60 | 80 | 100 | V |
| Maximum RMS Voltage | VRMS | 14 | 21 | 28 | 35 | 42 | 56 | 70 | V |
| Maximum DC Blocking Voltage | VDC | 20 | 30 | 40 | 50 | 60 | 80 | 100 | V |
| Maximum Average Forward Rectified Current @TL=100 ℃ | I(AV) | 1.0 | | | | | | | Α |
| Peak Forward Surge Current 8.3ms Single Half Sine-Wave Super Imposed On Rated Load (JEDEC Method) | IFSM | 30 | | | | | | | Α |
| Maximum Forward Voltage at 1.0A DC | VF | 0.55 | | | 0.70 | | 0.85 | | V |
| Maximum DC Reverse Current @T _J =25°C at Rated DC Blocking Voltage @TJ=100°C | lR | 1.0 10 | | | | | | | mA |
| Typical Junction Capacitance (Note1) | СJ | 110 | | | | | | | pF |
| Typical Thermal Resistance (Note2) | Røjl | 20 | | | | | | | °C/W |
| Operating Temperature Range | TJ | -55 to + 150 | | | | | | | $^{\circ}$ C |
| Storage Temperature Range | Tstg | -55 to + 150 | | | | | | | $^{\circ}$ |

NOTES:1.Measured at 1.0 MHz and applied reverse voltage of 4.0V DC.

- $\hbox{2.Thermal resistance junction to lead}.$
- 3.The typical data above is for reference only(典型值仅供参考).

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The curve graph is for reference only, can't be the basis for judgment(曲线图仅供参考)!

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