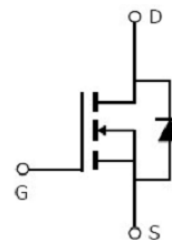


**Main Product Characteristics:**

$V_{DSS}$	600V
$R_{DS(on)}$	0.36Ω (typ.)
$I_D$	11A


**TO220**

**Marking and pin Assignment**

**Schematic diagram**
**Features and Benefits:**
**Features:**

- High dv/dt and avalanche capabilities
- 100% avalanche tested
- Low input capacitance and gate charge
- Low gate input resistance


**Description:**

The SSF11NS60 series MOSFETs is a new technology, which combines an innovative super junction technology and advance process. This new technology achieves low Rdson, energy saving, high reliability and uniformity, superior power density and space saving.

**Absolute max Rating:**

Symbol	Parameter	Max.	Units
$I_D @ TC = 25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$ ①	11	A
$I_D @ TC = 100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$ ①	7	
$I_{DM}$	Pulsed Drain Current②	44	
$P_D @ TC = 25^\circ C$	Power Dissipation③	162	W
	Linear Derating Factor	1.5	W/°C
$V_{DS}$	Drain-Source Voltage	600	V
$V_{GS}$	Gate-to-Source Voltage	± 30	V
$E_{AS}$	Single Pulse Avalanche Energy @ L=22.5mH	281	mJ
$I_{AS}$	Avalanche Current @ L=22.5mH	5	A
$T_J T_{STG}$	Operating Junction and Storage Temperature Range	-55 to +150	°C

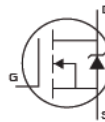
## Thermal Resistance

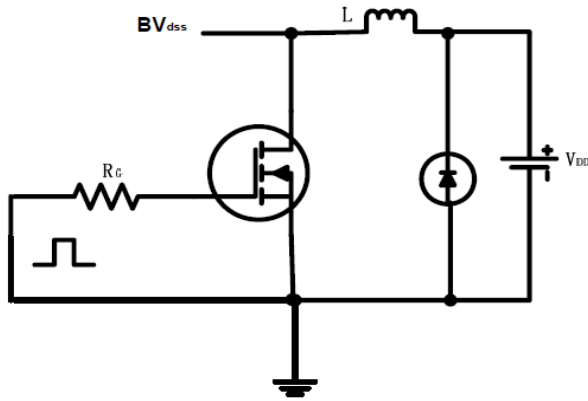
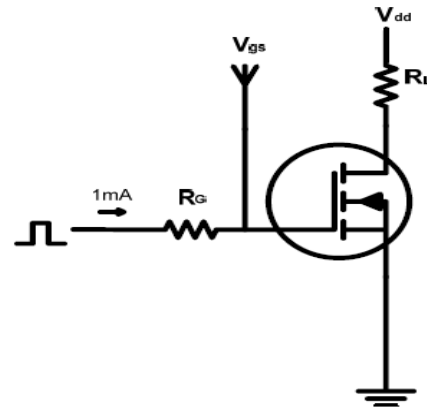
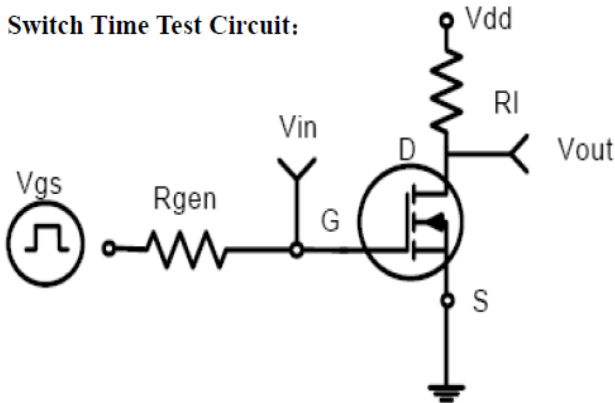
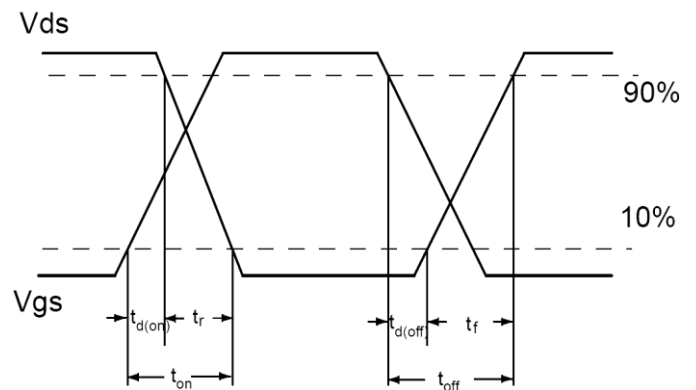
Symbol	Characterizes	Typ.	Max.	Units
R <sub>θJC</sub>	Junction-to-case <sup>③</sup>	—	0.77	°C/W
R <sub>θJA</sub>	Junction-to-ambient (t ≤ 10s) <sup>④</sup>	—	62	°C/W

## Electrical Characterizes @T<sub>A</sub>=25°C unless otherwise specified

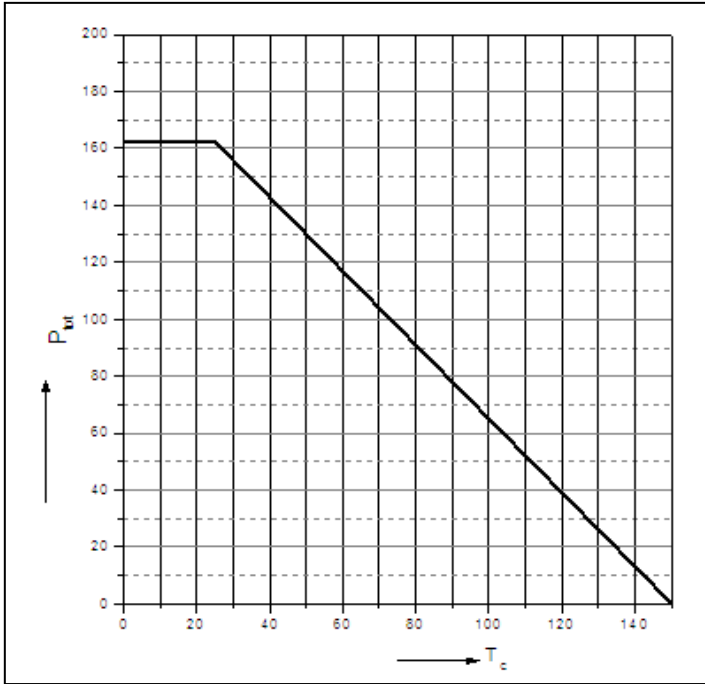
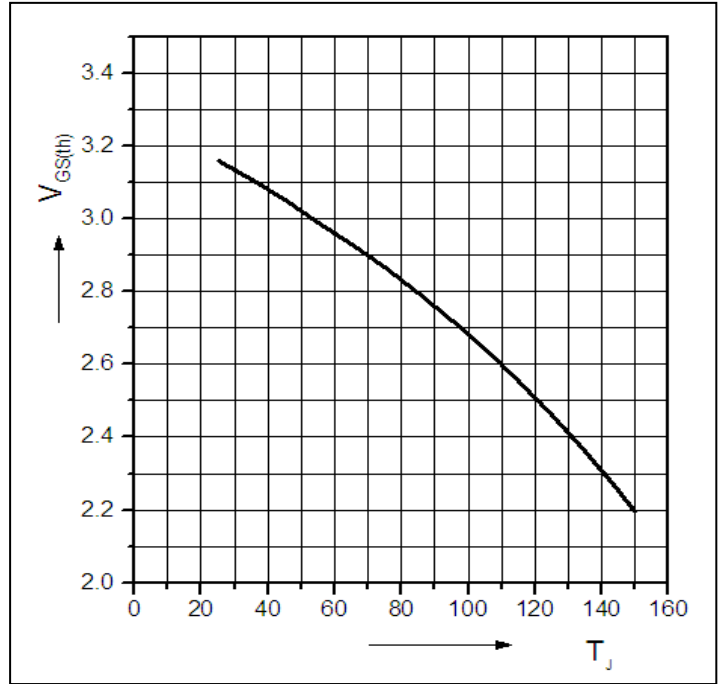
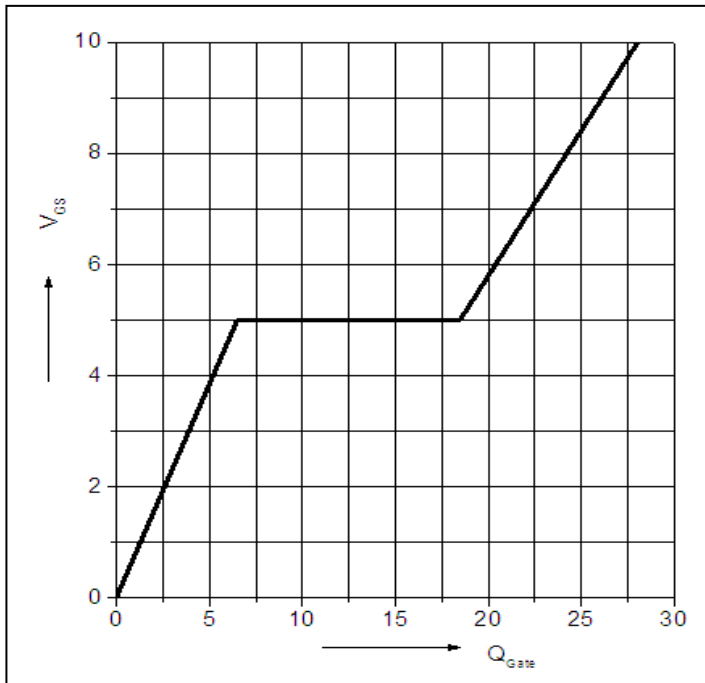
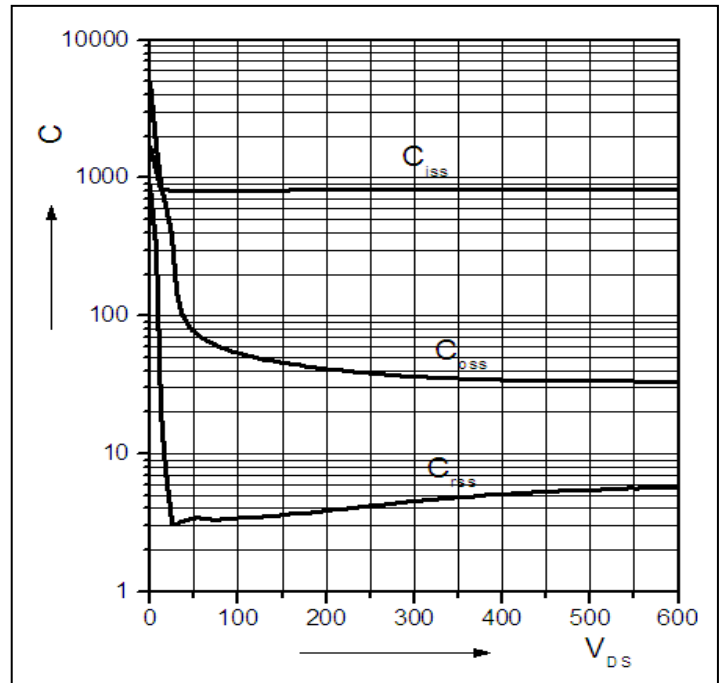
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source breakdown voltage	600	—	—	V	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250μA
R <sub>DS(on)</sub>	Static Drain-to-Source on-resistance	—	0.36	0.41	Ω	V <sub>GS</sub> =10V, I <sub>D</sub> = 5.5A
		—	0.88	—		T <sub>J</sub> = 125°C
V <sub>GS(th)</sub>	Gate threshold voltage	2	—	4	V	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250μA
		—	2.46	—		T <sub>J</sub> = 125°C
I <sub>DSS</sub>	Drain-to-Source leakage current	—	—	1	μA	V <sub>DS</sub> = 600V, V <sub>GS</sub> = 0V
		—	—	50		T <sub>J</sub> = 125°C
I <sub>GSS</sub>	Gate-to-Source forward leakage	—	—	100	nA	V <sub>GS</sub> = 30V
		—	—	-100		V <sub>GS</sub> = -30V
Q <sub>g</sub>	Total gate charge	—	28.41	—	nC	I <sub>D</sub> = 11A,
Q <sub>gs</sub>	Gate-to-Source charge	—	6.64	—		V <sub>DS</sub> =480V,
Q <sub>gd</sub>	Gate-to-Drain("Miller") charge	—	12.34	—		V <sub>GS</sub> = 10V
t <sub>d(on)</sub>	Turn-on delay time	—	12.85	—	ns	V <sub>GS</sub> =10V, V <sub>DS</sub> =300V,
t <sub>r</sub>	Rise time	—	9.45	—		R <sub>L</sub> =54.5Ω,
t <sub>d(off)</sub>	Turn-Off delay time	—	30.40	—		R <sub>GEN</sub> =4.7Ω
t <sub>f</sub>	Fall time	—	6.30	—		I <sub>D</sub> =5.5A
C <sub>iss</sub>	Input capacitance	—	824.8	—	pF	V <sub>GS</sub> = 0V
C <sub>oss</sub>	Output capacitance	—	78.06	—		V <sub>DS</sub> = 50V
C <sub>rss</sub>	Reverse transfer capacitance	—	2.75	—		f = 600KHz

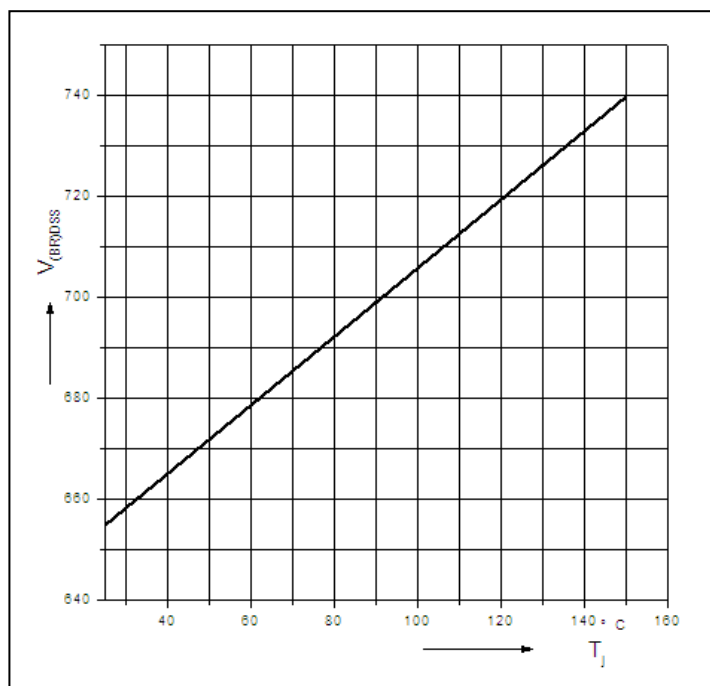
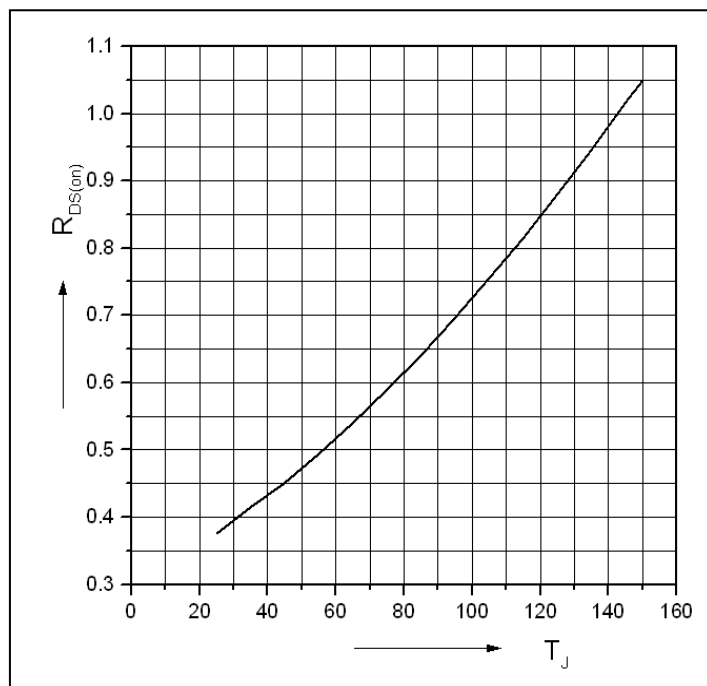
## Source-Drain Ratings and Characteristics

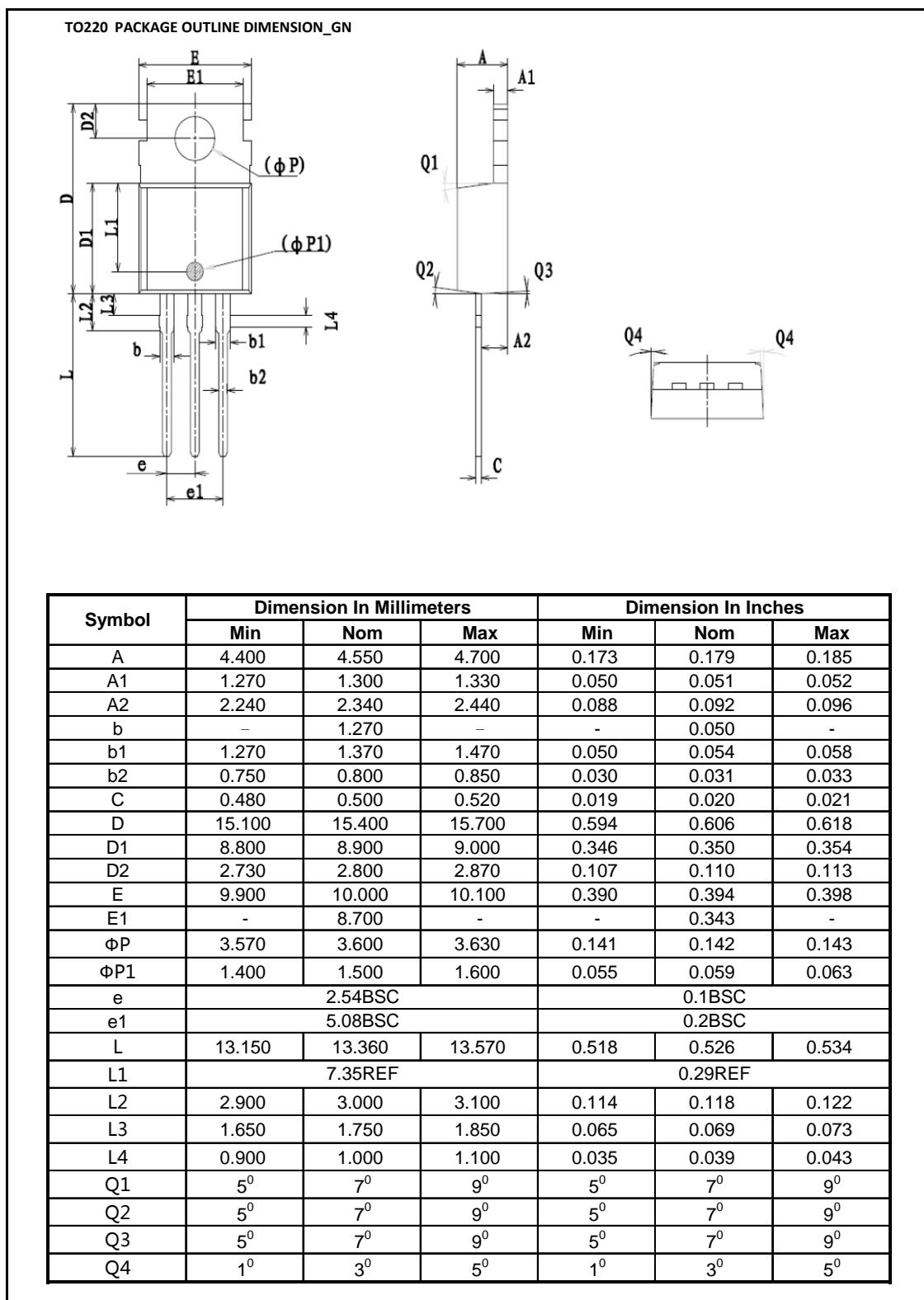
Symbol	Parameter	Min.	Typ.	Max.	Units	Conditions
I <sub>S</sub>	Continuous Source Current (Body Diode)	—	—	11	A	MOSFET symbol showing the integral reverse p-n junction diode. 
I <sub>SM</sub>	Pulsed Source Current (Body Diode)	—	—	44	A	
V <sub>SD</sub>	Diode Forward Voltage	—	—	1.5	V	I <sub>S</sub> =11A, V <sub>GS</sub> =0V
t <sub>rr</sub>	Reverse Recovery Time	—	313	—	ns	T <sub>J</sub> = 25°C, I <sub>F</sub> = 11A, di/dt = 100A/μs
Q <sub>rr</sub>	Reverse Recovery Charge	—	2.97	—	uC	

**Test circuits and Waveforms**
**EAS test circuits:**

**Gate charge test circuit:**

**Switch Time Test Circuit:**

**Switch Waveforms:**

**Notes:**

- ① The maximum current rating is limited by bond-wires.
- ② Repetitive rating; pulse width limited by max. junction temperature.
- ③ The power dissipation PD is based on max. junction temperature, using junction-to-case thermal resistance.
- ④ The value of  $R_{\theta JA}$  is measured with the device mounted on 1in 2 FR-4 board with 2oz. Copper, in a still air environment with  $T_A = 25^\circ\text{C}$
- ⑤ These curves are based on the junction-to-case thermal impedance which is measured with the device mounted to a large heatsink, assuming a maximum junction temperature of  $T_{J(MAX)} = 150^\circ\text{C}$ .

**Typical electrical and thermal characteristics**

**Figure 1: Power dissipation**

**Figure 2. Typ. Gate to source cut-off voltage**

**Figure 3. Typ. gate charge**

**Figure 4: Typ. Capacitances**

**Typical electrical and thermal characteristics**

**Figure 5. Drain-source breakdown voltage**

**Figure 6. Drain-source on-state resistance**

**Mechanical Data:**


**Ordering and Marking Information**
**Device Marking: SSF11NS60**

**Package (Available)**  
**TO220**  
**Operating Temperature Range**  
**C : -55 to 150 °C**

**Devices per Unit**

Package Type	Units/Tube	Tubes/Inner Box	Units/Inner Box	Inner Boxes/Carton Box	Units/Carton Box
TO220	50	20	1000	6	6000

**Reliability Test Program**

Test Item	Conditions	Duration	Sample Size
High Temperature Reverse Bias(HTRB)	$T_j=125^{\circ}\text{C}$ to $150^{\circ}\text{C}$ @ 80% of Max $V_{DSS}/V_{CES}/V_R$	168 hours 500 hours 1000 hours	3 lots x 77 devices
High Temperature Gate Bias(HTGB)	$T_j=150^{\circ}\text{C}$ @ 100% of Max $V_{GSS}$	168 hours 500 hours 1000 hours	3 lots x 77 devices

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