TOSHIBA Field Effect Transistor Silicon P-Channel MOS Type

SSM3J113TU

High Speed Switching Applications

- 2.0V drive
 - Low on-resistance: $R_{on} = 449m\Omega (max) (@V_{GS} = -2.0 V)$

 $R_{on} = 249m\Omega \text{ (max)} (@V_{GS} = -2.5 \text{ V})$

 $R_{on} = 169m\Omega \text{ (max)} (@V_{GS} = -4.0 \text{ V})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Drain-Source voltage		V _{DS}	-20	V	
Gate-Source voltage		V _{GSS}	± 12	V	
Drain current	DC	۱ _D	-1.7	А	
	Pulse	I _{DP}	-3.4		
Drain power dissipation		PD (Note 1)	800	mW	
		PD (Note 2)	500		
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	

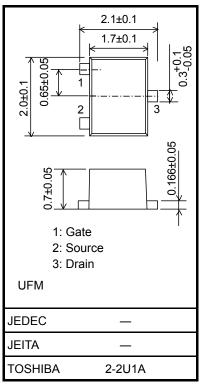
Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Mounted on ceramic board. (25.4 mm \times 25.4 mm \times 0.8 mm, Cu Pad: 645 mm2) Note 2: Mounted on FR4 board.

(25.4 mm \times 25.4 mm \times 1.6 mm, Cu Pad: 645 mm2)

Electrical Characteristics (Ta = 25°C)



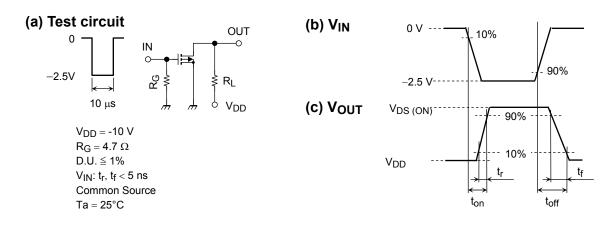
Weight: 6.6 mg (typ.)

Charact	eristic	Symbol	Test Conditions	Min	Тур.	Max	Unit
Drain-Source breakdown voltage		V (BR) DSS	$I_D = -1 \text{ mA}, V_{GS} = 0$	-20	_		v
		V (BR) DSX	$I_D = -1 \text{ mA}, V_{GS} = +12 \text{V}$	-8	—		
Drain cut-off curren	t	I _{DSS}	$V_{DS}=-20~V,~V_{GS}=0$	_	_	-1	μA
Gate leakage curre	nt	I _{GSS}	$V_{GS}=\pm 12V,\ V_{DS}=0$	_	_	±1	μA
Gate threshold volta	age	V _{th}	$V_{DS} = -3 \text{ V}, \text{ I}_{D} = -0.1 \text{ mA}$	-0.5	_	-1.1	V
Forward transfer ac	Imittance	Y _{fs}	$V_{DS} = -3 V, I_D = -0.65 A$ (Note3)	1.3	2.7	_	S
Drain-Source on-resistance		R _{DS} (ON)	$I_D = -0.65 \text{ A}, V_{GS} = -4.0 \text{ V}$ (Note3)	_	129	169	mΩ
			$I_D = -0.65 \text{ A}, V_{GS} = -2.5 \text{ V}$ (Note3)		189	249	
			$I_D = -0.65 \text{ A}, V_{GS} = -2.0 \text{ V}$ (Note3)		249	449	
Input capacitance		C _{iss}	$V_{DS} = -10 V, V_{GS} = 0, f = 1 MHz$	_	370	_	pF
Output capacitance		C _{oss}	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$	_	116		pF
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, \text{ V}_{GS} = 0, \text{ f} = 1 \text{ MHz}$	_	73		pF
Switching time	Turn-on time	t _{on}	$V_{DD} = -10 \text{ V}, \text{ I}_D = -0.65 \text{ A},$ $V_{GS} = 0 \sim -2.5 \text{ V}, \text{ R}_G = 4.7 \Omega$	_	33		ns
	Turn-off time	t _{off}		_	47		
Drain-Source forward voltage		V _{DSF}	$I_D = 1.7 \text{ A}, V_{GS} = 0 \text{ V}$ (Note3)	_	0.77	1.2	V

Note3: Pulse test

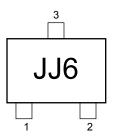
Unit: mm

Switching Time Test Circuit



Marking

Equivalent Circuit (top view)



Precaution

 V_{th} can be expressed as the voltage between gate and source when the low operating current value is I_D =–0.1mA for this product. For normal switching operation, $V_{GS\ (on)}$ requires a higher voltage than $V_{th,}$ and $V_{GS\ (off)}$ requires a lower voltage than $V_{th.}$

(The relationship can be established as follows: $V_{GS (off)} < V_{th} < V_{GS (on)}$)

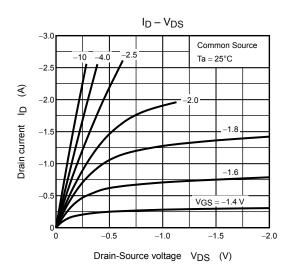
Take this into consideration when using the device.

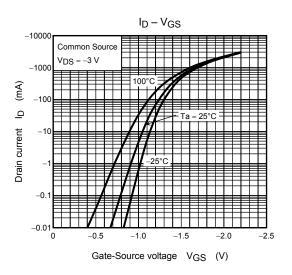
Handling Precaution

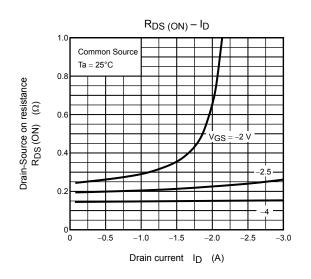
When handling individual devices which are not yet mounted on a circuit board, be sure that the environment is protected against electrostatic discharge. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.

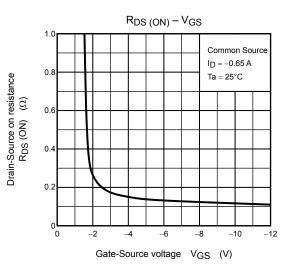
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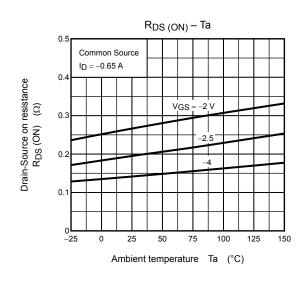


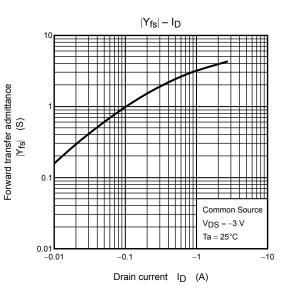




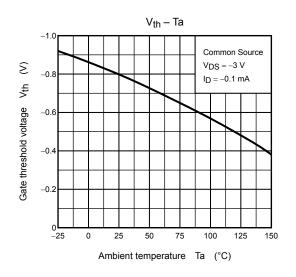


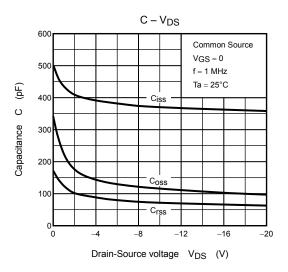


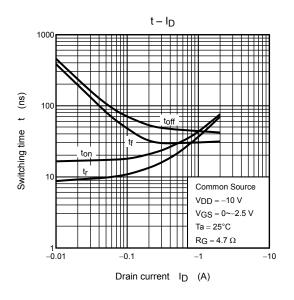


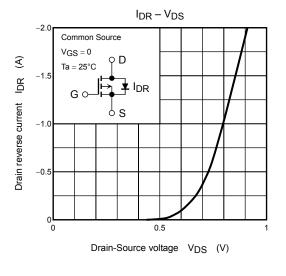


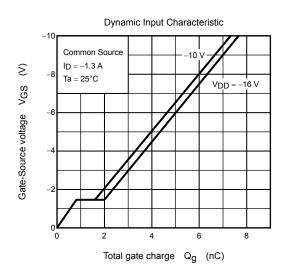
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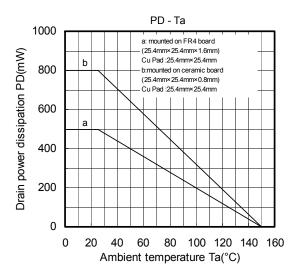




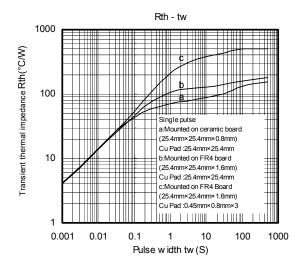








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