

MULTIPLE RS-232 DRIVERS AND RECEIVERS

- MEETS AND EXCEEDS THE REQUIREMENTS OF EIA/TIA-232-E AND ITUV.28 STANDARD
- SINGLE CHIP WITH EASY INTERFACE BETWEEN UART AND SERIAL PORT CONNECTOR OF IBM PC/ATTM AND COMPATIBLES
- DESIGNED TO SUPPORT DATA RATES UP TO 120 Kbps
- PINOUT COMPATIBLE WITH ST75C185

DESCRIPTION

The ST75185 contains three drivers and five receivers. The pinout matches the DB9S connector design in order to decrease the part count, reduce the board space required and allow easy interconnection of the UART and serial port connector of IBM PC/ATTM and compatibles. The bipolar circuits and processing of the ST75185 provides a rugged low-cost solution for this function at the expense of quiescent power and external passive components relative to the ST75C185.

The ST75185 complies with the requirements of the EIA/TIA 232-E and ITU (formally CCITT) v.28 standards. These standards are for data interchange between a host computer and peripheral at signalling rates up to 20k-bits/s. The switching speeds of the ST75185 are fast enough to support rates up to 120K-bits/s with lower capacitive loads (shorter cables). Interoperability at the higher signalling rates cannot be assured

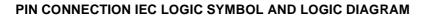
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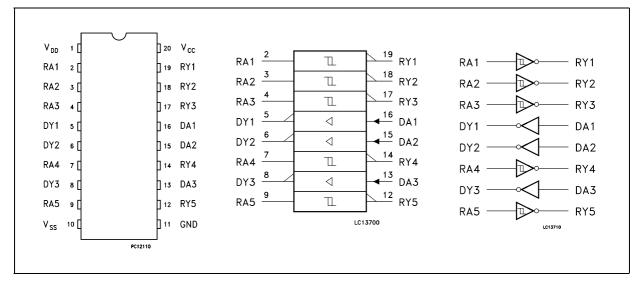
unless the designer has design control of the cable and the interface circuits at the both ends. For interoperability at signalling rates to 120 K-bits/s, use of EIA/ITA-423-B (ITU v.10) and EIA/ITA-422-B (ITU v.11) standards are recommended.

The ST75185 is characterized for operation over the range of 0°C to 70°C.

ORDERING CODES

Туре	Temperature Range	Package	Comments
ST75185CTR	0 to 70 °C	TSSOP20 (Tape & Reel)	2500 parts per reel
ST75185CD	0 to 70 °C	SO-20 (Tube)	40 parts per tube / 25 tubes per box
ST75185CDR	0 to 70 °C	SO-20 (Tape & Reel)	1000 parts per reel
ST75185CPR	0 to 70 °C	SSOP-20 (Tape & Reel)	1350 parts per reel





PIN DESCRIPTION

PIN N°	SYMBOL	NAME AND FUNCTION
1	V _{DD}	Supply Voltage (+12V)
2	RA1	First Receiver Input
3	RA2	Second Receiver Input
4	RA3	Third Receiver Input
5	DY1	First Driver Output
6	DY2	Second Driver Output
7	RA4	Fourth Receiver Input
8	DY3	Third Driver Output
9	RA5	Fifth Receiver Input
10	V _{SS}	Supply Voltage (-12V)
11	GND	Ground
12	RY5	Fifth Receiver Ouput
13	DA3	Third Driver Intput
14	RY4	Fourth Receiver Ouput
15	DA2	Second Driver Input
16	DA1	First Driver Input
17	RY3	Third Receiver Ouput
18	RY2	Second Receiver Output
19	RY1	First Receiver Output
20	V _{CC}	Supply Voltage (+5V)

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ABSOLUTE MAXIMUM RATINGS OVER OPERATING FREE-AIR TEMPERATURE RANGE

Symbol	Parameter	Value	Unit
V _{DD}	Supply Voltage (Note 1)	15	V
V _{SS}	Supply Voltage (Note 1)	-15	V
V _{CC}	Supply Voltage (Note 1)	10	V
VI	Input Voltage Range (DRIVER)	-15 to 7	V
VI	Input Voltage Range (RECEIVER)	-30 to 30	V
Vo	Output Voltage Range (DRIVER)	-15 to 15	V
Ι _Ο	Receiver Low Level Output Current	20	mA
PD	Continuous Total Power Dissipation	See dissipation Rating Table	
T _A	Operating Free-Air Tempereature Range	0 to 70	°C
T _{stg}	Storage Temperature Range	-65 to + 150	°C
ΤL	Lead Temperature 1.6mm from case for 10 sec	260	°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these condition is not implied. NOTE 1: All voltage are with respect to the network ground terminal.

DISSIPATION RATING TABLE

Package	Power Rating	Derating Factor	Power Rating
	at T _A ≤ 25°C	above T _A = 25°C (*)	at T _A ≤ 70°C
MICROPACKAGE (D)	1125 mW	9.0 mW/°C	720 mW

(*) This is the reverse of the traditional junction-case thermal resistance $\mathsf{R}_{tJ\text{-}C}$

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit	
V _{DD}	Supply Voltage		7.5	15	V
V _{SS}	Supply Voltage		-7.5	-15	V
V _{CC}	Supply Voltage		4.5	5.5	V
VI	Driver Input Voltage		0	V _{CC}	V
I _{ОН}	High Level Output Current	DRIVER		-6	mA
ЧОН			-0.5	IIIA	
I _{OL}	Low Level Output Current	DRIVER		6	mA
OL		RECEIVER		16	IIIA
T _A	Operating Free-Air Tempereature Range		0	70	°C

SUPPLY CURRENTS

	D	-	Fest Cond	itions		Value		11
Symbol	Parameter	V _{DD}	V _{SS}		Min.	Тур.	Max.	Unit
I _{DD}	Supply Current from V _{DD}	9	-9	No load.			15	mA
		12	-12	All inputs at			19	
		15	-15	1.9V			25	
		9	-9	No load.			4.5	mA
		12	-12	All inputs at			5.5	
		15	-15	0.8V			9	
I _{SS}	Supply Current from V _{SS}	9	-9	No load.			-15	mA
		12	-12		All inputs at -19			
		15	-15	1.9V			-25	
		9	-9	No load.			-3.2	mA
		12	-12	All inputs at			-3.2	
		15	-15	0.8V			-3.2	
I _{CC}	Supply Current from V_{CC}	No I	oad. All inp V _{CC} = 5				30	mA

DRIVER ELECTRICAL CHARACTERISTICS OVER OPERATING FREE-AIR TEMPERATURE **RANGE** (V_{DD} = 9V, V_{SS} = -9V, V_{CC} = 5V, unless otherwise specified)

Symbol	Parameter	Test Conditions		Unit		
Symbol	Farameter	Test conditions	Min.	Тур.	Max.	Unit
V _{OH}	High Level Output Voltage	$V_{IL} = 0.8 V R_L = 3K\Omega$ (See Figure 1)	6	7.5		V
V _{OL}	Low Level Output Voltage (Note 3)	$V_{IH} = 1.9 V R_L = 3K\Omega$ (See Figure 1)		-7.5	-6	V
I _{IH}	High Level Input Current	V _I = 5 V (See Figure 2)			10	μΑ
۱ _{IL}	Low Level Input Current	$V_1 = 0 V$ (See Figure 2)			-1.6	mA
I _{OS(H)}	High Level Short Circuit Output Current (Note 4)	$V_{IL} = 0.8 V$ $V_O = 0 V$ (See Figure 1)	-4.5	-12	-19.5	mA
I _{OS(L)}	Low Level Short Circuit Output Current	$V_{IH} = 2 V V_O = 0 V (See Figure 1)$	4.5	12	19.5	mA
R _O	Output Resistance	$V_{DD} = V_{SS} = V_{CC} = 0 V$ $V_{O} = -2 \text{ to } 2 V \text{ (Note 5)}$	300			Ω

NOTE 3: The algebraic convention, where the more positive (less negative) limits designated as maximum, is used in this datasheet for logic levels only (e.g. if - 10V is a maximum, the typical value is a more negative voltage). NOTE 4: Output short circuit conditions must maintain the total power dissipation below absolute maximum ratings. NOTE 5: Test conditions are those specified by EIA-232-E and as listed above.

Symbol	Poromotor	Test Conditions	Value			Unit
Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
t _{PLH}	Propagation Delay Time, Low to High Level Output	$R_L = 3 \text{ to } 7 \text{ K}\Omega C_L = 15 \text{ pF}$ (See Figure 3, 4)		315	500	ns
t _{PHL}	Propagation Delay Time, High to Low Level Output	$R_L = 3 \text{ to } 7 \text{ K}\Omega C_L = 15 \text{ pF}$ (See Figure 3, 4)		75	175	ns
t _{TLH}	Transition Time Low to High Level Output	$R_L = 3 \text{ to } 7 \text{ K}\Omega C_L = 15 \text{ pF}$ (See Figure 3, 4)		60	100	ns
		$R_L = 3 \text{ to } 7 \text{ K}\Omega C_L = 2500 \text{ pF}$ (Note 6, See Figure 3, 4)		1.7	2.5	μs
t _{THL}	Transition Time High to Low Level Output	$R_L = 3 \text{ to } 7 \text{ K}\Omega C_L = 15 \text{ pF}$ (See Figure 3, 4)		40	7.5	ns
		$R_L = 3 \text{ to } 7 \text{ K}\Omega$ $C_L = 2500 \text{ pF}$ (Note 6, See Figure 3, 4)		1.5	2.5	μs

DRIVER SWITCHING CHARACTERISTICS (V_{DD} = 12V, V_{SS} = -12V, V_{CC} = 5V, T_A = 25 °C)

NOTE 6: Measured between -3V and 3V points of output waveform (EIA-232-E conditions), all unused inputs are tied.

RECEIVER ELECTRICAL CHARACTERISTICS OVER OPERATING CONDITIONS

Symbol	Parameter	Test Ca	onditions		Value		Unit
Symbol	Parameter	Test Co	onations	Min.	Тур.	Max.	Unit
V _{T+}	Positive Going Threshold Voltage	(See Figure 6)			2.2	2.4	V
V _{T-}	Negative Going Threshold Voltage	$T_A = 25 \degree C$ (Se	e Figure 6)	0.75	0.97		V
V _{hys}	Input Hysteresis (V _T + - V _T -)			0.5			V
V _{OH}	High Level Output Voltage	I _{OH} = -0.5mA	V _{IH} = 0.75 V	2.6	4	5	V
			Inputs Open	2.6			
V _{OL}	Low Level Output Voltage	V _I = 3 V I _{OL} =	= 10 mA		0.2	0.45	V
IIH	High Level Input Current	V _I = 25 V (See	e Figure 6)	3.6		8.3	mA
		V _I = 3 V (See	e Figure 6)	0.43			
۱ _{IL}	Low Level Input Current	V _I = -25 V (See	e Figure 6)	-3.6		-8.3	mA
		V _I = -3 V (See	e Figure 6)	-0.43			
I _{OS}	Short-Circuit Output Current	$V_{I} = 0 V V_{O} =$ (See Figure 5)	= 0 V		-3.4	-12	mA

All typical values are at TA = 25° C, VCC = 5V, VDD = 9V and VSS=-9V

RECEIVER SWITCHING CHARACTERISTICS ($V_{DD} = 12V$, $V_{SS} = -12V$, $V_{CC} = 5V$, $T_A = 25^{\circ}C$)

Symbol	Parameter	Test Conditions	Value			Unit
Symbol	Falaneter	Test conditions	Min.	Тур.	Max.	Onit
t _{PLH}	Propagation Delay Time Low to High Level Output	$R_L = 5 K\Omega$ $C_L = 50 pF$ (See Figure 6)		400	1000	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	$R_L = 5 K\Omega$ $C_L = 50 pF$ (See Figure 6)		70	150	ns
t _{TLH}	Transition Time Low to High Level Output	$R_L = 5 K\Omega$ $C_L = 50 pF$ (See Figure 6)		200	525	ns
t _{THL}	Transition Time High to Low Level Output	$R_L = 5 K\Omega$ $C_L = 50 pF$ (See Figure 6)		20	60	ns

APPLICATION CIRCUITS

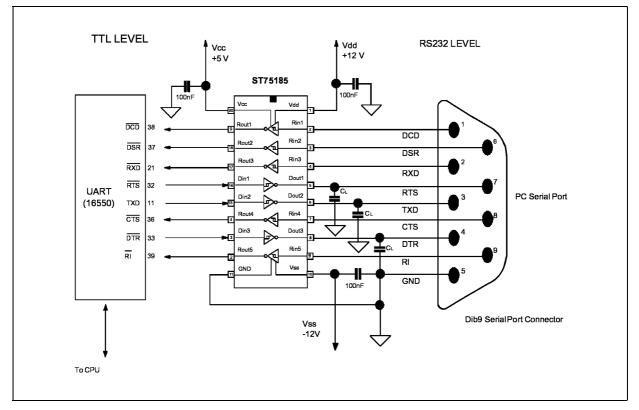


Figure 1 : Driver Test Circuit for $\mathsf{V}_{OH^{\text{,}}}\,\mathsf{I}_{SO(H)}$ and $\mathsf{I}_{SO(L)}$

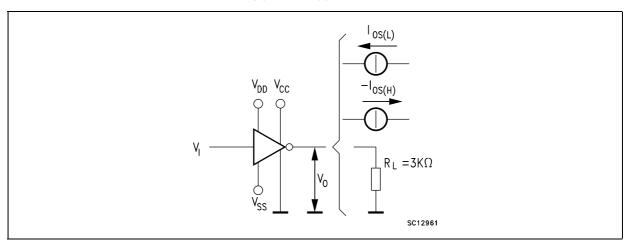
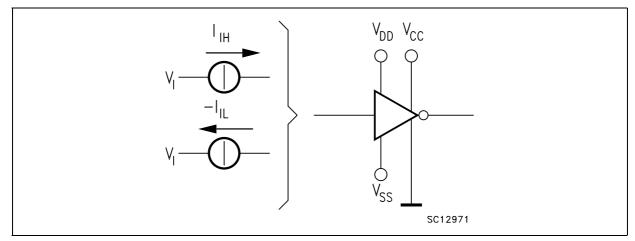


Figure 2 : Driver Test Circuit for I_{IH} and I_{IL}





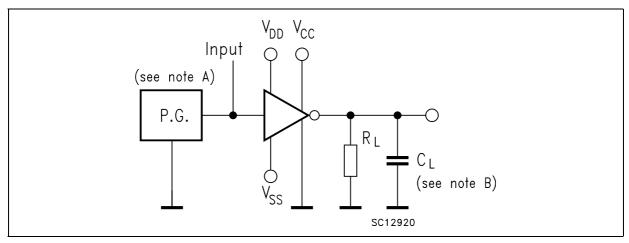
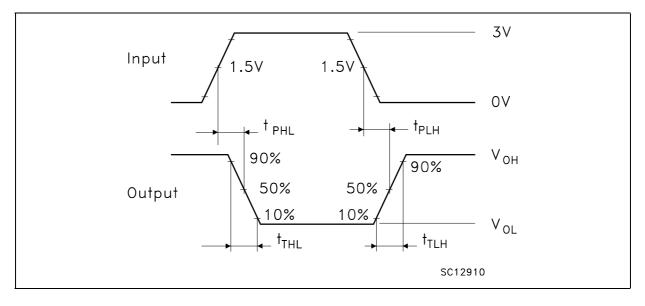


Figure 4 : Driver Voltage Waveforms





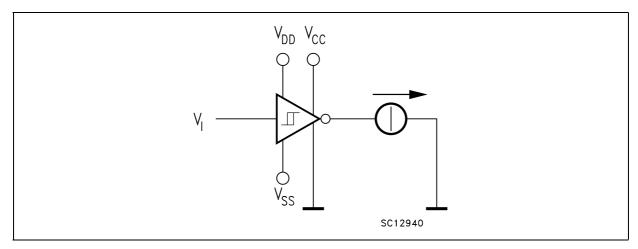


Figure 6 : Receiver Test Circuit for V_{T}, V_{OH}, V_{OL}

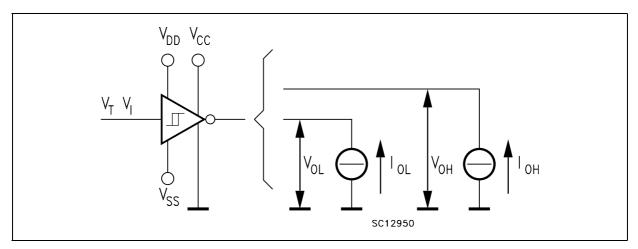


Figure 7 : Receiver Test Circuit

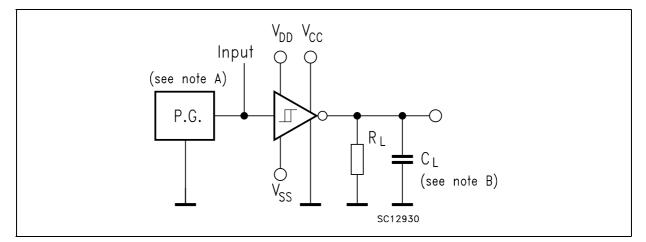
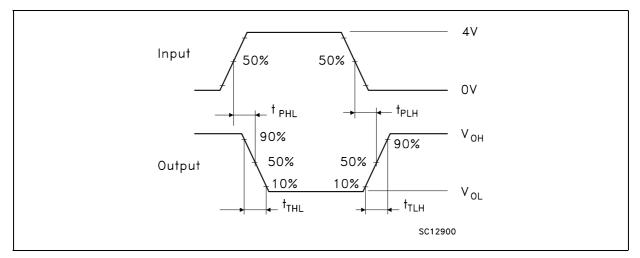


Figure 8 : Receiver Voltage Waveforms



NOTE A: The pulse generator has the following characteristics: $t_W = 25\mu s$, PRR = 20KHz, $Z_O = 50 \Omega$, $t_r = t_f < 50ns$ NOTE B: C_L includes probe and jig capacitance.



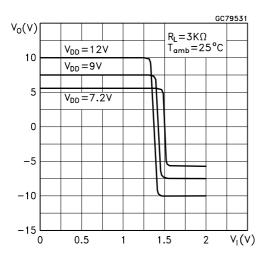


Figure 10 : Driver Short Circuit Output Current vs Free-Air Temperature

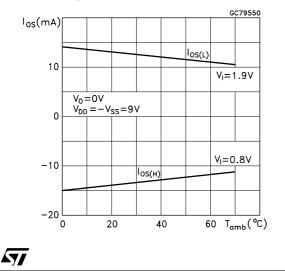


Figure 11 : Device Supply Current vs Temperature

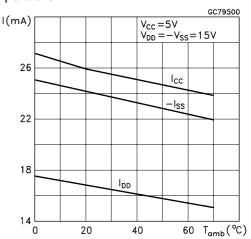


Figure 12 : Driver Output Current vs Output Voltage

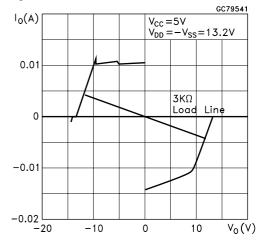


Figure 13 : Driver Output Slew Rate vs Load Capacitance

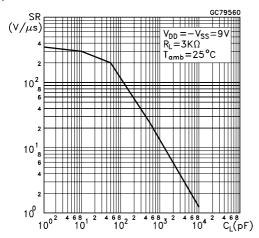
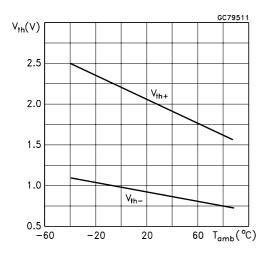
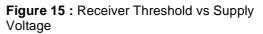
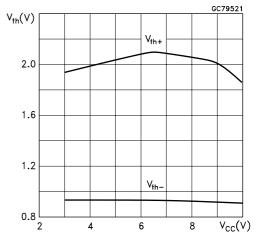


Figure 14 : Receiver Threshold vs Temperature



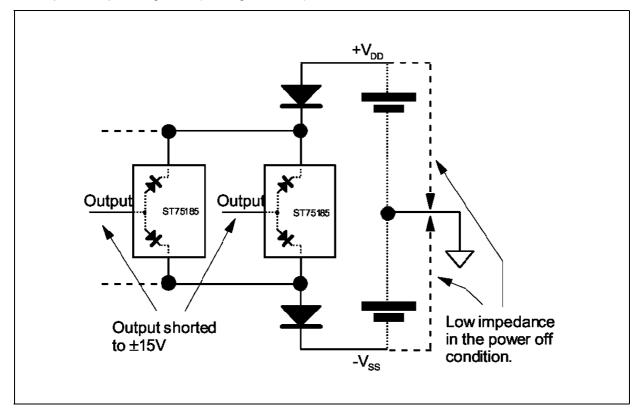




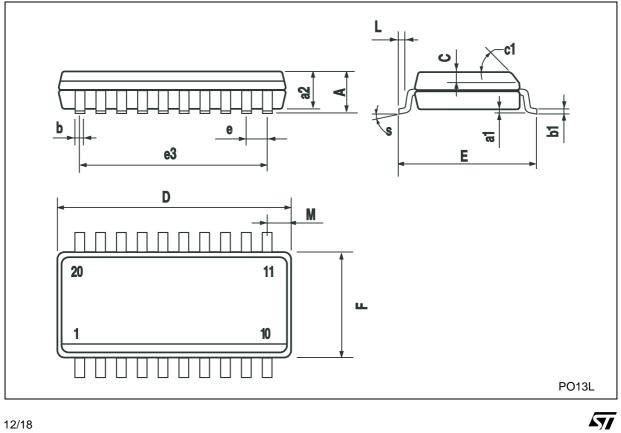
APPLICATION INFORMATION: DIODES ON POWER SUPPLY

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Diodes placed in series with the VDD and VSS leads protect the ST75185 in the fault condition in which the devices output are shorted to $\pm 15V$ and the power supplies are at low state and provide low-impedance path to ground (see Figure below).



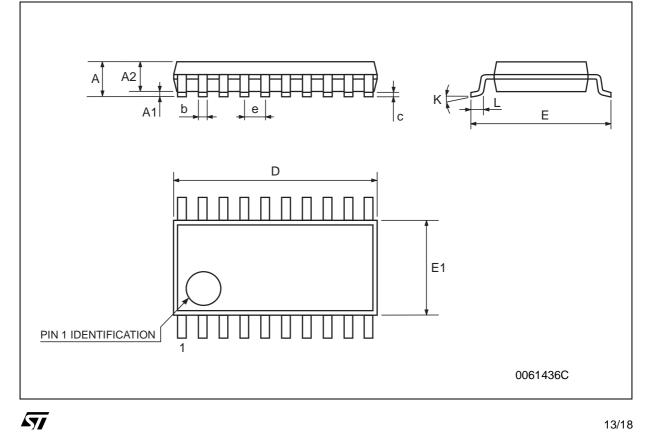
	SO-20 MECHANICAL DATA									
		mm.			inch					
DIM.	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.				
А			2.65			0.104				
a1	0.1		0.2	0.004		0.008				
a2			2.45			0.096				
b	0.35		0.49	0.014		0.019				
b1	0.23		0.32	0.009		0.012				
С		0.5			0.020					
c1			45°	(typ.)		•				
D	12.60		13.00	0.496		0.512				
Е	10.00		10.65	0.393		0.419				
е		1.27			0.050					
e3		11.43			0.450					
F	7.40		7.60	0.291		0.300				
L	0.50		1.27	0.020		0.050				
М			0.75			0.029				
S		1	8° (r	max.)	L	1				



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DIM.		mm.			inch	
	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.
А			2			0.079
A1	0.05			0.002		
A2	1.65	1.75	1.85	0.065	0.069	0.073
b	0.22		0.38	0.009		0.015
С	0.09		0.25	0.004		0.010
D	6.9	7.2	7.5	0.272	0.283	0.295
Е	7.4	7.8	8.2	0.291	0.307	0.323
E1	5	5.3	5.6	0.197	0.209	0.220
е		0.65 BSC			0.0256 BSC	
K	0°	4°	8°	0°	4°	8°

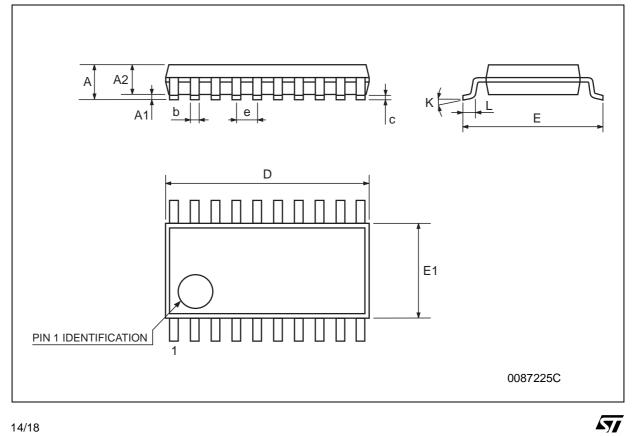




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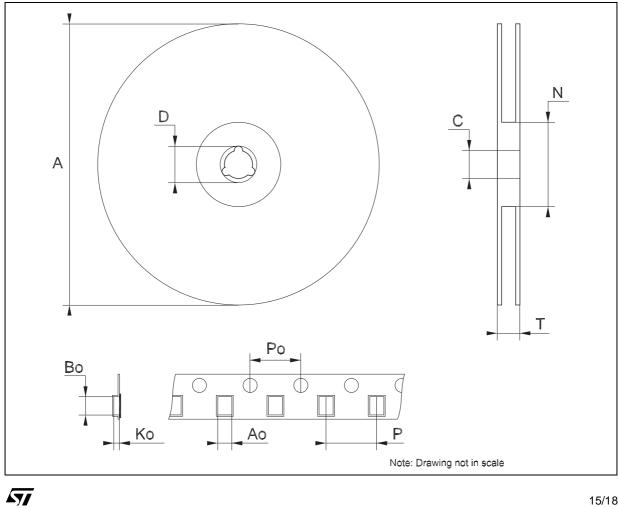
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TSSOP20 MECHANICAL DATA								
DIM.		mm.			inch			
	MIN.	ТҮР	MAX.	MIN.	/IN. TYP.	MAX.		
А			1.2			0.047		
A1	0.05		0.15	0.002	0.004	0.006		
A2	0.8	1	1.05	0.031	0.039	0.041		
b	0.19		0.30	0.007		0.012		
С	0.09		0.20	0.004		0.0079		
D	6.4	6.5	6.6	0.252	0.256	0.260		
Е	6.2	6.4	6.6	0.244	0.252	0.260		
E1	4.3	4.4	4.48	0.169	0.173	0.176		
е		0.65 BSC			0.0256 BSC			
К	0°		8°	0°		8°		
L	0.45	0.60	0.75	0.018	0.024	0.030		



-	mm.			inch		
DIM.	MIN.	ТҮР	TYP MAX. MIN. TY	TYP.	MAX.	
А			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
Ν	60			2.362		
Т			30.4			1.197
Ao	10.8		11	0.425		0.433
Во	13.2		13.4	0.520		0.528
Ko	3.1		3.3	0.122		0.130
Po	3.9		4.1	0.153		0.161
Р	11.9		12.1	0.468		0.476

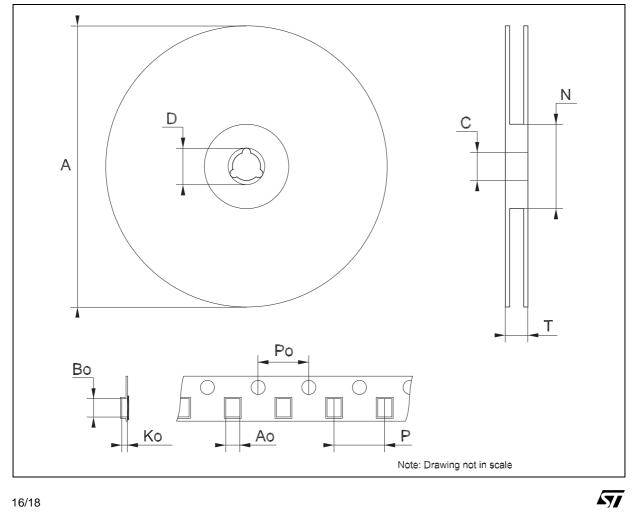




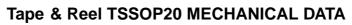
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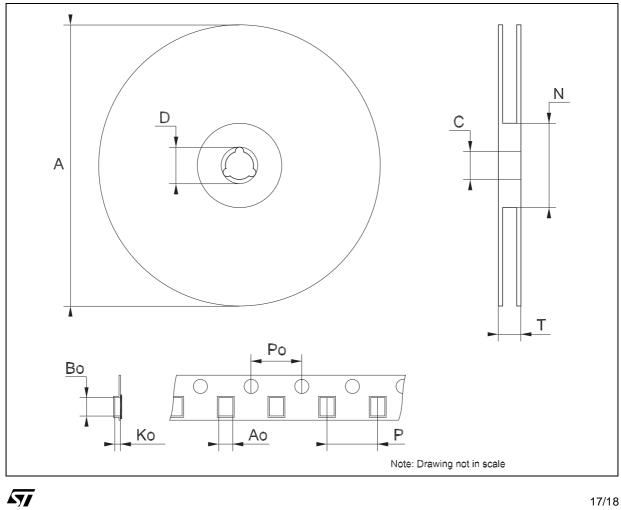
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Tape & Reel SSOP20 MECHANICAL DATA							
DIM	mm.			inch			
DIM.	MIN.	ТҮР	MAX.	MIN.	TYP.	MAX.	
А			330			12.992	
С	12.8		13.2	0.504		0.519	
D	20.2			0.795			
Ν	60			2.362			
Т			22.4			0.882	
Ao	8.4		8.6	0.331		0.339	
Во	7.7		7.9	0.303		0.311	
Ko	2.9		3.1	0.114		0.122	
Po	3.9		4.1	0.153		0.161	
Р	11.9		12.1	0.468		0.476	



DIM.	mm.			inch		
DIM.	MIN.	TYP	MAX.	MAX. MIN. TYP.	TYP.	MAX.
А			330			12.992
С	12.8		13.2	0.504		0.519
D	20.2			0.795		
Ν	60			2.362		
Т			22.4			0.882
Ao	6.8		7	0.268		0.276
Во	6.9		7.1	0.272		0.280
Ko	1.7		1.9	0.067		0.075
Po	3.9		4.1	0.153		0.161
Р	11.9		12.1	0.468		0.476





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