UTC UNISONIC TECHNOLOGIES CO., LTD

STD888

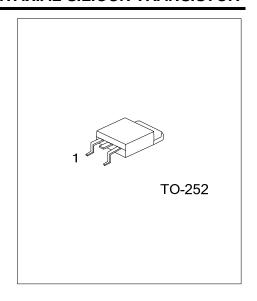
PNP EPITAXIAL SILICON TRANSISTOR

HIGH CURRENT, HIGH PERFORMANCE, LOW VOLTAGE PNP **TRANSISTOR**

DESCRIPTION

The UTC STD888 is a high current, high performance, low voltage PNP transistor; it uses UTC's advanced technology to provide customers high DC current gain and very low saturation

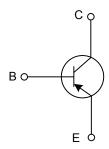
The UTC STD888 is suitable for switching regulator in battery charger applications, heavy load driver and voltage regulation in bias supply circuits, etc.



FEATURES

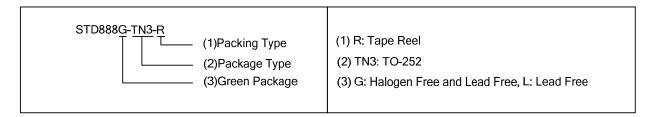
- * Very low collector to emitter saturation voltage
- * High DC current gain

EQUIVALENT CIRCUIT



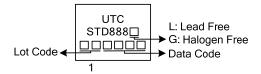
ORDERING INFORMATION

Ordering Number		Dookogo	Pin Assignment			Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing	
STD888L-TN3-R	STD888G-TN3-R	TO-252	В	С	Е	Tape Reel	



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■ MARKING



■ ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	RATINGS	UNIT
Collector-Base Voltage (I _E =0)	V_{CBO}	-60	V
Collector-Emitter Voltage (I _B =0)	V_{CEO}	-30	V
Emitter-Base Voltage (I _C =0)	V_{EBO}	-6	V
Collector Current	Ic	-5	Α
Collector Peak Current (tp<5ms)	I _{CM}	-10	Α
Total Dissipation at T _C =25°C	P_D	15	W
Junction Temperature	TJ	150	°C
Storage Temperature	T _{STG}	-65 ~ + 150	°C

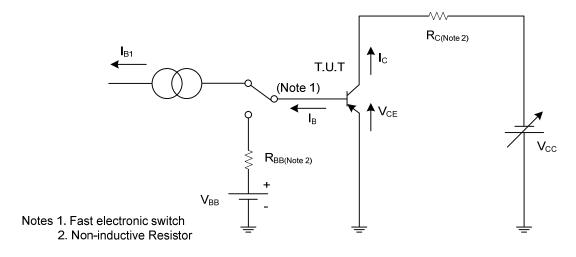
Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ **ELECTRICAL CHARACTERISTICS** (T_C=25°C unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Collector-Base Breakdown Voltage	BV_CBO	I _E =0, I _C =-100μA	-60			V
Collector-Emitter Breakdown Voltage (Note)	BV _{CEO}	I _B =0, I _C =-10mA	-30			V
Emitter-Base Breakdown Voltage	BV_{EBO}	I _C =0, I _E =-100μA	-6			V
Collector Cut-off Current	I _{CBO}	V_{CB} =-30V, I_{E} =0			-10	nΑ
Emitter Cut-off Current	I _{EBO}	I _C =0, V _{EB} =-6V			-10	nA
	V _{CE(sat)}	I _C =-500mA, I _B =-5mA			-0.15	V
		I _C =-2A, I _B =-50mA			-0.25	V
Collector Emitter Seturation Voltage		I _C =-5A, I _B =-250mA			-0.70	V
Collector-Emitter Saturation Voltage		I _C =-6A, I _B =-250mA			-0.70	V
		I _C =-8A, I _B =-400mA			-1	V
		I _C =-10A, I _B =-500mA			-1.5	V
Dana Emittar Catamatian Valtage (Nata)	V	I _C =-2A, I _B =-50mA			-1.1	V
Base-Emitter Saturation Voltage (Note)	$V_{BE(sat)}$	I _C =-6A, I _B =-250mA			-1.4	V
	h _{FE}	I _C =-10mA, V _{CE} =-1V	150	200		
		I _C =-500mA, V _{CE} =-1V	150	200	300	
DC Current Gain (Note)		I_C =-5A, V_{CE} =-1V	75	100		
		I_C =-8A, V_{CE} =-1V	40	55		
		I _C =-10A, V _{CE} =-1V	15	35		
Delay Time	t _D			180	220	ns
Rise Time	t _R	I _C =-3A, I _{B1} =-I _{B2} =-60mA		160	210	ns
Storage Time	t _S	V _{CC} =-20V		250	300	ns
Fall Time	t _F			80	100	ns

Note: Pulsed: Pulse duration=300µs, duty cycle≤1.5%.

■ RESISTIVE LOAD SWITCHING TEST CIRCUIT



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