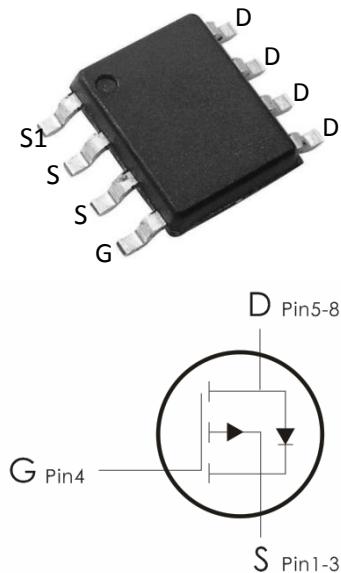


## Description:

This P-Channel MOSFET uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

## Features:

- 1)  $V_{DS}=-30V, I_D=-8A, R_{DS(ON)}<35m\Omega @ V_{GS}=-10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra  $R_{DS(ON)}$ .
- 5) Excellent package for good heat dissipation.



## Absolute Maximum Ratings: ( $T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
$V_{DS}$	Drain-Source Voltage	-30	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current- $T_C=25^\circ C$	-8	A
	Pulsed Drain Current <sup>1</sup>	-30	
$P_D$	Power Dissipation	3.1	W
$T_J, T_{STG}$	Operating and Storage Junction Temperature Range	-55 to +150	$^\circ C$

## Thermal Characteristics:

Symbol	Parameter	Max	Units
$R_{Theta A}$	Thermal Resistance,Junction to Ambient <sup>2</sup>	40	$^\circ C / W$

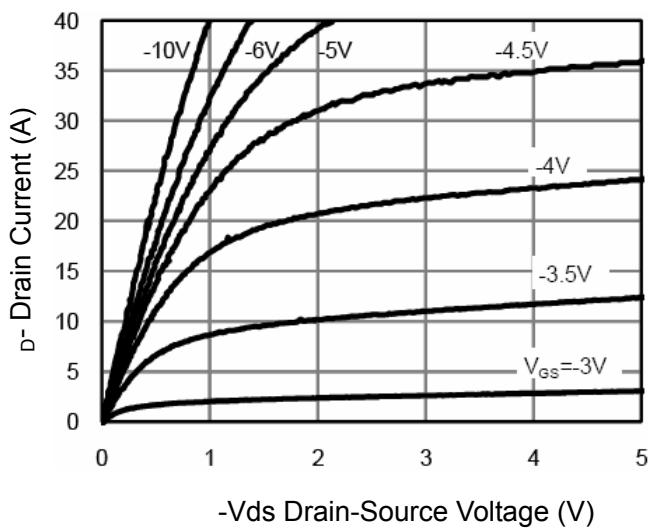
**Electrical Characteristics:** ( $T_C=25^\circ\text{C}$  unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
<b>Off Characteristics</b>						
<b><math>\text{BV}_{\text{DSS}}</math></b>	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_D=250 \mu\text{A}$	-30	-33	---	V
<b><math>I_{\text{DSS}}</math></b>	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=-30\text{V}, T_J=25^\circ\text{C}$	---	---	-1	$\mu\text{A}$
<b><math>I_{\text{GSS}}</math></b>	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	$\pm 100$	nA
<b>On Characteristics<sup>3</sup></b>						
<b><math>V_{\text{GS}(\text{th})}</math></b>	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_D=250 \mu\text{A}$	-1.3	-1.65	-2.5	V
<b><math>R_{\text{DS}(\text{ON})}</math></b>	Drain-Source On Resistance <sup>2</sup>	$V_{\text{GS}}=-10\text{V}, I_D=-6.5\text{A}$	---	30	35	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_D=-5\text{A}$	---	53	65	
<b><math>G_{\text{FS}}</math></b>	Forward Transconductance	$V_{\text{DS}}=-5\text{V}, I_D=-6.5\text{A}$	14	---	---	S
<b>Dynamic Characteristics<sup>4</sup></b>						
<b><math>C_{\text{iss}}</math></b>	Input Capacitance	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	660	---	$\text{pF}$
<b><math>C_{\text{oss}}</math></b>	Output Capacitance		---	100	---	
<b><math>C_{\text{rss}}</math></b>	Reverse Transfer Capacitance		---	65	---	
<b>Switching Characteristics<sup>4</sup></b>						
<b><math>t_{\text{d}(\text{on})}</math></b>	Turn-On Delay Time	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=-10\text{V}$ $I_D=-4\text{A}, R_{\text{GEN}}=3\Omega$	---	7.5	---	ns
<b><math>t_r</math></b>	Rise Time		---	5.5	---	ns
<b><math>t_{\text{d}(\text{off})}</math></b>	Turn-Off Delay Time		---	19	---	ns
<b><math>t_f</math></b>	Fall Time		---	7	---	ns
<b><math>Q_g</math></b>	Total Gate Charge	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=-10\text{V}, I_D=-6.5\text{A}$	---	9.2	---	nC
<b><math>Q_{\text{gs}}</math></b>	Gate-Source Charge		---	1.6	---	nC
<b><math>Q_{\text{gd}}</math></b>	Gate-Drain "Miller" Charge		---	2.2	---	nC
<b>Drain-Source Diode Characteristics</b>						
<b><math>V_{\text{SD}}</math></b>	Source-Drain Diode Forward Voltage <sup>3</sup>	$V_{\text{GS}}=0\text{V}, I_S=-8\text{A}, T_J=25^\circ\text{C}$	---	---	-1.2	V
<b><math>I_S</math></b>	Diode Forward Current <sup>2</sup>	---	---	-8	A	

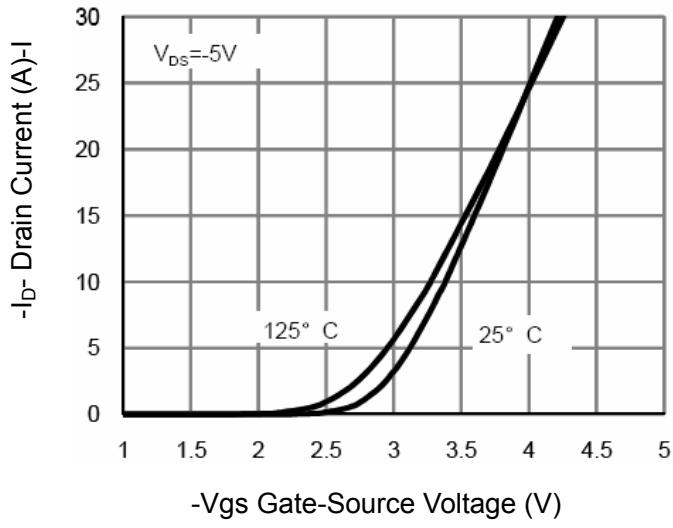
**Notes:**

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board,  $t \leq 10$  sec.
3. Pulse Test: Pulse Width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 2\%$ .
4. Guaranteed by design, not subject to production

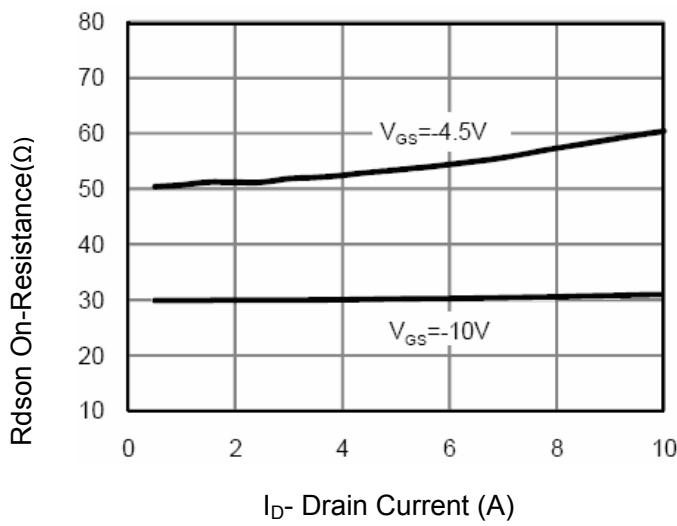
**Typical Characteristics:** ( $T_c=25^\circ\text{C}$  unless otherwise noted)



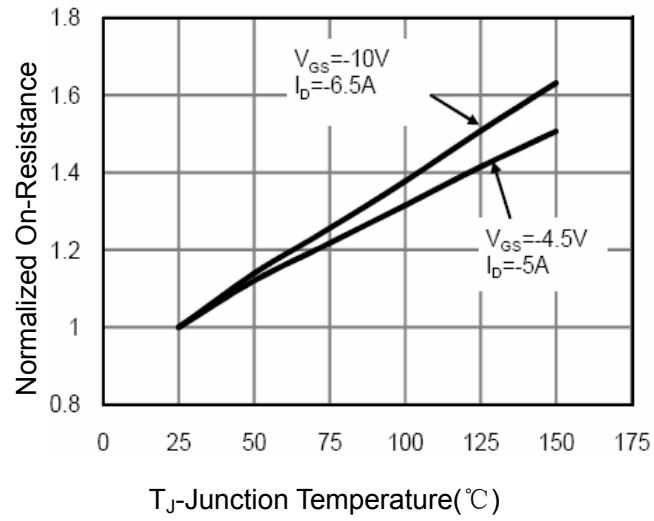
**Figure 1 Output Characteristics**



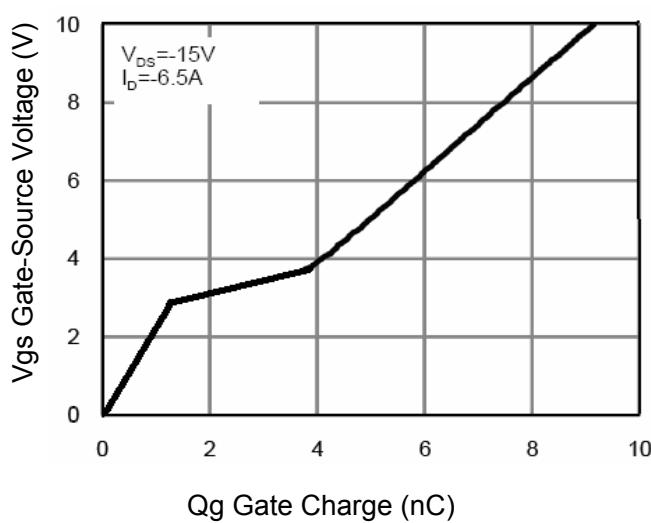
**Figure 2 Transfer Characteristics**



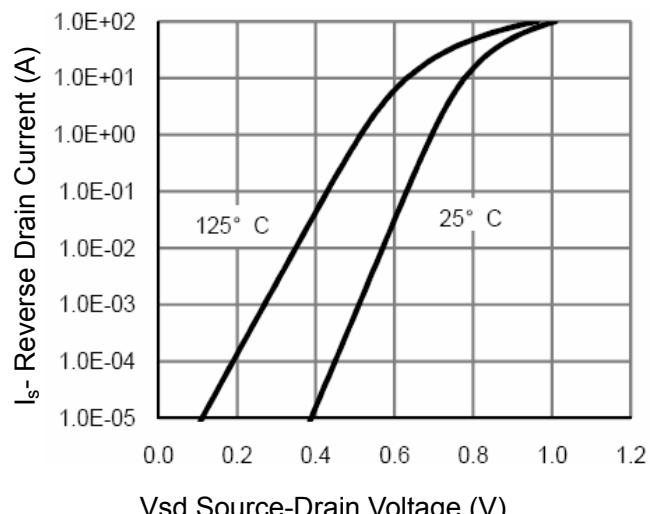
**Figure 3 Rdson- Drain Current**



**Figure 4 Rdson-Junction Temperature**

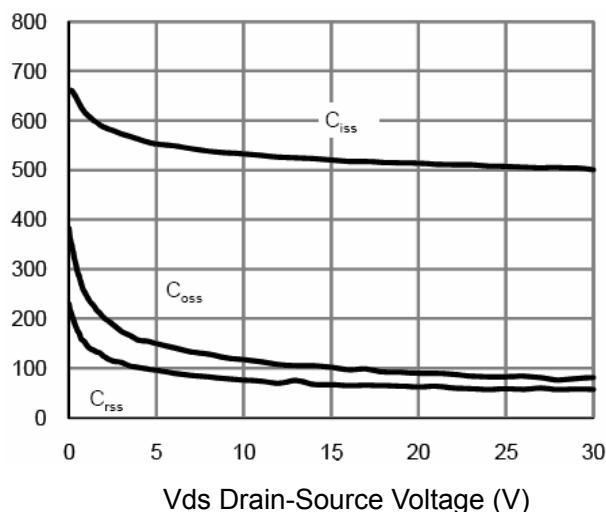


**Figure 5 Gate Charge**

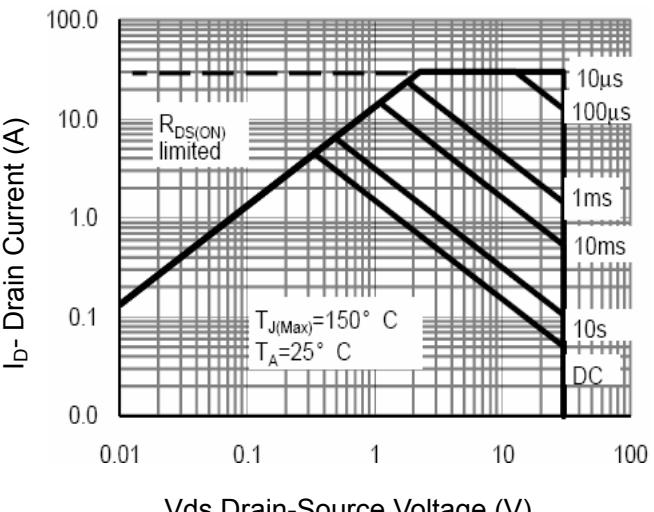


**Figure 6 Source- Drain Diode Forward**

C Capacitance (pF)

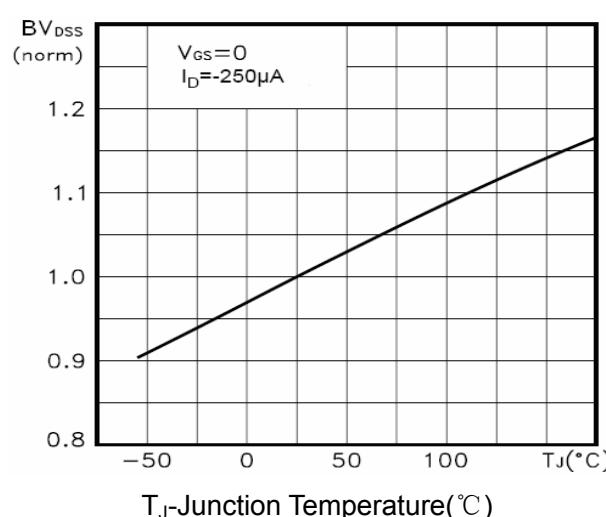


**Figure 7 Capacitance vs Vds**



**Figure 8 Safe Operation Area**

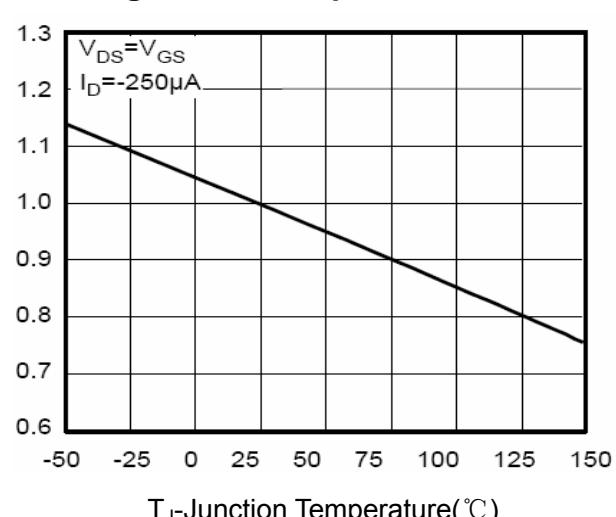
Normalized BV<sub>DSS</sub>



T<sub>J</sub>-Junction Temperature(°C)

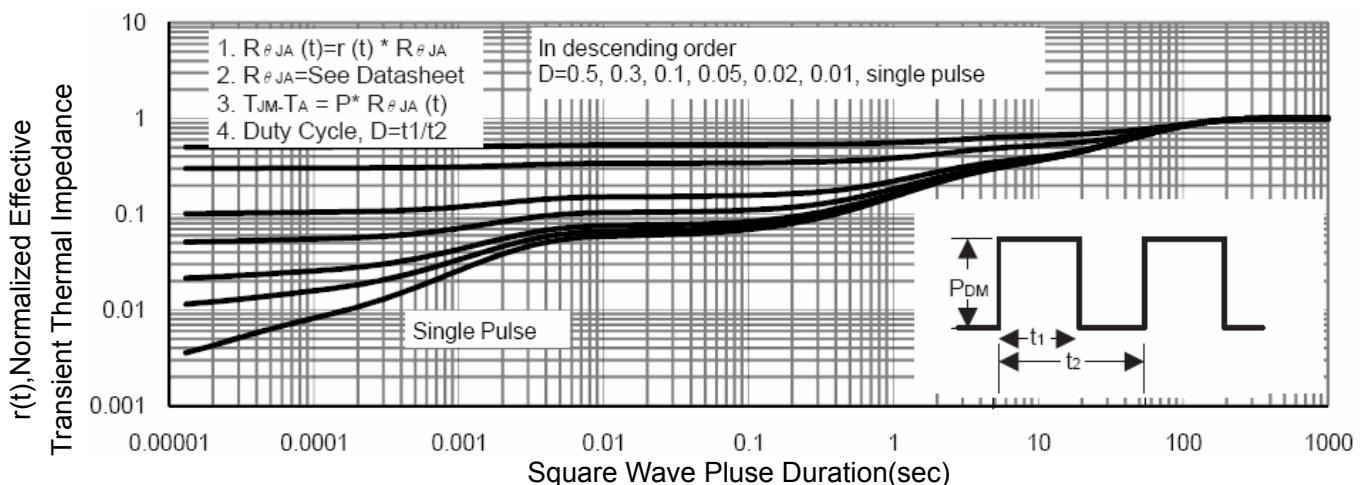
**Figure 9 BV<sub>DSS</sub> vs Junction Temperature**

V<sub>th</sub> (V) Variance



T<sub>J</sub>-Junction Temperature(°C)

**Figure 10 V<sub>GS(th)</sub> vs Junction Temperature**



**Figure 11 Normalized Maximum Transient Thermal Impedance**



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