## STPS2L40

## Low drop power Schottky rectifier

### Main product characteristics

I <sub>F(AV)</sub>	2 A
V <sub>RRM</sub>	40 V
T <sub>j</sub> (max)	150° C
V <sub>F</sub> (max)	0.34 V

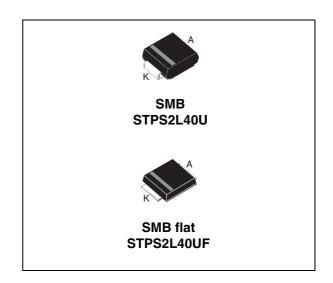
#### **Features and Benefits**

- Very small conduction losses
- Negligible switching losses
- Low forward voltage drop
- Surface mount miniature package
- Avalanche capability specified

### **Description**

Single chip Schottky rectifiers suited to Switched Mode Power Supplies and high frequency DC to DC converters.

Packaged in SMB, and low profile SMB, this device is especially intended for surface mounting and used in low voltage, high frequency inverters, free wheeling and polarity protection applications.



#### **Order codes**

Part number	marking
STPS2L40U	GD4
STPS2L40UF	FGD4

Table 1. Absolute Ratings (limiting values)

Symbol		Value	Unit		
V <sub>RRM</sub>	Repetitive peak reverse v	Repetitive peak reverse voltage			
1	Average forward current	SMB	T <sub>L</sub> = 130° C δ = 0.5	2	Α
I <sub>F(AV)</sub>	Average forward current	SMB flat	T <sub>L</sub> = 140° C δ = 0.5		A
I <sub>FSM</sub>	Surge non repetitive forward current		t <sub>p</sub> = 10 ms sinusoidal	75	Α
P <sub>ARM</sub>	Repetitive peak avalanche power $t_p = 1 \mu s$ $T_j = 2$		t <sub>p</sub> = 1 μs T <sub>j</sub> = 25° C	2200	W
T <sub>stg</sub>	Storage temperature rang	-65 to + 150	° C		
Tj	Operating junction temperature (1)			150	°C

<sup>1.</sup>  $\frac{dPtot}{dT_i} < \frac{1}{Rth(i-a)}$  condition to avoid thermal runaway for a diode on its own heatsink

STPS2L40 **Characteristics** 

#### **Characteristics** 1

Table 2. Thermal resistances

Symbol	Parameter	Value	Unit	
Ь	lunction to load	SMB	20	° C/W
R <sub>th (j-l)</sub> Junction to lead		SMB flat	10	C/VV

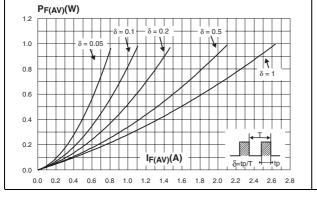
Table 3. Static electrical characteristics

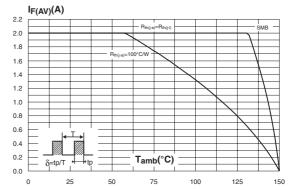
Symb ol	Tests Conditions	Tests Conditions		Min.	Тур.	Max.	Unit
	I <sub>R</sub> <sup>(1)</sup> Reverse leakage current	T <sub>j</sub> = 25° C				220	μΑ
I <sub>R</sub> <sup>(1)</sup>		T <sub>j</sub> = 100° C	V <sub>R</sub> = 40 V			20	mA
		T <sub>j</sub> = 125° C			38	80	mA
		T <sub>j</sub> = 25° C	I <sub>F</sub> = 1 A			0.39	
		T <sub>j</sub> = 125° C			0.25	0.28	V
V <sub>E</sub> <sup>(1)</sup>	Forward voltage drop	T <sub>j</sub> = 25° C	1 0 4			0.43	V
V <sub>F</sub> \' / Forward voltage drop	T <sub>j</sub> = 125° C	I <sub>F</sub> = 2 A		0.31	0.34		
		T <sub>j</sub> = 25° C	1 – 4 Δ			0.5	V
		T <sub>j</sub> = 125° C	l <sub>F</sub> = 4 A		0.39	0.45	V

<sup>1.</sup> Pulse test:  $t_p = 380 \mu s$ ,  $\delta < 2$ 

To evaluate the conduction losses use the following equation: P = 0.22 x  $I_{F(AV)}$  + 0.06  $I_{F}^{2}_{(RMS)}$ 

Figure 1. Average forward power dissipation Figure 2. Average forward current versus versus average forward current ambient temperature ( $\delta$  = 0.5) SMB

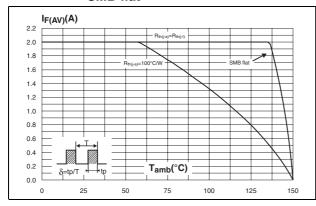




STPS2L40 Characteristics

Figure 3. Average forward current versus ambient temperature ( $\delta$  = 0.5) SMB flat

Figure 4. Non repetitive surge peak forward current versus overload duration (maximum values) SMB



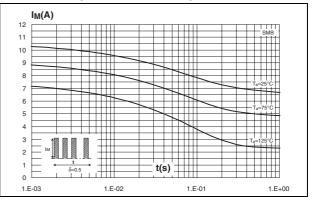
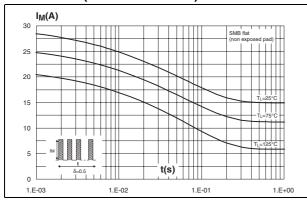


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values) SMB flat

Figure 6. Normalized avalanche power derating versus pulse duration



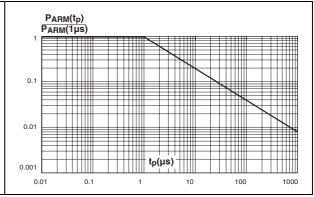
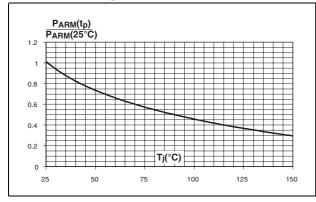
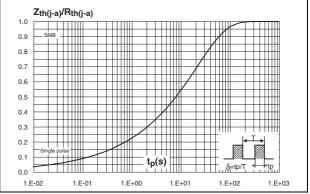


Figure 7. Normalized avalanche power derating versus junction temperature

Figure 8. Relative variation of thermal impedance junction to ambient versus pulse duration - SMB

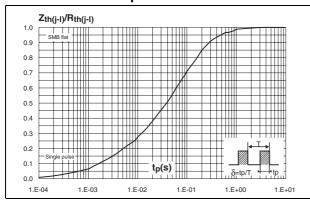




Characteristics STPS2L40

Figure 9. Relative variation of thermal impedance junction to lead versus pulse duration - SMB flat

Figure 10. Reverse leakage current versus reverse voltage applied (typical values)



1.E+01

1.E+01

1.E-01

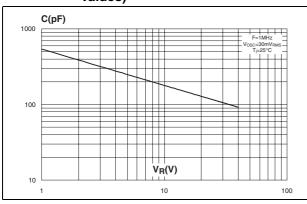
1.E-02

1.E-03

0 5 10 15 20 25 30 35 40

Figure 11. Junction capacitance versus reverse voltage applied (typical values)

Figure 12. Forward voltage drop versus forward current (high level)



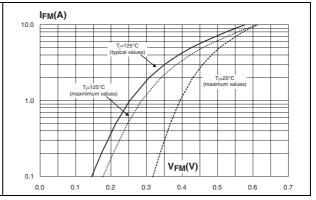
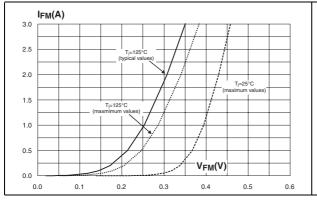
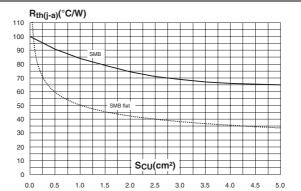


Figure 13. Forward voltage drop versus forward current (low level)

Figure 14. Thermal resistance junction to ambient versus copper surface under each lead (epoxy printed board FR4, e<sub>CU</sub>=35µm)





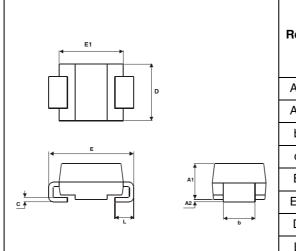
4/8

STPS2L40 Package Information

# 2 Package Information

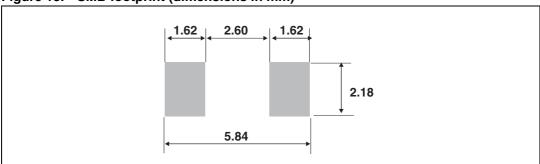
Epoxy meets UL94,V0

Table 4. SMB dimensions



	Dimensions			
Ref.	Millim	Millimeters Inc		hes
	Min.	Max.	Min.	Max.
A1	1.90	2.45	0.075	0.096
A2	0.05	0.20	0.002	0.008
b	1.95	2.20	0.077	0.087
С	0.15	0.40	0.006	0.016
Е	5.10	5.60	0.201	0.220
E1	4.05	4.60	0.159	0.181
D	3.30	3.95	0.130	0.156
L	0.75	1.50	0.030	0.059

Figure 15. SMB footprint (dimensions in mm)



5/8

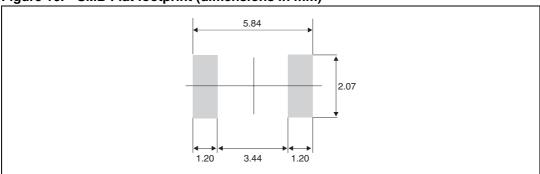
Package Information STPS2L40

Table 5. SMB Flat dimensions

				Dim	ensions	;	
	Ref.	Mi	illimet	ers		Inches	
A T		Min.	Тур.	Max.	Min.	Тур.	Max.
D A C -	Α	0.90		1.10	0.035		0.043
<u> </u>	b <sup>(1)</sup>	1.95		2.20	0.077		0.087
L\$ L2	c <sup>(1)</sup>	0.15		0.40	0.006		0.016
E E1	D	3.30		3.95	0.130		0.156
	Е	5.10		5.60	0.200		0.220
L1	E1	4.05		4.60	0.189		0.181
<del></del>	L	0.75		1.50	0.029		0.059
	L1		0.40			0.016	
	L2		0.60			0.024	

<sup>1.</sup> Applies to plated leads

Figure 16. SMB Flat footprint (dimensions in mm)



In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

6/8

# **3 Ordering Information**

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
STPS2L40U	GD4	SMB	0.107 g	2500	Tape and reel
STPS2L40UF	FGD4	SMB flat	0.50 g	5000	Tape and reel

## 4 Revision history

Date	Revision	Description of Changes
Jul-2003	2A	Last update.
31-Jan-2007	3	Reformatted to current standard. Added ECOPACK statement. Added SMB flat package.

**577** 

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577