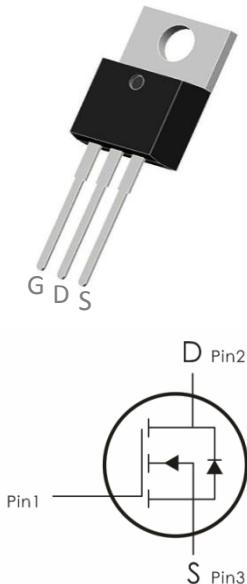


Description:

This N-Channel MOSFET uses advanced trench technology and design to provide excellent $R_{DS(on)}$ with low gate charge. It can be used in a wide variety of applications.

Features:

- 1) $V_{DS}=100V, I_D=140A, R_{DS(ON)}<5.5m\Omega @V_{GS}=10V$
- 2) Low gate charge.
- 3) Green device available.
- 4) Advanced high cell density trench technology for ultra $R_{DS(ON)}$.
- 5) Excellent package for good heat dissipation.



Absolute Maximum Ratings: ($T_c=25^\circ C$ unless otherwise noted)

Symbol	Parameter	Ratings	Units
V_{DS}	Drain-Source Voltage	100	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Continuous Drain Current	140	A
	Continuous Drain Current- $T_C=100^\circ C$	97	
	Pulsed Drain Current	550	
E_{AS}	Single Pulse Avalanche Energy ⁵	1200	mJ
P_D	Power Dissipation	330	W
T_J, T_{STG}	Operating and Storage Junction Temperature Range	-55 to +175	$^\circ C$

Thermal Characteristics:

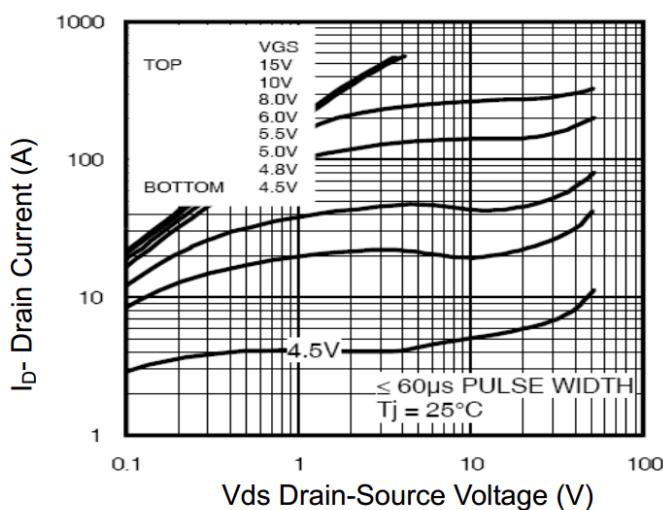
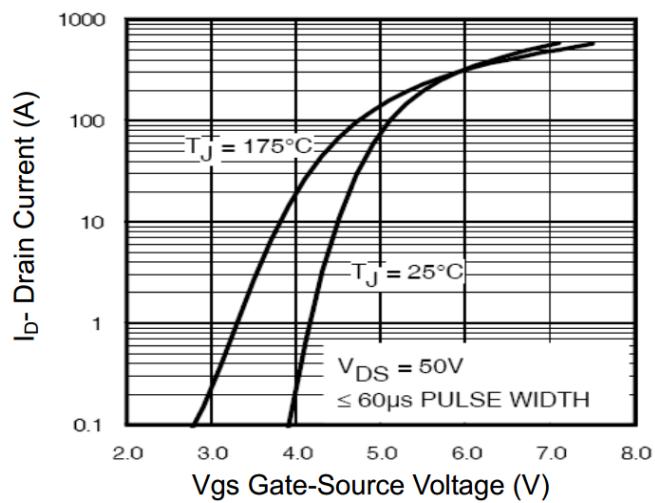
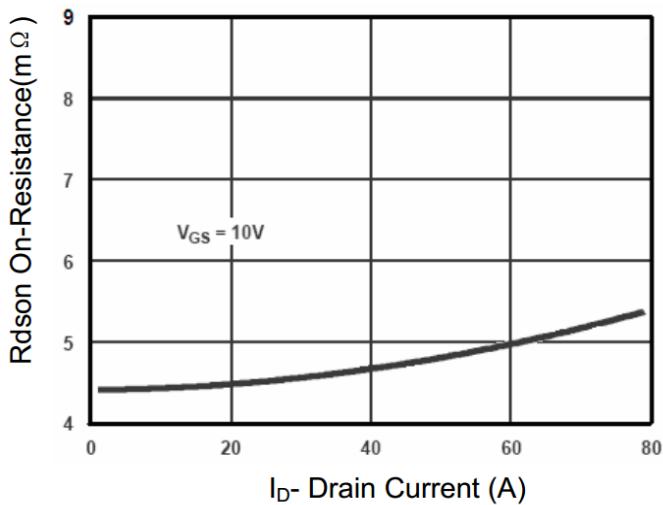
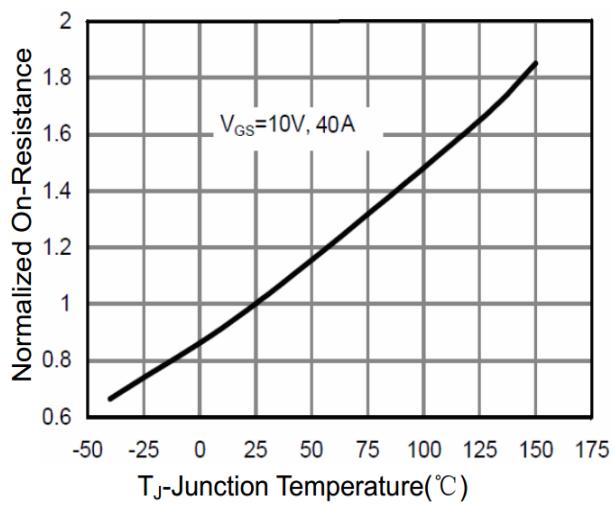
Symbol	Parameter	Max	Units
R_{eJC}	Thermal Resistance,Junction to Case ²	0.45	$^\circ C/W$

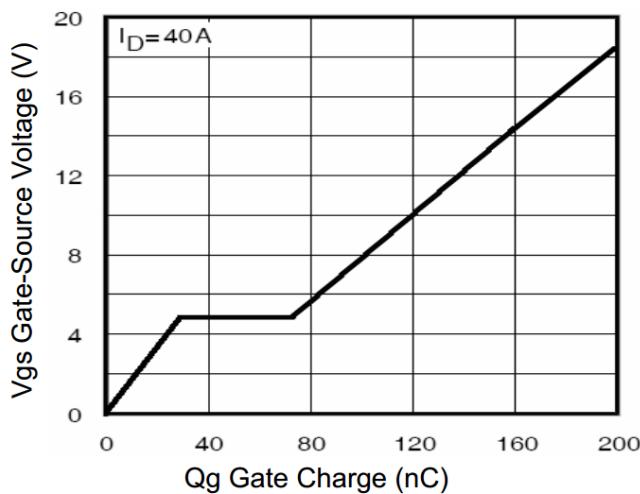
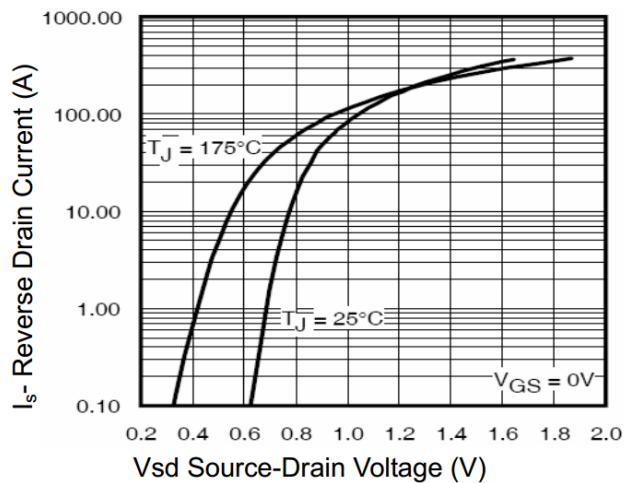
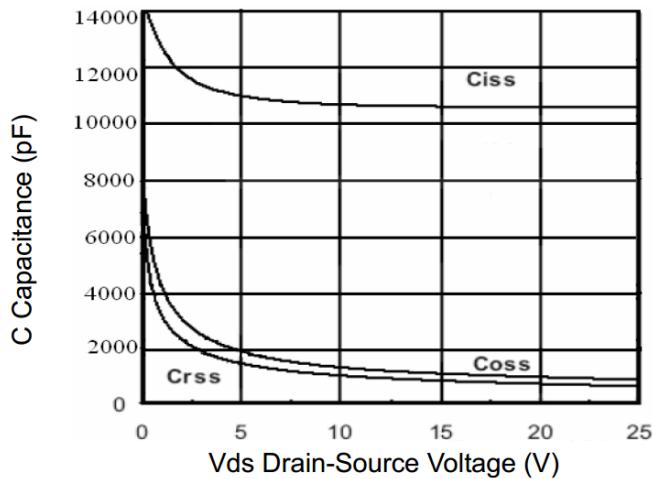
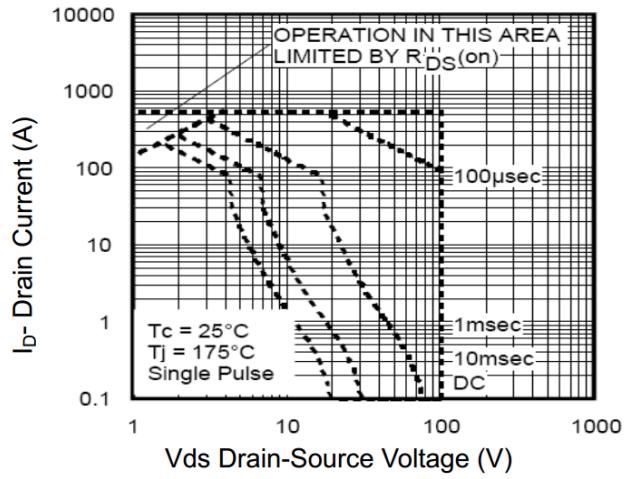
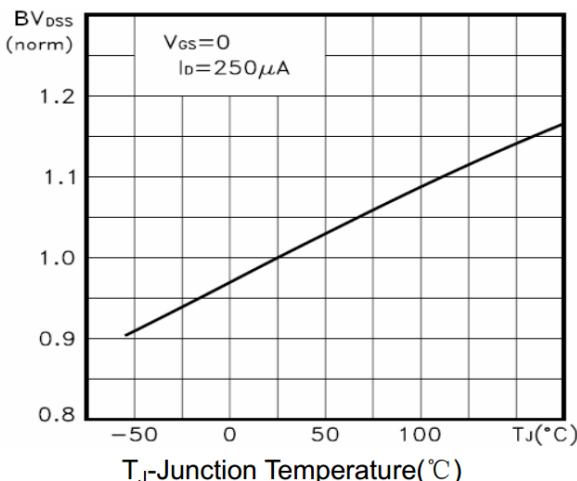
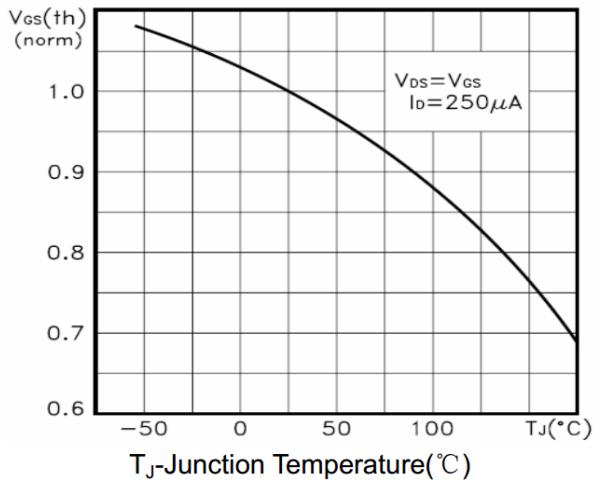
Electrical Characteristics: ($T_c=25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250 \mu\text{A}$	100	110	---	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}$	---	---	1	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{A}$	---	---	± 100	nA
On Characteristics³						
$V_{\text{GS}(\text{th})}$	GATE-Source Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250 \mu\text{A}$	2	3.2	4	V
$R_{\text{DS}(\text{ON})}$	Drain-Source On Resistance	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=40\text{A}$	---	4.6	5.5	$\text{m}\Omega$
G_{FS}	Forward Transconductance	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=40\text{A}$	170	---	---	S
Dynamic Characteristics⁴						
C_{iss}	Input Capacitance	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, f=1\text{MHz}$	---	10500	---	pF
C_{oss}	Output Capacitance		---	914	---	
C_{rss}	Reverse Transfer Capacitance		---	695	---	
Switching Characteristics⁴						
$t_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=65\text{V}, I_{\text{D}}=40\text{A}$ $V_{\text{GS}}=10\text{V}, R_{\text{GEN}}=2.5\Omega$	---	25	---	ns
t_r	Rise Time		---	100	---	ns
$t_{\text{d(off)}}$	Turn-Off Delay Time		---	65	---	ns
t_f	Fall Time		---	77	---	ns
Q_g	Total Gate Charge	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=44\text{V}, I_{\text{D}}=40\text{A}$	---	120	---	nC
Q_{gs}	Gate-Source Charge		---	30	---	nC
Q_{gd}	Gate-Drain "Miller" Charge		---	35	---	nC
Drain-Source Diode Characteristics						
V_{SD}	Source-Drain Diode Forward Voltage ³	$V_{\text{GS}}=0\text{V}, I_{\text{S}}=40\text{A}$	---	0.85	1.2	V

Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t \leq 10$ sec.
3. Pulse Test: Pulse Width $\leq 300 \mu s$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production
5. EAS condition: $T_J=25^\circ C$, $V_{DD}=50V$, $V_G=10V$, $L=1mH$, $R_g=25\Omega$.

Typical Characteristics: ($T_C=25^\circ C$ unless otherwise noted)

Figure 1 Output Characteristics

Figure 2 Transfer Characteristics

Figure 3 Rdson- Drain Current

Figure 4 Rdson-JunctionTemperature


Figure 5 Gate Charge

Figure 6 Source- Drain Diode Forward

Figure 7 Capacitance vs Vds

Figure 8 Safe Operation Area

Figure 9 BV_{DSS} vs Junction Temperature

Figure 10 $V_{GS(th)}$ vs Junction Temperature

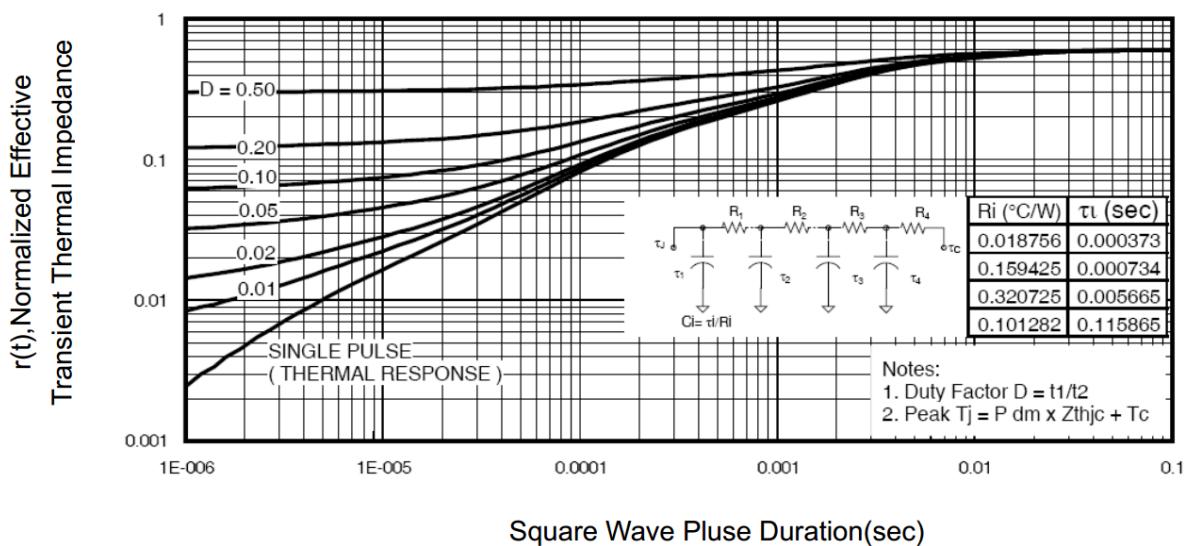


Figure 11 Normalized Maximum Transient Thermal Impedance



0086-0755-8278-9056

www.doingter.cn