

# SUR482H

#### Epitaxial planar NPN/PNP silicon transistor

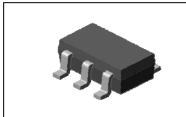
### **Description**

• Dual chip digital transistor

#### **Features**

- Both SRC1204 chip and SRA2219 chip in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process
- "Green" device and RoHS compliant device
- Available in full lead (Pb)-free device





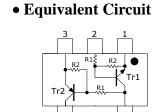
Package: SOT-353

## **Ordering Information**

Type NO.	Marking	Package Code	
SUR482H	X5□	SOT-353	

□ : Year & Week Code

### **Equivalent circuit & PIN Connections**



	$\mathbf{R}_{1}$	$\mathbf{R}_2$
Tr1	47ΚΩ	47ΚΩ
Tr2	4.7ΚΩ	10ΚΩ

#### **PIN Connections**

- 1. COMMON 1
- 2. IN 1
- 3. COMMON 2
- 4. OUT 2
- 5. OUT 1, IN 2

### **Absolute Maximum Ratings** [Tr1,Tr2]

(Ta=25°C)

Characteristic	Symbol	Rating		Unit	
Character istic	Tr1		Tr2	Cint	
Output voltage	Vo	50	-50	V	
Input voltage	V <sub>I</sub>	40,-10	-20,-7	V	
Output current	I <sub>O</sub>	100 -100		mA	
Power dissipation	$P_D^*$	200		mW	
Junction temperature	Tı	150		°C	
Storage temperature range	$T_{stg}$	-55 ~ 150		°C	

: Total rating

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## **Electrical Characteristics** [Tr1]

 $(Ta=25^{\circ}C)$ 

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	V <sub>0</sub> =50V, V <sub>I</sub> =0	-	ı	500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>0</sub> =5V, I <sub>0</sub> =10mA	80	200	-	ı
Output voltage	V <sub>O(ON)</sub>	I <sub>O</sub> =10mA, I <sub>I</sub> =0.5mA	-	0.1	0.3	٧
Input voltage (ON)	$V_{I(ON)}$	V <sub>0</sub> =0.2V, I <sub>0</sub> =5mA	-	2.8	5.0	>
Input voltage (OFF)	V <sub>I(OFF)</sub>	V <sub>0</sub> =5V, I <sub>0</sub> =0.1mA	1.0	1.2	-	<b>V</b>
Transition frequency	f <sub>T</sub> *	V <sub>O</sub> =10V, I <sub>O</sub> =5mA, f=1MHz	-	200	-	MHz
Input current	$I_{\rm I}$	$V_I=5V$ , $I_O=0$	-	ı	0.18	mA
Input resistor (Input to base)	R <sub>1</sub>	-	33	47	61	ΚΩ
Input resistor (Base to common)	R <sub>2</sub>	-	33	47	61	ΚΩ

<sup>\* :</sup> Characteristic of transistor only

## **Electrical Characteristics** [Tr2]

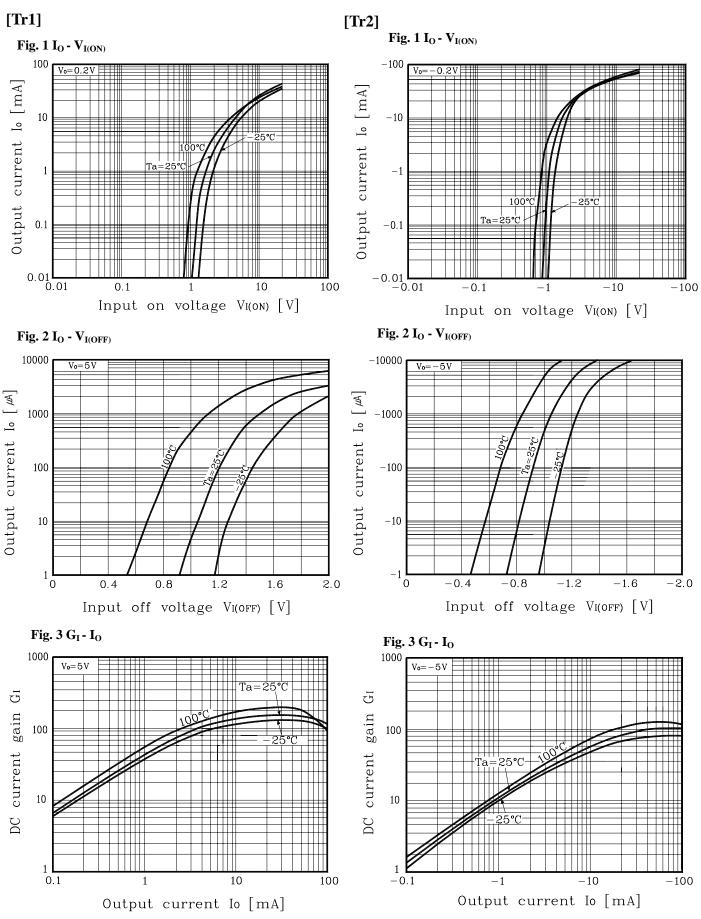
(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Output cut-off current	I <sub>O(OFF)</sub>	V <sub>0</sub> =-50V, V <sub>I</sub> =0	-	-	-500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>0</sub> =-5V, I <sub>0</sub> =-10mA	30	-	-	-
Output voltage	V <sub>O(ON)</sub>	$I_{O}$ =-10mA, $I_{I}$ =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	V <sub>0</sub> =-0.2V, I <sub>0</sub> =-5mA	-	-1.2	-1.6	V
Input voltage (OFF)	$V_{I(OFF)}$	V <sub>0</sub> =-5V, I <sub>0</sub> =-0.1mA	-0.5	-0.82	-	V
Transition frequency	f <sub>T</sub> *	V <sub>O</sub> =-10V, I <sub>O</sub> =-5mA, f=1MHz	-	200	-	MHz
Input current	I <sub>I</sub>	$V_{\rm I}$ =-5V, $I_{\rm O}$ =0	-	-	-1.8	mA
Input resistor (Input to base)	$R_1$	-	3.3	4.7	6.1	<b>K</b> Ω
Input resistor (Base to common)	R <sub>2</sub>	-	7	10	13	<b>K</b> Ω

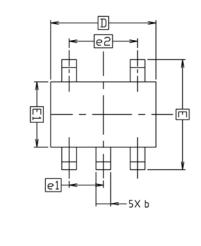
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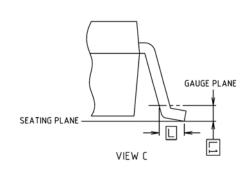
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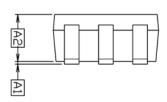
## **Electrical Characteristic Curves**

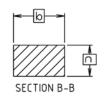


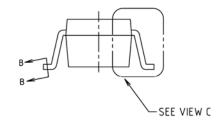
## **Outline Dimension**





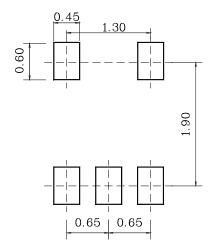






		MILLIMETERS			
SYMBOL	MINIMUM	NOMINAL	NOTE		
A1	0.00	_	0.10		
A2	0.90	0.95	1.00		
Ь	0.25	_	0.40		
С	0.10	_	0.25		
D	1.90	2.00	2.10		
E	1.95	2.10	2.25		
E1	1.15	1.25	1.35		
e1	0.65 BSC				
e2	1.30 BSC				
L	0.25	_	_		
1.1					

### \* Recommend PCB solder land [Unit: mm]



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