Triacs

Silicon Bidirectional Thyristors

Designed primarily for full-wave ac control applications, such as light dimmers, motor controls, heating controls and power supplies.

Features

- Blocking Voltage to 400 V
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Four Quadrant Gating
- Pb-Free Package is Available*

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Off–State Voltage (Note 1) (T _J = -40 to +125°C, Gate Open)	$V_{ m DRM,} \ V_{ m RRM}$	400	V
On–State RMS Current (All Conduction Angles, T _C = +80°C)	I _{T(RMS)}	8.0	Α
Peak Non–Repetitive Surge Current (One Full Cycle Sine Wave, 60 Hz, T _J = +80°C)	I _{TSM}	100	Α
Circuit Fusing Consideration (t = 8.3 ms)	I ² t	40	A ² s
Peak Gate Power (Pulse Width = 10 μ s, T _C = +80°C)	P _{GM}	16	W
Average Gate Power (t = 8.3 ms, T _C = +80°C)	P _{G(AV)}	0.35	W
Peak Gate Current (Pulse Width = 10 μ s, T _C = +80°C)	I _{GM}	4.0	Α
Operating Junction Temperature Range	T_J	-40 to +125	°C
Storage Temperature Range	T _{stg}	-40 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

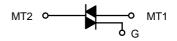
 V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.



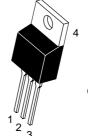
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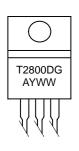
TRIACS 8 AMPERES RMS, 400 VOLTS



MARKING DIAGRAM



TO-220AB CASE 221A STYLE 4



A = Assembly Location

′ = Year

WW = Work Week

G = Pb-Free Package

PIN ASSIGNMENT			
1	Main Terminal 1		
2	Main Terminal 2		
3	Gate		
4	Main Terminal 2		

ORDERING INFORMATION

Device	Package	Shipping
T2800D	TO-220AB	500 Units/Box
T2800DG	TO-220AB (Pb-Free)	500 Units/Box

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	2.2	°C/W
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 10 Sec	TL	260	°C

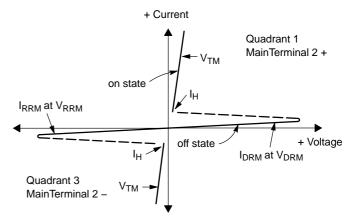
ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted: Electricals apply in both directions)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Peak Repetitive Blocking Current (V_D = Rated V_{DRM} , V_{RRM} ; Gate Open) $T_C = 25^{\circ}C$ $T_C = 100^{\circ}C$	I _{DRM} , I _{RRM}	_ _	_ _	10 2.0	μA mA
ON CHARACTERISTICS					
Peak On-State Voltage (Note 2) (I _T = ±30 A Peak)	V _{TM}	-	1.7	2.0	V
Gate Trigger Current (Continuous dc) $(V_D=12\ Vdc,\ R_L=100\ \Omega) \\ MT2(+),\ G(+) \\ MT2(+),\ G(-) \\ MT2(-),\ G(-) \\ MT2(-),\ G(+) \\ MT2(-),\ G$	I _{GT}	- - - -	10 20 15 30	25 60 25 60	mA
Gate Trigger Voltage (Continuous dc) (All Quadrants) ($V_D = 12 \text{ Vdc}, R_L = 100 \Omega$)	V _{GT}	-	1.25	2.5	V
Gate Non-Trigger Voltage (Continuous dc) $(V_D = 12 \text{ V}, R_L = 100 \Omega, T_C = 100^{\circ}\text{C})$	V _{GD}	0.2	-	-	V
Holding Current $(V_D = 12 \text{ Vdc, Initiating Current} = \pm 200 \text{ mA, Gate Open})$	IH	-	15	30	mA
Gate Controlled Turn-On Time $(V_D = Rated V_{DRM}, I_T = 10 A, I_{GT} = 80 mA, Rise Time = 0.1 \mu s)$	t _{gt}	-	1.6	-	μS
DYNAMIC CHARACTERISTICS					
Critical Rate-of-Rise of Commutation Voltage $(V_D = Rated\ V_{DRM},\ I_{T(RMS)} = 8\ A,\ Commutating\ di/dt = 4.1\ A/ms,\ Gate\ Unenergized,\ T_C = 80^{\circ}C)$	dv/dt(c)	-	10	-	V/μs
Critical Rate-of-Rise of Off-State Voltage (V _D = Rated V _{DRM} , Exponential Voltage Rise, Gate Open, T _C = 100°C)	dv/dt	60	-	-	V/μs

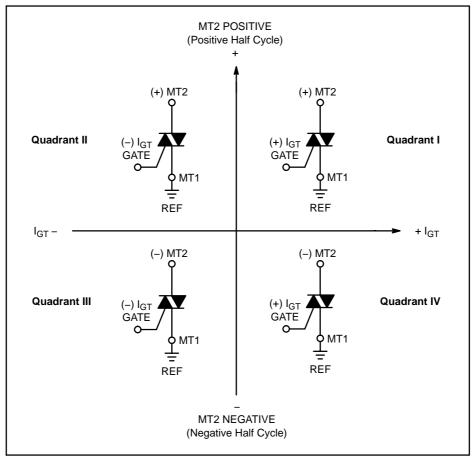
^{2.} Pulse Test: Pulse Width ≤ 2.0 ms, Duty Cycle ≤ 2%.

Voltage Current Characteristic of Triacs (Bidirectional Device)

Symbol	Parameter
V_{DRM}	Peak Repetitive Forward Off State Voltage
I _{DRM}	Peak Forward Blocking Current
V _{RRM}	Peak Repetitive Reverse Off State Voltage
I _{RRM}	Peak Reverse Blocking Current
V _{TM}	Maximum On State Voltage
I _H	Holding Current



Quadrant Definitions for a Triac



All polarities are referenced to MT1.

With in-phase signals (using standard AC lines) quadrants I and III are used.

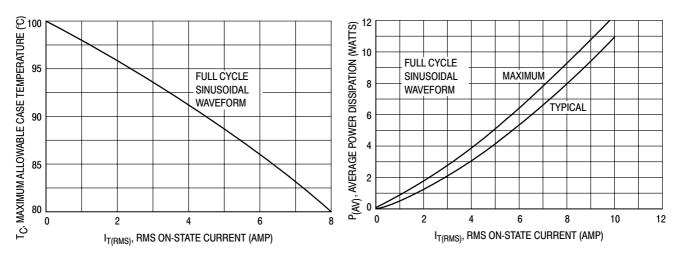
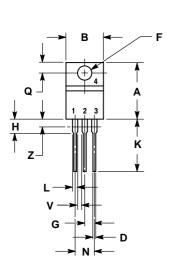


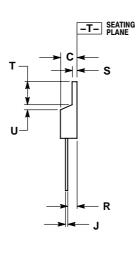
Figure 1. Current Derating

Figure 2. Power Dissipation

PACKAGE DIMENSIONS

TO-220 **CASE 221A-07 ISSUE AA**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
- 2 CONTROLLING DIMENSION: INCH
- DIMENSION Z DEFINES A ZONE WHERE ALL
 BODY AND LEAD IRREGULARITIES ARE
 ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.014	0.022	0.36	0.55
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

STYLE 4:

PIN 1. MAIN TERMINAL 1

- MAIN TERMINAL 2 2. 3.
- MAIN TERMINAL 2

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