HSOP3-P-1.50

Weight: 0.05 g (typ.

Cathode (K)

Anode (A)

TOSHIBA Bipolar Linear Integrated Circuit Silicon Monolithic

TA76431F,TA76431FR

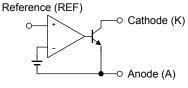
Adjustable Precision Shunt Regulator

Features

- Precision Reference Voltage: $V_{REF} = 2.495 V \pm 2.2\%$
- Small Temperature Coefficient: |αV_{REF}| = 46 ppm/°C
- Adjustable Output Voltage: $V_{REF} \le V_{OUT} \le 36 V$
- Low Dynamic Output Impedance: $|Z_{KA}| = 0.15 \Omega$ (Typ.)
- Small Flat Package
- TA76431FR is a new Toshiba shunt regulator. This device's pin assignment is the reverse of that of the TA76431F.

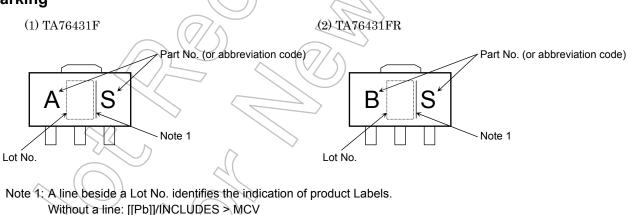
Functional Block Diagram

gram Circuit Symbol hode (K) Reference (REF)



This IC contains electrostatic sensitive elements. Please take care to avoid generating static electricity when handling these devices.

Marking



With a line: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Pin Assignment

2

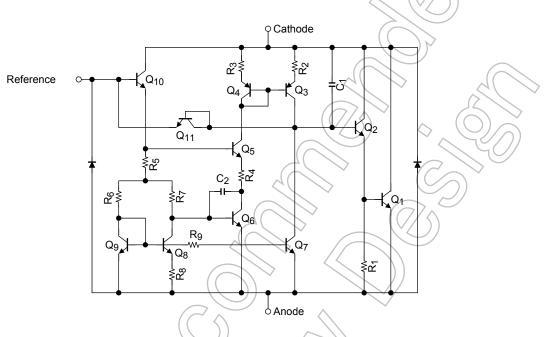
No.	(1) TA76431F	(2) TA76431FR			
1	Cathode (K)	Reference (REF)			
2	Anode (A)	Anode (A)			
3	Reference (REF)	Cathode (K)			

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How to Order

No.	Product No.	Package Type	Packing Type and Capacity		
(1)	TA76431F (F)		On cut tape (TE12L,F): 100 pcs/tape section		
(1)	TA76431F (TE12L,F)	PW-Mini (SOT-89)	Embossed tape: 1000 pcs/reel		
(2)	TA76431FR (F)	(surface-mount type) On cut tape (TE12L,F): 100 pcs/tape section			
(2)	TA76431FR (TE12L,F)		Embossed tape: 1000 pcs/reel		

Equivalent Circuit



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Cathode voltage	VKA	37	V	
Cathode current	IK	-100 to 150	mA	
Reference voltage	V _{REF}	7	V	
Reference current	IREF	50	μA	
Reference-anode reverse current	-IREF	10	mA	
Power dissipation (Ta = 25° C)	B	500	mW	
Power dissipation (1a - 25 C)	PD	1000 (Note 2)	IIIVV	
Operating temperature	Topr	-40 to 85	°C	
Storage temperature	T _{stg}	–55 to 150	°C	

Note 2: Mounted on ceramic substrate (250 mm² \times 0.8 mm (t))

Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

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Operating Ranges

Characteristics	Symbol	Min	Тур.	Max	Unit
Cathode voltage	V _{KA}	V _{REF}	—	36	V
Cathode current	١ _K	1	_	100	mA
Operating temperature	T _{opr}	-40	_	85	°C

Electrical Characteristics (Unless otherwise specified, $Ta = 25^{\circ}C$, $I_{K} = 10$ mA)

		~ ((//				
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Reference voltage	V _{REF}	V _{KA} = V _{REF}	2.440	2.495	2.550	V
Deviation of reference input voltage over temperature	V _{REF (dev)}	$0^{\circ}C \le Ta \le 70^{\circ}C, V_{KA} = V_{REF}$	-	8	17	mV
Ratio of change in reference input	A)//A)/	$V_{REF} \le V_{KA} \le 10 V$	- (0.8	2.7	m)////
voltage to the change in cathode voltage	$\Delta V_{REF} / \Delta V$	$10 \text{ V} \le \text{V}_{\text{KA}} \le 36 \text{ V}$	-((0.5	2.0	mV/V
Reference Input current	I _{REF}	V _{KA} = V _{REF}	R	(1 <u>4</u>)	4	μA
Deviation of reference input current over temperature	IREF (dev)	$\begin{array}{l} 0^{\circ}C \leq Ta \leq 70^{\circ}C, \ V_{KA} = V_{REF}, \\ R_{1} = 10 \ k\Omega, \ R_{2} = \infty \end{array}$		0.3	1.2	μA
Minimum cathode current for regulation	I _{Kmin}	VKA=VREF	D	0.4	1.0	mA
Off-State cathode current	I _{Koff}	V _{KA} = 36 V, V _{REF} = 0 V) _	-	1.0	μA
Dynamic impedance	Z _{KA}	$\label{eq:VKA} \begin{split} V_{KA} = V_{REF}, f \leq 1 \text{kHz}, \\ 1 \text{mA} \leq I_K \leq 100 \text{mA} \end{split}$	_	0.15	0.5	Ω

The deviation parameters V_{REF} (dev) and I_{REF} (dev) are defined as the maximum variation of the V_{REF} and I_{REF} over the rated temperature range.

The average temperature coefficient of the VREF is defined as:

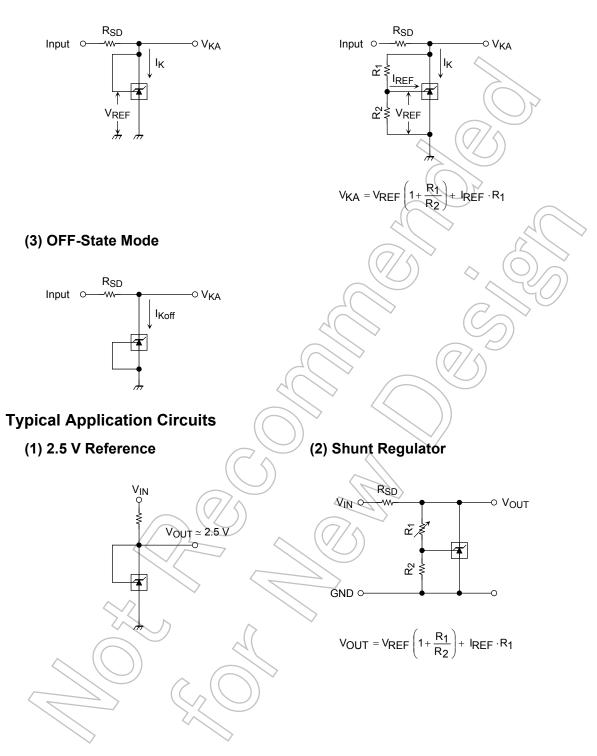
max VREF (dev) VREF min ∆Ta

VREF (dev) ×10⁶ VREF @25°C (ppm/°C) αVREF ∆Ta

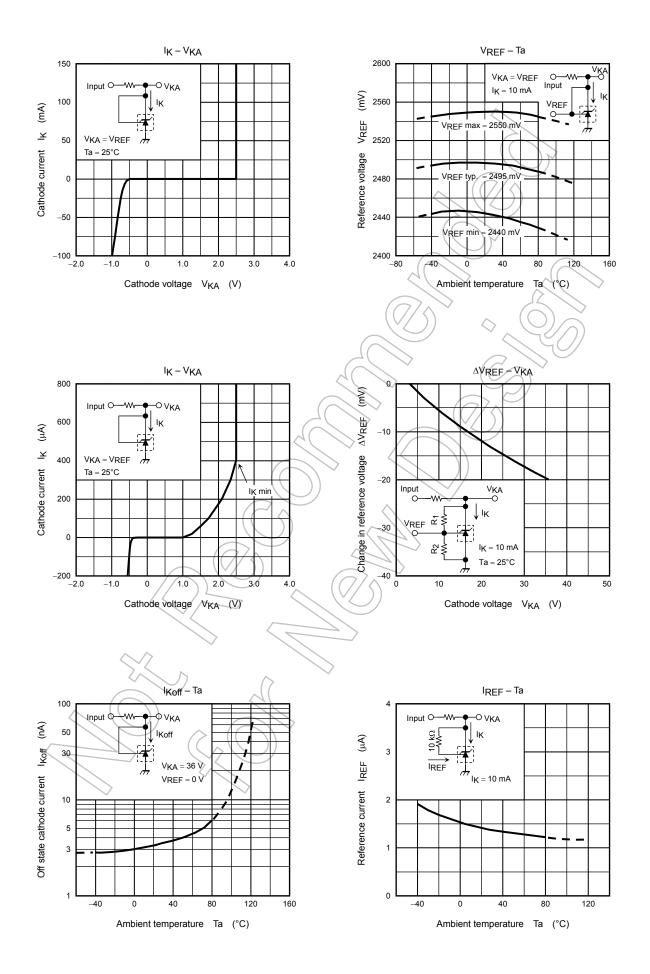
Test Parameter



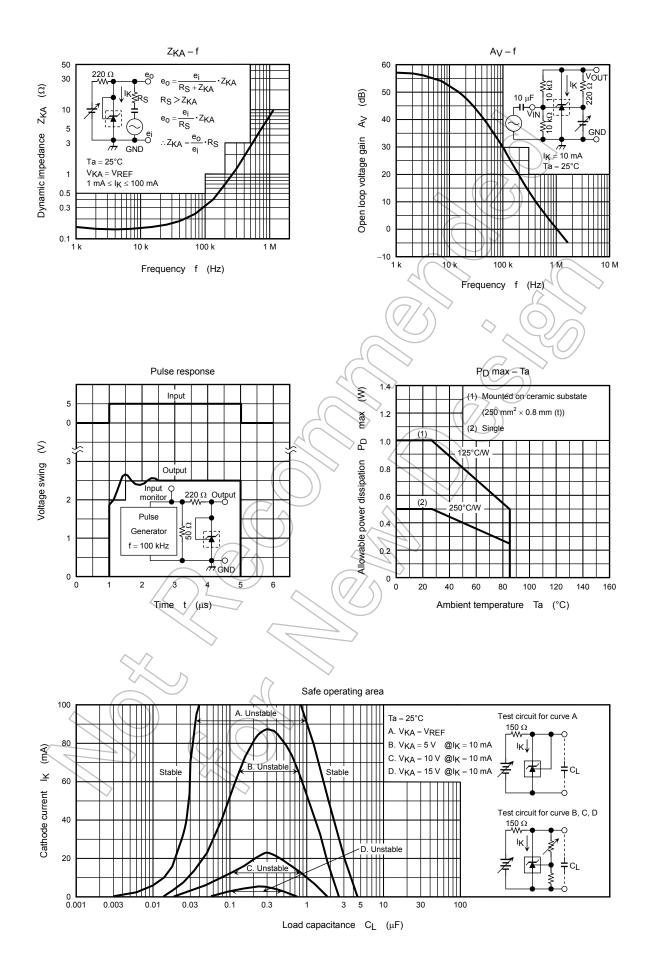
(2) $V_{KA} > V_{REF}$ Mode



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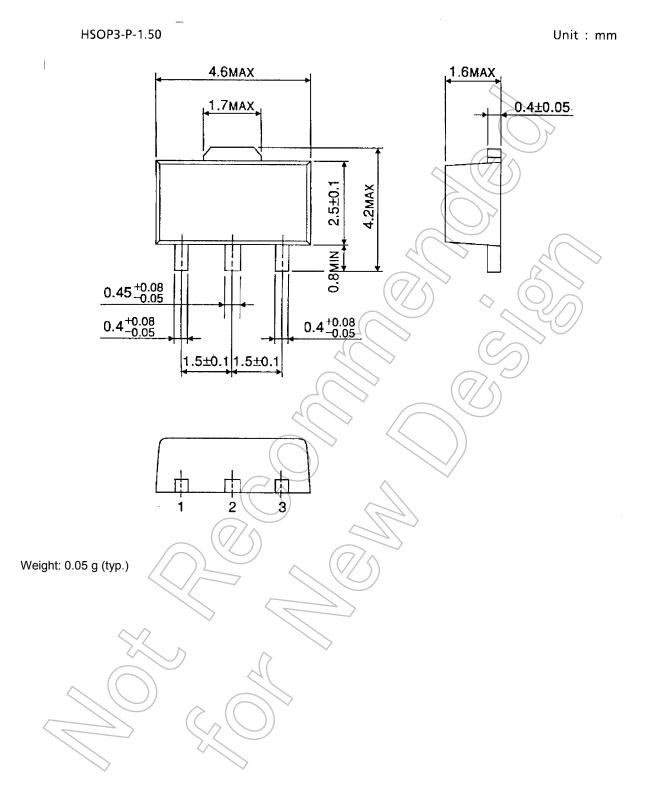


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Package Dimensions



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