TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA8052AS

0.3A MOTOR DRIVER WITH BRAKE FUNCTION

The TA8052AS is a full-bridge driver which directly drives a bidirectional DC motor. Inputs DI1 and DI2 are combined to select one of forward, reverse, stop, and brake modes. Since the inputs are TTL-compatible, the IC can be directly controlled from a CPU or other control system. The IC also has various protective functions.

FEATURES

• Output current: 300mA (max.)

• Four modes : Forward, reverse, stop, and brake

• Low Standby Current : 100 μ A (Max.)

Multiple protective functions

: Thermal shutdown, current limiter, andovervoltage shut down.

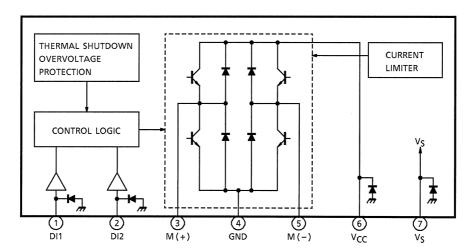
• Bulit-in diode for counteracting counter electromotive force

Small SIP-7pin

SIP7-P-2.54A

Weight: 0.7 g (typ.)

BLOCK DIAGRAM AND PIN LAYOUT



PIN DESCRIPTION

PIN No.	SYMBOL	DESCRIPTION
1	DI1	Output status control pin.
2	DI2	Connects to a PNP-type voltage comparator.
3	M (+)	Connects to the DC motor. Diodes for absorbing counter electromotive force are contained on the V $_{\rm CC}$ and GND sides.
4	GND	Grounded
5	M (-)	Connects to the DC motor together with pin 3 and has the same function as pin 3. This pin is controlled by the inputs from pins 1 and 2.
6	V _{CC}	Power supply pin. This pin has a function to turn off the output when the applied voltage exceeds 30V, thus protecting the IC and the load.
7	V _S	Power supply pin for the control section. This pin is completely separated from the V _{CC} pin.

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TRUTH TABLE

INF	PUT	OUT	PUT	011701714007			
DI1	DI2	M (+)	M (-)	OUTPUT MODE			
Н	Н	L	L	BRAKE			
L	Н	L	Н	REVERSE			
Н	L	Н	L	FORWARD			
L	L	OFF (high impedance)		STOP (*)			

^{*:} LOW STANDBY CURRENT MODE : 100µA (MAX.)

MAXIMUM RATINGS (Ta = 25°C)

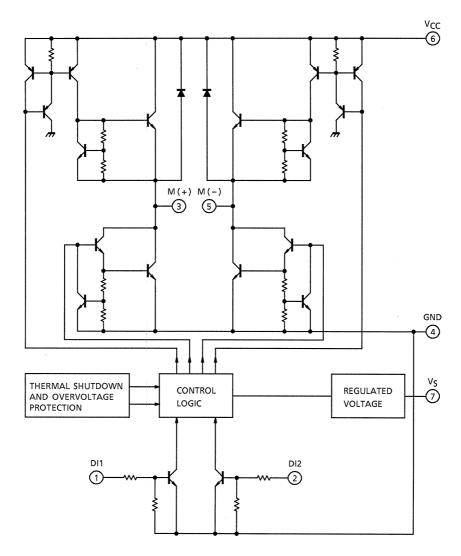
CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V_{CC}	50 (1s)	V
Input Voltage	V _{IN}	-0.3~V _{CC} +0.3	V
Output Current	I _{OUT}	300	mA
Power Dissipation	P_{D}	0.92	W
Operation Temperature	T _{opr}	-40~85	°C
Storage Temperature	T _{stg}	-55~150	°C
Lead Temperature Time	T _{sol}	260 (10s)	°C

ELECTRICAL CHARACTERISTICS (VS, $V_{CC} = 8\sim16V$, Ta = $-40\sim85^{\circ}C$)

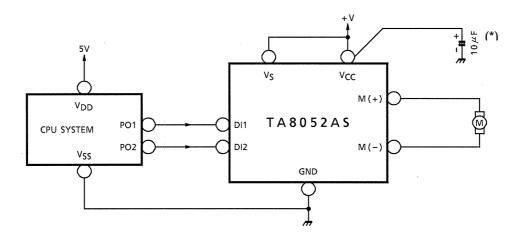
CHARACTERISTIC	SYMBOL	PIN	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT
	I _{S1}	Vs	_	Stop	_	_	0.05	mA
Current Consumption (I)	I _{S2}		_	Forward / Reverse	_	6	15	
	I _{S3}		_	Brake	_	9	20	
	I _{CC1}	V _{CC}	_	Stop	_	_	0.05	mA
Current Consumption (II)	I _{CC2}		_	Forward / Reverse	_	7.5	15	
	I _{CC3}		_	Brake	_	_	1	
Input Voltage	V _{IL}	DI1 / DI2		_	_	_	0.8	V
input voltage	V _{IH}		_		2.0	_	_	
Input Current	I _{IL}	DI1 / DI2	_	V _{IN} = 0.4V	_	10	20	μА
input Current	I _{IH}		_	V _{IN} = 5V	_	170	350	
Output Saturation Voltage	V _{sat} (total)	M (+) / M (-)	_	I _O = 200mA	_	1.8	2.5	V
Output Leakage Current	I _{LEAK-U}	M (+) / M (-)	_	V _O = 0V	_	_	-100	μА
Output Leakage Current	I _{LEAK-L}		_	$V_O = V_{CC}$	_	_	100	
Diode Forward Voltage	V _{F-U}	M (+) / M (-)	_	I _F = 200mA	_	1.1	_	V
Diode Forward Voltage	V _{F-L}		_	I _F = 200mA	_	1.1	_	
Output Limit Current	I _{SC}		_	Ta = 25°C	0.3	0.55	_	Α
Shutdown Temperature	T _{SD-H}		_	$ON \rightarrow OFF$	_	150		°C
Shuluown Temperalure	T _{SD-L}		_	OFF → ON	_	130	_	C
Overvoltage Detection	V _{SD}		_		27	30	33	V
Transfer Dolay Time	t _{pLH}		_		_	1	10	- µs
Transfer Delay Time	t _{pHL}		_		_	1	10	

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I/O EQUIVALENT CIRCUIT



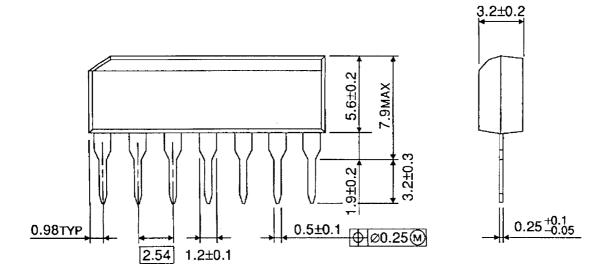
EXAMPLE OF APPLICATION CIRCUIT

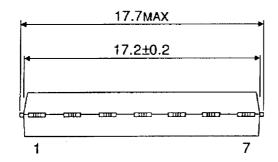


*: Connect this capacitor as close to the IC as Possible.

PACKAGE DIMENSIONS

SIP7-P-2.54A Unit: mm





Weight: 0.7g (Typ.)

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RESTRICTIONS ON PRODUCT USE

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