<u>TOSHIBA</u>

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62003PA,TD62003APA,TD62004PA,TD62004APA

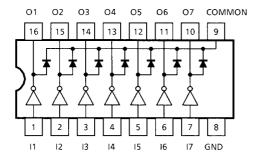
7CH DARLINGTON SINK DRIVER

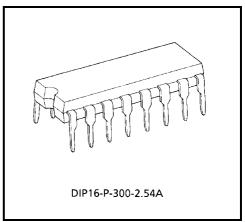
The TD62003PA / APA Series are high-voltage, high-current darlington drivers comprised of seven NPN darlington pairs. All units feature integral clamp diodes for switching inductuve loads. Applications include relay, hammer, lamp and display (LED) drivers.

FEATURES

- Output current (single output) 500 mA (Max.)
- High sustaining voltage output 35 V (Min.) (TD62003PA series) 50 V (Min.) (TD62003APA series)
- Output clamp diodes
- $\label{eq:Inputs compatible with various types of logic.} TD62003PA, APA \qquad R_{IN} = 2.7 \ \text{k}\Omega \\ TD62004PA, APA \qquad R_{IN} = 10.5 \ \text{k}\Omega \\ \end{tabular}$
- Package DIP-16 pin

PIN CONNECTION (TOP VIEW)

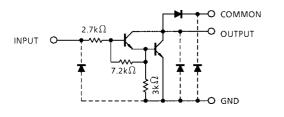


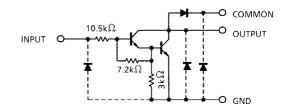


Weight: 1.11g (Typ.)

SCHEMATICS (EACH DRIVER)

TD62003PA / APA





TD62004PA / APA

Note: The input and output parasitic diodes cannot be used as clamp diodes.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTER	RISTIC	SYMBOL	RATING	UNIT	
Output Sustaining	PA	Vor (outo)	-0.5~35	V	
Voltage	APA	VCE (SUS)	-0.5~50	v	
Output Current		IOUT	I _{OUT} 500		
Input Voltage	out Voltage		-0.5~30	V	
Clamp Diode Reverse Voltage	PA	V _R	35	V	
	APA	VR	50	v	
Clamp Diode Forward C	urrent	١ _F	500	mA	
Power Dissipation		PD	1.47	W	
Operating	PA	т	-30~75	°C	
Temperature	APA	T _{opr}	-40~85	C C	
Storage Temperature	Storage Temperature		-55~150	°C	

RECOMMENDED OPERATING CONDITIONS

(Ta = -40~85°C for Type-APA and Ta = -30~75°C for Type-PA)

CHARACT	ERISTIC	SYMBOL	CONDITION		MIN	TYP.	MAX	UNIT
Output Sustaining PA					0	_	35	V
Voltage	APA	V _{CE} (SUS)				_	50	v
Output Current	PA		T _{pw} = 25 ms 7 Circuits	Duty = 10%	0	_	370	-mA / ch
	FA			Duty = 50%	0	_	140	
	APA	- I _{OUT}		Duty = 10%	0	_	400	
				Duty = 50%	0	_	170	
		V _{IN}			0	_	24	V
Input Voltage	TD62003	Variation	I _{OUT} = 400 mA, h _{FE} = 800		2.8	_	24	V
	TD62004	VIN (ON)			6.2	_	24	
	TD62003	VIN (OFF)			0	_	0.7	v
	TD62004			0	_	1.0	V	
Clamp Diode Reverse Voltage	PA	N			_	_	35	v
	APA	– V _R			_	_	50	V
Clamp Diode Forward Current		١ _F			-	_	350	mA
Power Dissipation		PD	Ta = 85°C		-	_	0.52	W

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

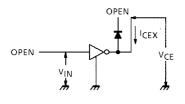
CHARACTER	ISTIC	SYMBOL	TEST CIR- CUIT		CONDITION	MIN	TYP.	MAX	UNIT
Output Leakage Current	APA	- I _{CEX}		V _{CE} = 50 V, Ta = 25°C		—	_	50	
			1	V _{CE} = 50 V, Ta = 85°C		_	_	100	μA
	PA			V _{CE} = 35 V, Ta = 25°C		_		50	
				V _{CE} = 35 V, Ta = 75°C		—	_	100	
Collector-Emitter Saturation Voltage		V _{CE (sat)}	2	I _{OUT} = 350 mA, I _{IN} = 500 μA		—	1.3	1.6	V
				I _{OUT} = 200 mA, I _{IN} = 350 μA		—	1.1	1.3	
				I _{OUT} = 100 mA, I _{IN} = 250 μA		_	0.9	1.1	
DC Current Transfer Ra	tio	h _{FE}	2	V _{CE} = 2 V, I	_{OUT} = 350 mA	1000		_	
	TD62003			V _{IN} = 2.4 V, I _{OUT} = 350 mA		_	0.4	0.7	m (
Input Current	TD62004	IN (ON)	3	V _{IN} = 9.5 V, I _{OUT} = 350 mA		_	0.8	1.3	mA
(Output On)	PA		4	I _{OUT} = 500 μA, Ta = 75°C		50	65	_	
	APA	I _{IN (OFF)}	4	I _{OUT} = 500 µ	uA, Ta = 85°C	50	65	_	μA
	TD62003	- Vin (on)		5 $V_{CE} = 2 V_{h_{FE}} = 800$ $I_{OUT} = 200 \text{ mA} - 1000 \text{ mA}$ $I_{OUT} = 350 \text{ mA} - 1000 \text{ mA}$	—	_	2.6		
Input Voltage			5		I _{OUT} = 200 mA	—	_	2.0	- V
(Output On)					I _{OUT} = 350 mA	_	_	4.7	
	1002004				I _{OUT} = 200 mA	_		4.4	
	APA		6	V _R = 50 V, Ta = 25°C		_	_	50	μA
Clamp Diode Reverse				V _R = 50 V, Ta = 85°C		_	_	100	
Current	PA	I _R		V _R = 35 V, Ta = 25°C		_	_	50	
				V _R = 35 V, Ta = 75°C		_	_	100	
Clamp Diode Forward V	oltage	V _F	7	I _F = 350 mA		_	_	2.0	V
Input Capacitance		C _{IN}	_			_	15	_	pF
Turn-On Delay	PA	t _{ON} 8		V _{OUT} = 35 V, R _L = 85 Ω C _L = 15 pF		_	0.1	_	
	APA		ð	V_{OUT} = 50 V, R _L = 125 Ω C _L = 15 pF		_	0.1	_	- ha
	PA		8	V _{OUT} = 35 V, R _L = 85 Ω C _L = 15 pF		_	0.2	_	
Turn-Off Delay	APA	toff		V_{OUT} = 50 V, R _L = 125 Ω C _L = 15 pF		_	0.2	_	

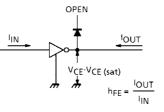
TEST CIRCUIT

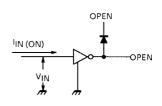
1. I_{CEX}

2. VCE (sat), hFE

3. I_{IN (ON)}



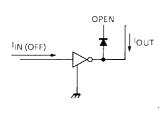


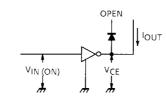


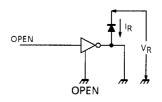
4. I_{IN (OFF)}

5. V_{IN (ON)}

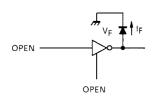






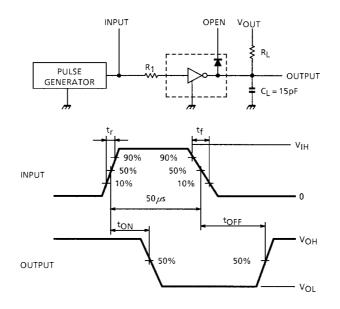


7. V_F



8. t_{ON}, t_{OFF}

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- Note 1: Pulse Width 50 μ s, Duty Cycle 10% Output Impedance 50 Ω , t_r ≤ 5ns, t_f ≤ 10ns
- Note 2: See below

INPUT CONDITION

TYPE NUMBER	RI	V _{IH}
TD620003PA / APA	0	3 V
TD620004 / APA	0	8 V

Note 3: CL includes probe and jig capacitance

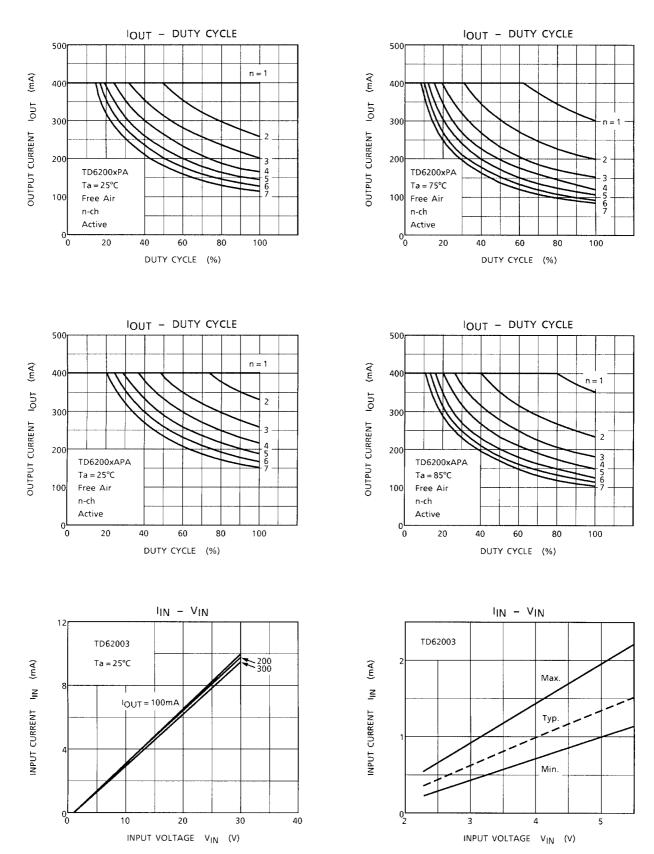
PRECAUTIONS for USING

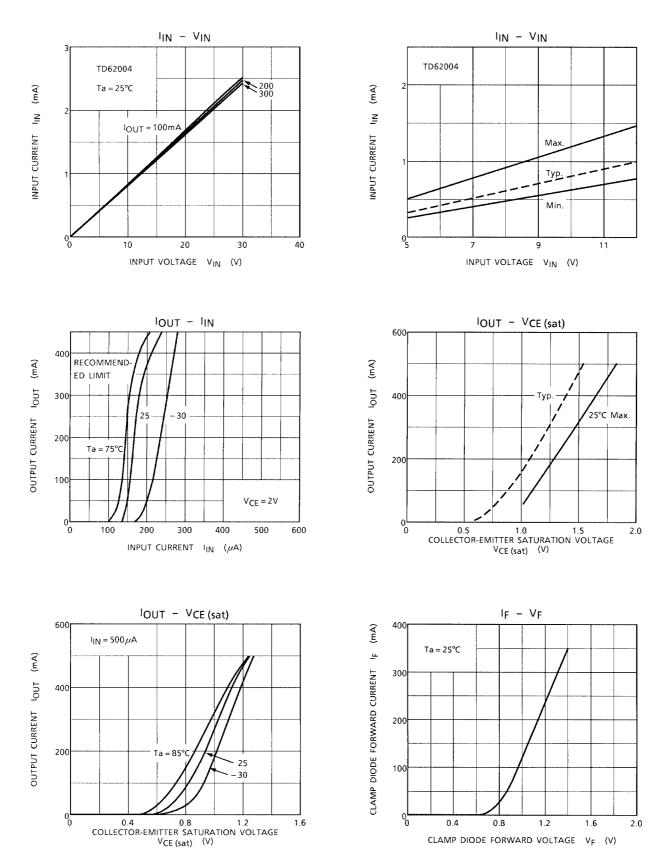
This IC does not include built-in protection circuits for excess current or overvoltage.

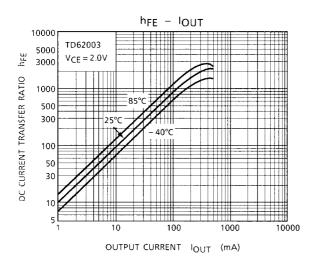
If this IC is subjected to excess current or overvoltage, it may be destroyed.

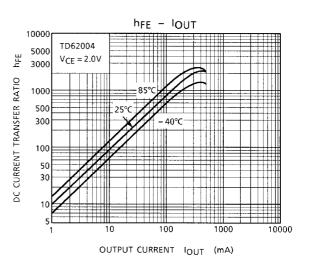
Hence, the utmost care must be taken when systems which incorporate this IC are designed.Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

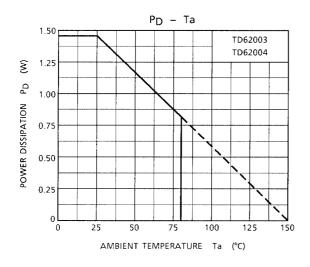
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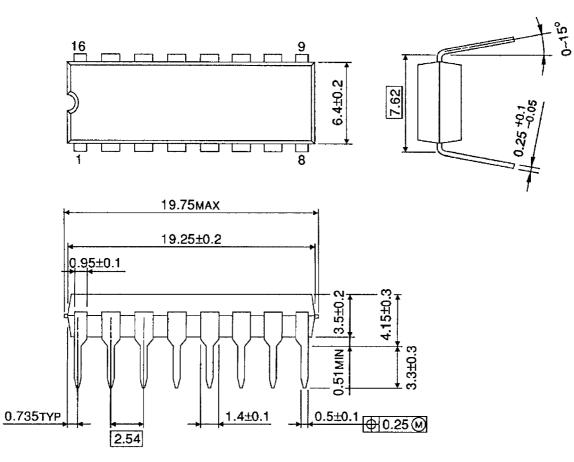




PACKAGE DIMENSIONS

DIP16-P-300-2.54A

Unit : mm



Weight: 1.11 g (Typ.)

RESTRICTIONS ON PRODUCT USE

Handbook" etc..

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