TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62107P,TD62107BP,TD62107F

4CH HIGH-CURRENT DARLINGTON SINK DRIVER

The TD62107P / BP / F are high–voltage, high–current darlington drivers and enable inputs which can gate the outputs. All units feature integral clamp diodes for switching inductive loads. The TD62107P / BP / F have a wide supply voltage range and all input are compatible with TTL and 5–V CMOS. Application include relay, hammer, lamp and stepping moter drivers.

Please observe the thermal condition for using.

FEATURES

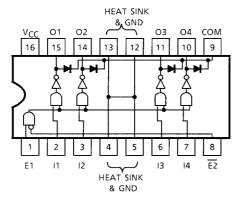
- Output current (single output) 750mA (MAX)
- High sustaining voltage output

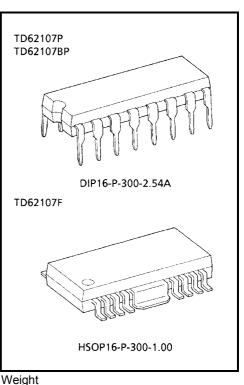
80 V MIN. (TD62107BP) 45 V MIN. (TD62107P) 35 V MIN. (TD62107F)

- Output clamp diodes
- Enable inputs E1, E2
- Wide supply voltage range VCC = 4.75~17 V
- Input compatible with TTL and 5–V CMOS
- GND terminal = heat sink
- Package type–P, BP : DIP–16pin
- Package type-F : HSOP-16pin

PIN CONNECTION (TOP VIEW)

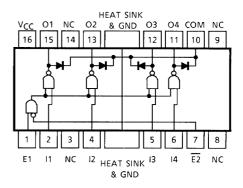
TD62107P / TD62107BP



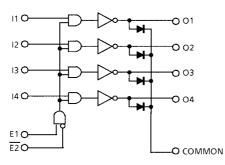


Weight DIP16-P-300-2.54A : 1.11 g (Typ.) HSOP16-P-300-1.00 : 0.50 g (Typ.)

TD62107F



SCHEMATICS (EACH DRIVER)

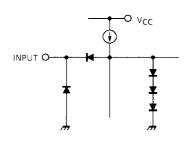


TRUTH TABLE

E1	Ē2	l1 ~ l4	01~04
L	L	L or H	Disable OFF
L	Н	L or H	Disable OFF
Н	L	L or H	Enable In
Н	Н	L or H	Disable OFF

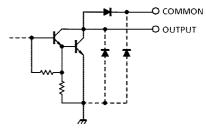
ln = l1 ~ l4

INPUT EQUIVALENT CIRCUIT



MAXIMUM RATINGS (Ta = 25°C)

OUTPUT EQUIVALENT CIRCUIT



Note: The input and output parasitic diodes cannot be used as clamp diodes.

CHARACTERISTIC SYMBOL RATING UNIT -0.5~17 Supply Voltage ٧ V_{CC} Ρ -0.5~45 Output Sustaining Voltage ΒP VCE (SUS) -0.5~80 V F -0.5~35 **Output Current** 750 mΑ IOUT V Input Voltage -0.5~V_{CC} + 0.5 V_{IN} Ρ 45 Clamp Diode Reverse Voltage BΡ V_R 80 V F 35 P, F 500 Clamp Diode Forword Current IF mΑ ΒP 750 P, BP 2.7 (Note 1) Power Dissipation PD W F 1.4 (Note 2) °C **Operating Temperature** Topr -40~85 Storage Temperature T_{stg} -55~150 °C

Note 1: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 50%)

Note 2: On Glass Epoxy PCB (60 × 30 × 1.6 mm Cu 30%)

RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)

CHARACTERISTIC		SYMBOL	CONDITION		MIN	TYP.	MAX	UNIT	
Supply Voltage		V _{CC}			4.75	_	15	V	
Output Sustaining Voltage	Р				0	_	45		
	BP	V _{CE (SUS)}			0	_	80	V	
	F			0	-	35			
Output Current	P, F	IOUT	T _{pw} = 25ms, Duty = 75%, 1 Circuit		0	_	500		
	BP		T _{pw} = 25ms, Duty = 10%, 4 Circuits		0	_	750		
	P, BP		T _{pw} = 25ms, 4 Circuits	Duty = 30%	0	_	400	mA	
	F			Duty = 40%		_	300		
Input Voltage		V _{IN}			0	_	V _{CC}	V	
Clamp Diode ReverseVoltage	Р	V _R			_	_	45		
	BP				_	_	80	V	
	F				_	_	35	1	
Clamp Diode Forward Current	P, F	IF			_	_	500	mA	
	BP				_	_	750	ША	
Power Dissipation	B, BP	PD			_	_	1.0	w	
	F		Ta = 85°C (Note)		_	_	0.7	vv	

Note: On Glass Epoxy PCB (60 × 30 × 1.6 mm Cu 30%)

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

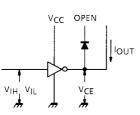
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN	TYP.	MAX	UNIT	
Input Voltage	"H" Level		V _{IH}	- 1		2.0	_	V _{CC}	v
	"L" Level	"L" Level] '		_	_	0.8	
Output Current		Р	I _{ОН}	2	V _{CE} = 45 V, Ta = 75°C	—	_	100	μA
	"H" Level	BP			V _{CE} = 80 V, Ta = 85°C	_	_	100	
		F			V _{CE} = 35 V, Ta = 85°C	_	_	100	
Output Voltage	"I " I ovol	P, F	V _{OL}	3	I _{OUT} = 50 mA	_	_	1.3	v
	"L" Level	BP			I _{OUT} = 750 mA	_	_	1.6	
input	"H" Level		IIH	4	V _{IN} = 13 V	_	_	100	μA
	"L" Level		۱ _{۱L}	5	V _{IN =} 0.4 V	_	_	-0.3	mA
Clamp Diode Reverse Current		Р	I _R	6	V _R = 45 V	_	_	100	μA
		BP			V _R = 80 V	_	_	100	
		F			V _R = 35 V	_	_	100	
Clamp Diode Forward Voltage		P, F	VF	7	I _F = 500 mA	_	_	2.0	v
		BP			I _F = 750 mA	_	_	2.0	
Supply Current	Output "H"	ICC	Іссн	4	V _{CC} = 13 V, V _{IN} = 0 V, OUTPUT OPEN	_	_	13	mA
	Output "L"		I _{CCL}	5	V _{CC} = 13 V, V _{IN} = 5 V, OUTPUT OPEN	-	_	17	mA
Turn-On Delay		Р	ton	8	V _{CC} = 5 V, R _L = 90 Ω C _L = 15 pF, V _{OUT} = 45 V	_	5	_	μs
		BP			V_{CC} = 5 V, V_{OUT} = 80 V R _L = 160 Ω	_	0.4	_	
		F			V_{CC} = 5 V, R _L = 70 Ω C _L = 15 pF, V _{OUT} = 35 V	_	5	_	
Turn-Off Delay BP		Р			$V_{CC} = 5 V, R_L = 90 \Omega$ $C_L = 15 pF, V_{OUT} = 45 V$	_	5	_	
		BP tOFF	8	$V_{CC} = 5 \text{ V}, V_{OUT} = 80 \text{ V}$ R _L = 160 Ω	_	1.7	_	μs	
		F			V_{CC} = 5 V, R _L = 70 Ω C _L = 15 pF, V _{OUT} = 35 V	_	5	_	

<u>TOSHIBA</u>

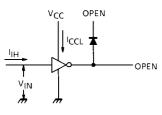
TD62107P/BP/F

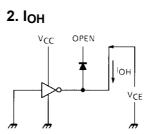
TEST CIRCUIT

 $1. \ V_{IH}, \ V_{IL}$



4. I_{IH}, I_{CCL}





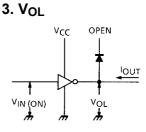
OPEN

OPEN

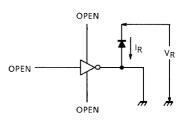
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5. IIL, ICCH

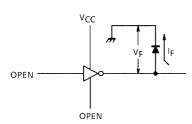
. VIN Vcc



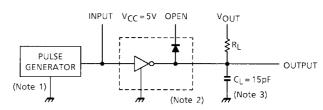




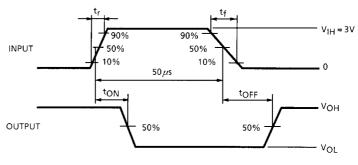
7. V_F







Input condition



Note 1: Pulse Width 50 μ s, Duty Cycle 10% Output Impedance 50 Ω , t_r ≤ 5ns, t_f ≤ 10 ns Note 2: V_{IH} = 3 V, E1 = V_{IH}, $\overline{E2}$ = GND, V_{CC} = 5 V Note 3: C_L includes probe and jig capacitance

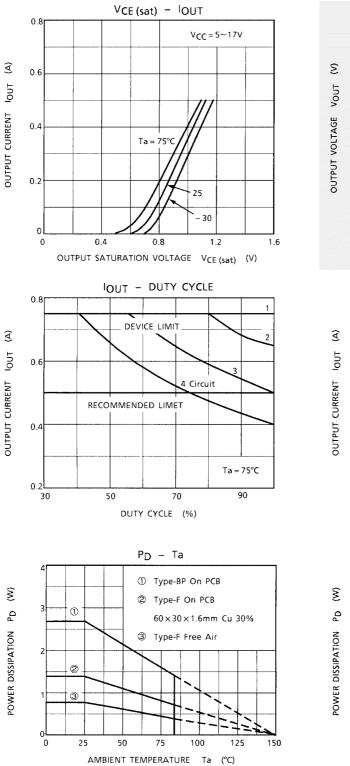
PRECAUTIONS for USING

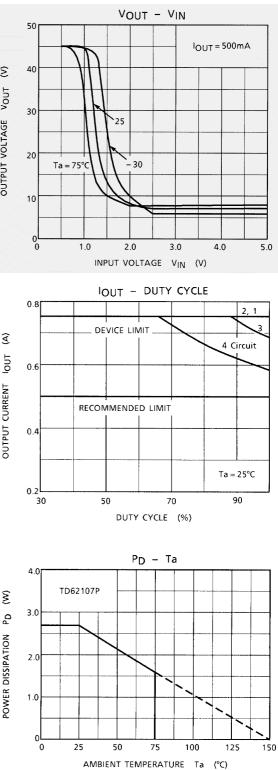
This IC does not include built-in protection circuits for excess current or overvoltage.

If this IC is subjected to excess current or overvoltage, it may be destroyed.

Hence, the utmost care must be taken when systems which incorporate this IC are designed.

Utmost care is necessary in the design of the output line, V_{CC} , COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

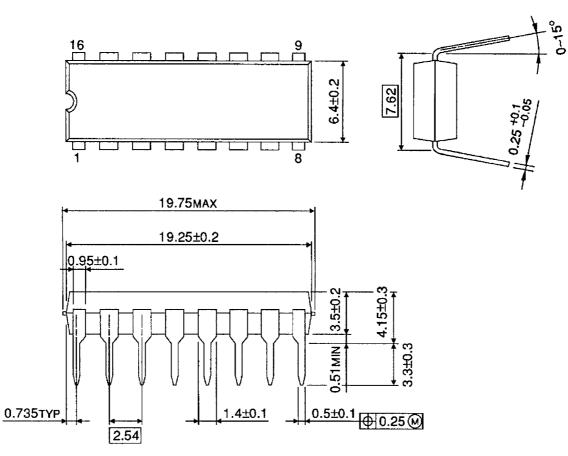




PACKAGE DIMENSIONS

DIP16-P-300-2.54A

Unit : mm

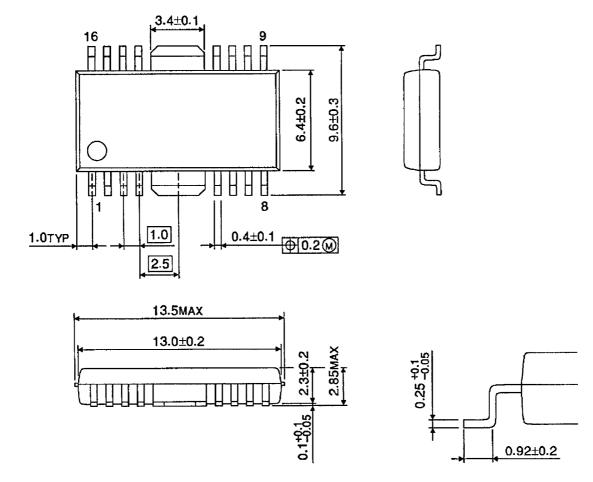


Weight: 1.11 g (Typ.)

PACKAGE DIMENSIONS

HSOP16-P-300-1.00

Unit : mm



Weight: 0.50 g (Typ.)

RESTRICTIONS ON PRODUCT USE

000707EBA

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