

Small Signal Product

Bi-directional ESD Protection Diode

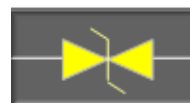
FEATURES

- Meet IEC61000-4-2 (ESD) $\pm 15\text{kV}$ (air), $\pm 8\text{kV}$ (contact)
- Designed for mounting on small surface
- Protects one Bi-directional I/O line
- Moisture sensitivity level 1
- Working Voltage : 5V, 12V, 24V
- Pb free version and RoHS compliant
- Packing code with suffix "G" means green compound (halogen-free)


0603

MECHANICAL DATA

- Case: 0603 small outline plastic package
- Terminal : Gold plated, solder per MIL-STD-705, method 2026 guaranteed
- High temperature soldering guaranteed : $260^{\circ}\text{C}/10\text{s}$
- Weight: $3 \pm 0.5 \text{ mg}$


APPLICATIONS

- Cell Phone Handsets and Accessories
- Notebooks, Desktops, and Servers
- Keypads, Side Keys, USB 2.0, LCD Displays
- Portable Instrumentation
- Touch Panel

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS ($T_A=25^{\circ}\text{C}$ unless otherwise noted)				
PARAMETER		SYMBOL	VALUE	UNIT
Peak Pulse Power ($t_p=8/20\mu\text{s}$ waveform)	TESDU5V0	P_{PP}	75	W
	TESDU12V		25	
	TESDU24V		47	
ESD per IEC 61000-4-2 (Air)		V_{ESD}	± 15	KV
ESD per IEC 61000-4-2 (Contact)			± 8	
Junction and Storage Temperature Range		T_J, T_{STG}	-55 to +150	$^{\circ}\text{C}$

PARAMETER		SYMBOL	MIN	MAX	UNIT	
Reverse Stand-Off Voltage	TESDU5V0	V_{RWM}	-	5	V	
	TESDU12V		-	12		
	TESDU24V		-	24		
Reverse Breakdown Voltage	TESDU5V0	$V_{(BR)}$	5.1	-	V	
	TESDU12V		$I_R = 1 \text{ mA}$	13		-
	TESDU24V		25	-		
Reverse Leakage Current	TESDU5V0	I_R	-	2	μA	
	TESDU12V		$V_R = 5 \text{ V}$			
	TESDU24V		$V_R = 12 \text{ V}$ $V_R = 24 \text{ V}$			
Clamping Voltage	TESDU5V0	V_C	$I_{PP} = 1 \text{ A}$	9.8	V	
			$I_{PP} = 5 \text{ A}$	15		
Clamping Voltage	TESDU12V	V_C	$I_{PP} = 1 \text{ A}$	25	V	
			$I_{PP} = 5 \text{ A}$	33		
Clamping Voltage	TESDU24V	V_C	$I_{PP} = 1 \text{ A}$	47	V	
			$I_{PP} = 5 \text{ A}$	51		
Junction Capacitance	TESDU5V0	C_J	$V_R = 0 \text{ V}$	15	pF	
	TESDU12V		$f = 1.0 \text{ MHz}$	12		
	TESDU24V		10			

Small Signal Product

RATINGS AND CHARACTERISTICS CURVES

($T_A=25^\circ\text{C}$ unless otherwise noted)

Fig. 1 Non-Repetitive Peak Pulse Power VS. Pulse Time

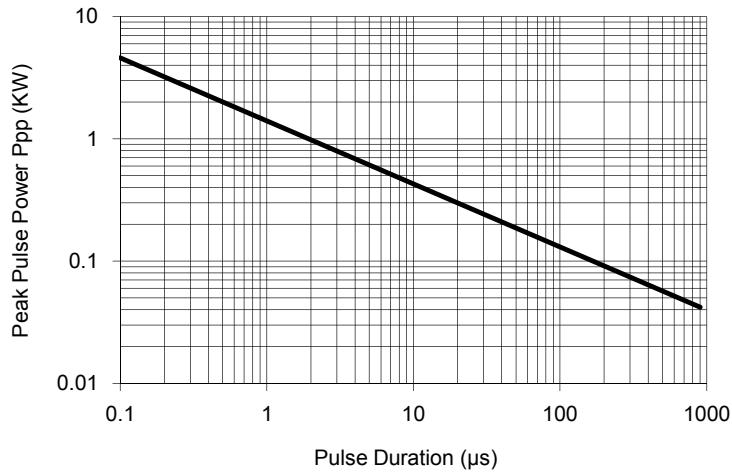


Fig. 2 Pulse Waveform

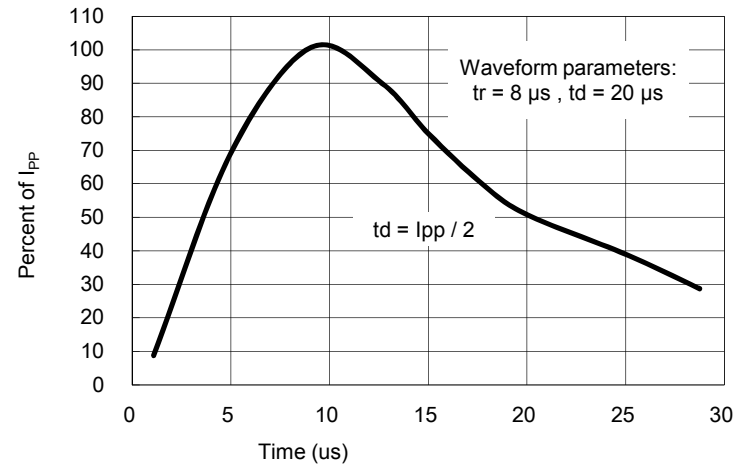


Fig. 3 Admissible Power Dissipation Curve

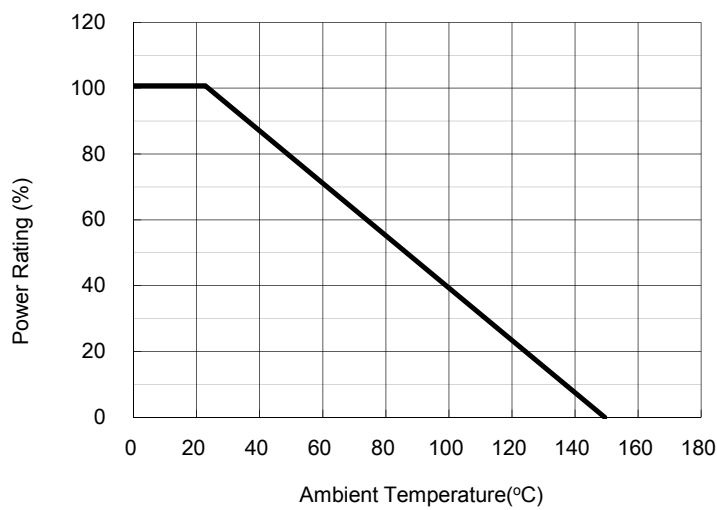


Fig. 4 Typical Junction Capacitance

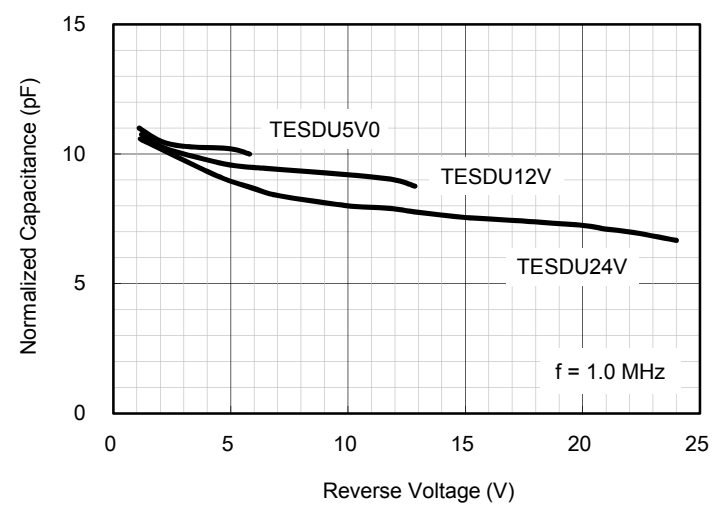
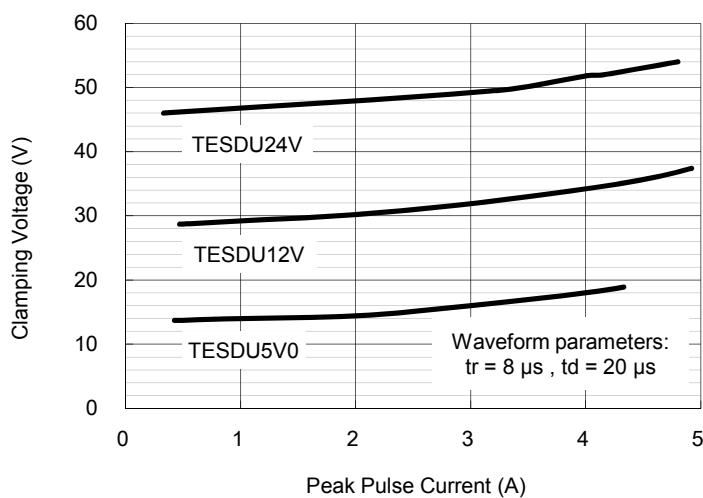


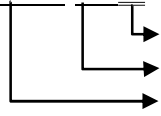
Fig. 5 Clamping Voltage VS. Peak Pulse Current



Small Signal Product

ORDER INFORMATION (EXAMPLE)

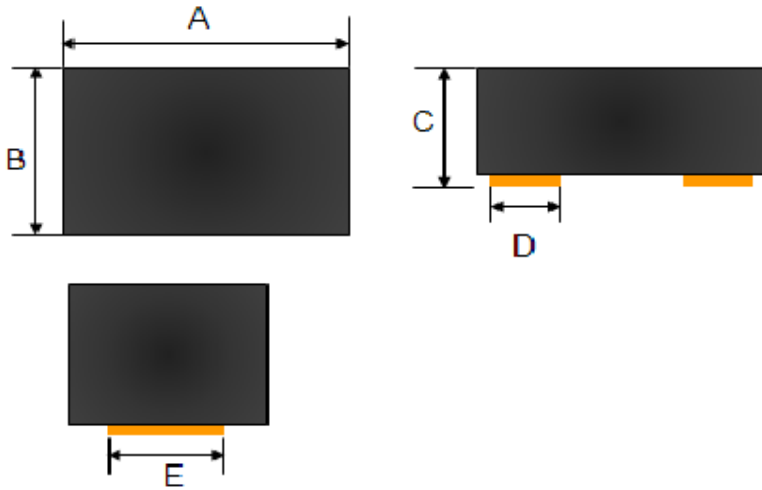
TESDU5V0 RZG



Green compound code
Packing code
Part no.

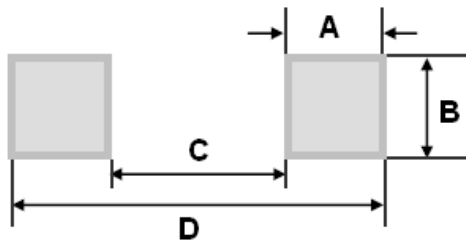
PACKAGE OUTLINE DIMENSIONS

0603



DIM.	Unit (mm)		Unit (inch)	
	Min	Max	Min	Max
A	1.60	1.80	0.063	0.071
B	0.80	1.00	0.031	0.039
C	0.70	0.85	0.028	0.033
D	0.45 (Typ.)		0.018 (Typ.)	
E	0.70 (Typ.)		0.028 (Typ.)	

SUGGEST PAD LAYOUT



DIM.	Unit (mm)	Unit (inch)
	Typ.	Typ.
A	0.60	0.024
B	1.00	0.039
C	0.65	0.026
D	1.85	0.073

Note: The suggested land pattern dimensions have been provided for reference only, as actual pad layouts may vary depending on application.

MARKING

Part NO.	Marking
TESDU5V0	E05
TESDU12V	E12
TESDU24V	E24

Small Signal Product

APPLICATIONS INFORMATION

- ◇ Designed to protect one data, I/O, or power supply line
- ◇ Designed to protect sensitive electronics from damage or latch-up due to ESD
- ◇ Designed to replace multilayer varistors (MLVs) in portable applications
- ◇ Features large cross-sectional area junctions for conducting high transient currents
- ◇ Offers superior electrical characteristics such as lower clamping voltage and no device degradation when compared to MLVs
- ◇ The combination of small size and high ESD surge capability makes them ideal for use in portable applications

CIRCUIT BOARD LAYOUT RECOMMENDATIONS

Good circuit board layout is critical for the suppression of ESD induced transients

- ◇ Place the ESD Protection Diode near the input terminals or connectors to restrict transient coupling
- ◇ Minimize the path length between the ESD Protection Diode and the protected line
- ◇ Minimize all conductive loops including power and ground loops
- ◇ The ESD transient return path to ground should be kept as short as possible
- ◇ Never run critical signals near board edges
- ◇ Use ground planes whenever possible

Notice

Specifications of the products displayed herein are subject to change without notice. TSC or anyone on its behalf, assumes no responsibility or liability for any errors or inaccuracies.

Information contained herein is intended to provide a product description only. No license, express or implied, to any intellectual property rights is granted by this document. Except as provided in TSC's terms and conditions of sale for such products, TSC assumes no liability whatsoever, and disclaims any express or implied warranty, relating to sale and/or use of TSC products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications. Customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify TSC for any damages resulting from such improper use or sale.