TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSVI)

# TJ20A10M3

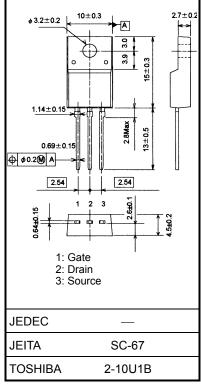
#### Swiching Regulator Applications

- Low drain-source ON resistance:  $R_{DS}$  (ON) = 63 m $\Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 50 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = -10 \ \mu A \ (max) \ (V_{DS} = -100 \ V)$
- Enhancement-model:  $V_{th} = -2.0$  to -4.0 V ( $V_{DS} = -10$  V,  $I_D = -1$  mA)

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V <sub>DSS</sub>	-100	V
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		VDGR	-100	V
Gate-source voltage		V <sub>GSS</sub>	±20	V
Drain current	DC (Note 1	) I <sub>D</sub>	-20	А
	Pulse (Note 1	) I <sub>DP</sub>	-40	А
Drain power dissipat	ion (Tc = 25°C)	PD	35	W
Single pulse avalance	he energy (Note 2	Eas	124	mJ
Avalanche current		IAS	-20	А
Repetitive avalanche	e energy (Note 3	) E <sub>AR</sub>	2.29	mJ
Channel temperature	9	T <sub>ch</sub>	150	°C
Storage temperature	range	T <sub>stg</sub>	-55 to 150	°C

Unit: mm



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the

Weight: 1.7 g (typ.)

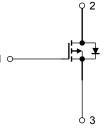
reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

- Note 1: Please use devices on condition that the channel temperature is below 150°C.
- Note 2: VDD = -25 V,  $Tch = 25^{\circ}C$ ,  $L = 500 \mu H$ ,  $RG = 25 \Omega$ , IAS = -20 A
- Note 3: Repetitive rating; pulse width limited by maximum channel temperature.

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	Rth (ch−c)	3.57	°C / W
Thermal resistance, channel to ambient	Rth (ch−a)	62.5	°C / W



Start of commercial production 2009-03

This transistor is an electrostatic sensitive device. Please handle with caution.

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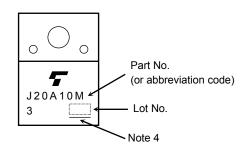
**Electrical Characteristics (Ta = 25°C)** 

Cha	racteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rrent	Igss	$V_{GS}=\pm 20~V,~V_{DS}=0~V$	_	_	±100	nA
Drain cut-OFF cu	irrent	IDSS	$V_{DS} = -100 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		-10	μA
Drain-source breakdown voltage		V (BR) DSS	$I_D = -10 \text{ mA}, \text{ V}_{GS} = 0 \text{ V}$	-100	_		v
		V (BR) DSX	$I_D = -10 \text{ mA}, \text{ V}_{GS} = 20 \text{ V}$	-75	_	_	
Gate threshold ve	oltage	V <sub>th</sub>	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$	-2.0	_	-4.0	V
Drain-source ON	resistance	R <sub>DS</sub> (ON)	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -10 \text{ A}$	_	63	90	mΩ
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -10 \text{ A}$	25	50	_	S
Input capacitance Reverse transfer capacitance Output capacitance		Ciss	V <sub>DS</sub> = -10V, V <sub>GS</sub> = 0 V, f = 1 MHz	_	5500		pF
		C <sub>rss</sub>		_	200	_	
		Coss			290		
Switching time	Rise time	tr	$U_{GS} = -10 \text{ A}$ $V_{GS} = -10 \text{ VOUT}$ $R_L = 5 \Omega$ $V_{DD} \approx -50 \text{ V}$ $Duty \le 1\%, t_W = 10 \mu\text{s}$	_	13		
	Turn-on time	ton			27	_	
	Fall time	tf			105	_	ns
	Turn-off time	t <sub>off</sub>		_	420	_	
Total gate charge (gate-source plus gate-drain)		Qg	V <sub>DD</sub> ≈ −80 V, V <sub>GS</sub> = −10 V, I <sub>D</sub> = −20 A		120	_	
Gate-source charge		Qgs1		_	20		nC
Gate-drain ("miller") charge		Qgd		_	32	_	

### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	-20	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>		_	_	-40	А
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = -20$ A, $V_{GS} = 0$ V	_	_	1.4	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = -20 \text{ A}, V_{GS} = 0 \text{ V},$	_	76	_	ns
Reverse recovery charge	Qrr	dI <sub>DR</sub> /dt = −50 A/µs	_	104	_	nC

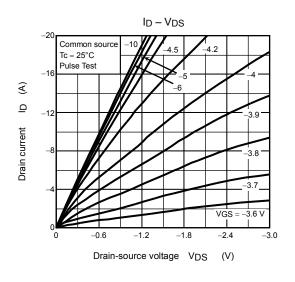
### Marking

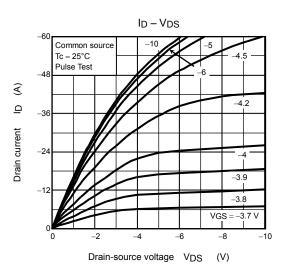


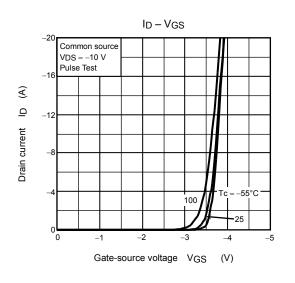
Note 4: A line under a Lot No. identifies the indication of product Labels Not underlined: [[Pb]]/INCLUDES > MCV Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

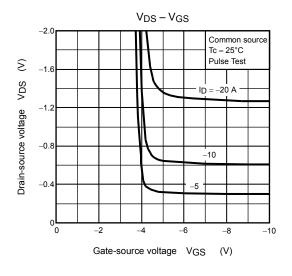
Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

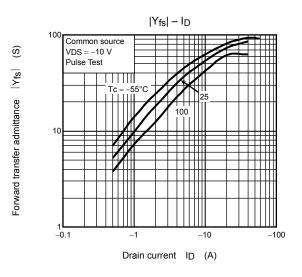
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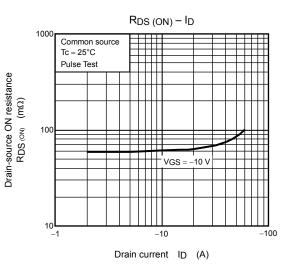




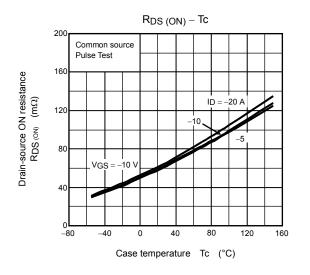


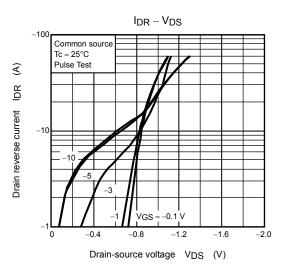


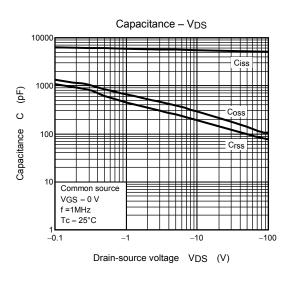


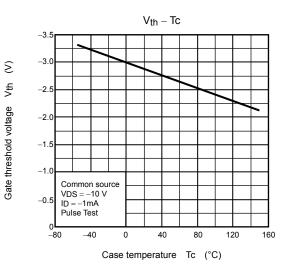


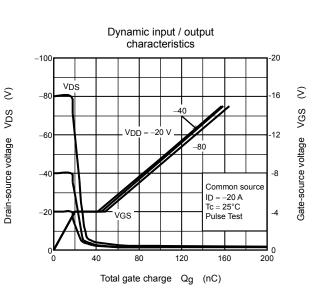
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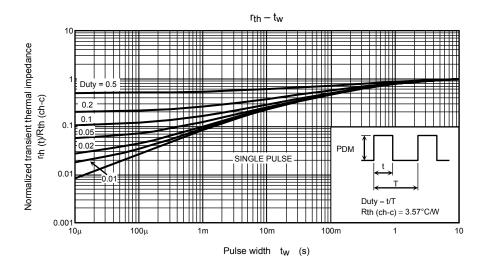




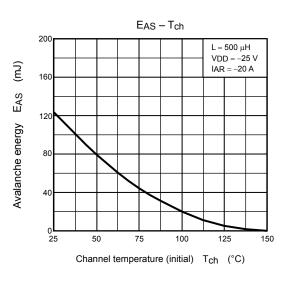


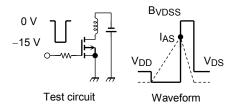
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2018-06-01



SAFE OPERATING AREA -100 ID max (pulse) \* 100 μ 1 m € -10 Drain current ID \* Single pulse Tc=25°C Curves must be derated linearly with increase in VDSS temperature. -0.1 -10 -0.1 -1 -100 -1000 Drain-source voltage VDS (V)





$RG = 25 \Omega$	$E_{AO} = \frac{1}{2}   _{1}   _{2}^{2}$	$\left(\frac{BVDSS}{BVDSS}-VDD\right)$		
$V_{DD}=-25~V,~L=500~\mu H$	LAS 2	(BVDSS-VDD)		

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