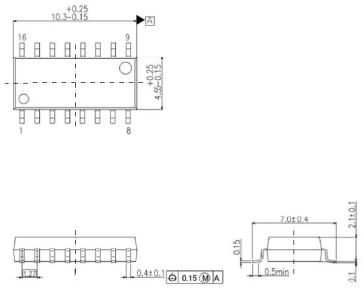
TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRANSISTOR

TLP290-4

Programmable Controllers
Power Supplies

Unit in mm



TOSHIBA 11-11F1

Weight: 0.19 g (typ.)

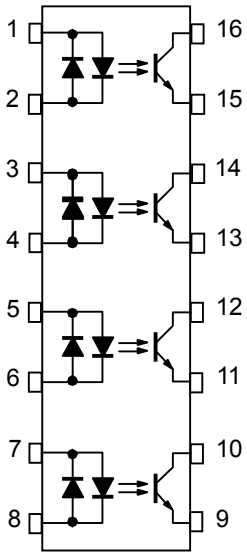
The Toshiba TLP290-4 consists of photo transistor, optically coupled to a gallium arsenide infrared emitting diode. TLP290-4 is housed in the SO16 package, very small and thin coupler.
Since TLP290-4 are guaranteed wide operating temperature (Ta=-55 to 110 ˚C), it's suitable for high-density surface mounting applications such as programmable controllers and hybrid ICs.

- Collector-Emitter Voltage : 80 V (min)
- Current Transfer Ratio : 50% (min)
  Rank GB : 100% (min)
- Isolation Voltage : 2500 Vrms (min)
- Guaranteed performance over -55 to 110 ˚C
- UL approved : UL1577 , File No. E67349
  cUL approved : CSA Component Acceptance Service No.5A
- Option (V4) type
  VDE approved : EN60747-5-2
  (Note) : When a EN60747-5-2 approved type is needed,
  Please designate "Option(V4)"

- Construction mechanical rating
  Creepage distance : 5.0 mm(min)
  Clearance : 5.0 mm(min)
  Insulation thickness : 0.1 mm(min)

Pin Configuration

TLP290-4



1,3,5,7 :ANODE, CATHODE
2,4,6,8 :CATHODE, ANODE
9,11,13,15 :EMITTER
10,12,14,16 :COLLECTOR

Current Transfer Ratio

TYPE	Classification (Note1)	Current Transfer Ratio (%) (I_C / I_F) I_F = 5 mA, V_{CE} = 5 V, Ta = 25°C		Marking of Classification
		Min	Max	
TLP290-4	Blank	50	400	Blank
	Rank GB	100	400	GB

Note1: ex. Rank GB: TLP290-4 (GB)

Application type name for certification test, please use standard product type name, i.e. TLP290–4 (GB,E: TLP290-4

Absolute Maximum Ratings (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT
LED	Forward Current	$I_{F(RMS)}$	±50	mA
	Forward Current Derating	ΔI_F /°C	−0.67 (Ta≥50°C)	mA /°C
	Pulse Forward Current (Note2)	I_{FP}	±1	A
	Junction Temperature	T_j	125	°C
DETECTOR	Collector-Emitter Voltage	V_{CEO}	80	V
	Emitter-Collector Voltage	V_{ECO}	7	V
	Collector Current	I_C	50	mA
	Collector Power Dissipation (1 Circuit)	P_C	100	mW
	Collector Power Dissipation Derating(Ta≥25°C) (1 Circuit)	ΔP_C /°C	−1.0	mW /°C
	Junction Temperature	T_j	125	°C
Operating Temperature Range		T_{opr}	−55 to 110	°C
Storage Temperature Range		T_{stg}	−55 to 125	°C
Lead Soldering Temperature		T_{sol}	260 (10s)	°C
Total Package Power Dissipation (1 Circuit)		P_T	170	mW
Total Package Power Dissipation Derating (Ta≥25°C) (1 Circuit)		ΔP_T /°C	−1.7	mW /°C
Isolation Voltage (Note3)		BV_S	2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook (“Handling Precautions”/“Derating Concept and Methods”) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note2: Pulse width ≤ 100μs, frequency 100Hz

Note3: AC, 1 minute, R.H.≤60%, Device considered a two terminal device : LED side pins shorted together and DETECTOR side pins shorted together.

Individual Electrical Characteristics (Ta = 25°C)

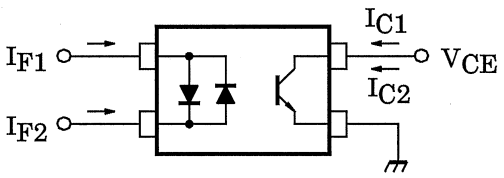
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
LED	Forward Voltage	V_F	I_F = 10 mA	1.1	1.20	1.4	V
	Capacitance	C_T	V = 0, f = 1 MHz	—	30	—	pF
DETECTOR	Collector-Emitter Breakdown Voltage	$V_{(BR)\ CEO}$	I_C = 0.5 mA	80	—	—	V
	Emitter-Collector Breakdown Voltage	$V_{(BR)\ ECO}$	I_E = 0.1 mA	7	—	—	V
	Collector Dark Current (Note5)	I_{CEO}	V_{CE} = 48 V,	—	0.01	0.1	μA
			V_{CE} = 48 V, Ta = 85°C	—	2	50	μA
	Capacitance (Collector to Emitter)	C_{CE}	V = 0, f = 1 MHz	—	10	—	pF

Coupled Electrical Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Current Transfer Ratio	I_C / I_F	I_F = 5 mA, V_{CE} = 5 V	50	—	400	%
		Rank GB	100	—	400	
Saturated CTR	I_C / $I_{F\ (sat)}$	I_F = 1 mA, V_{CE} = 0.4 V	—	60	—	%
		Rank GB	30	—	—	
Collector-Emitter Saturation Voltage	$V_{CE\ (sat)}$	I_C = 2.4 mA, I_F = 8 mA	—	—	0.4	V
		I_C = 0.2 mA, I_F = 1 mA	—	0.2	—	
		Rank GB	—	—	0.4	
Off-State Collector Current	$I_{C\ (off)}$	V_F = 0.7 V, V_{CE} = 48 V	—	—	10	μA
CTR symmetry	I_C (ratio)	I_C (I_F = −5 mA) / I_C (I_F = 5 mA) Note5	0.33	—	3	—

Note5:

$$I_{C(ratio)} = \frac{I_{C2}(I_F = I_{F2}, V_{CE} = 5V)}{I_{C1}(I_F = I_{F1}, V_{CE} = 5V)}$$



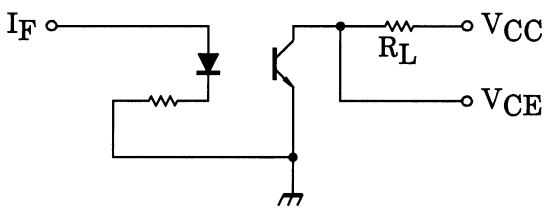
Isolation Characteristics (Ta = 25°C)

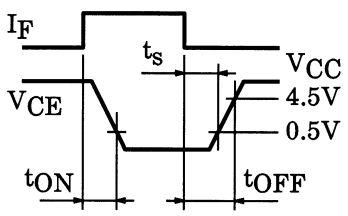
CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Capacitance (Input to Output)	C_S	V_S = 0 V, f = 1 MHz	—	0.8	—	pF
Isolation Resistance	R_S	V_S = 500 V, R.H.≤60%	1×10^{12}	10^{14}	—	Ω
Isolation Voltage	BV_S	AC , 1 minute	2500	—	—	Vrms
		AC , 1 second, in OIL	—	5000	—	
		DC , 1 minute, in OIL	—	5000	—	Vdc

Switching Characteristics (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
Rise Time	t_r	V_{CC} = 10 V, I_C = 2 mA R_L = 100Ω	—	2	—	μs
Fall Time	t_f		—	3	—	
Turn-On Time	t_{on}		—	3	—	
Turn-Off Time	t_{off}		—	3	—	
Turn-On Time	t_{ON}	R_L = 1.9 kΩ (Fig.1) V_{CC} = 5 V, I_F = 16 mA	—	2	—	μs
Storage Time	t_s		—	25	—	
Turn-Off Time	t_{OFF}		—	40	—	

(Fig.1) Switchin Time Test Circuit





Soldering and Storage

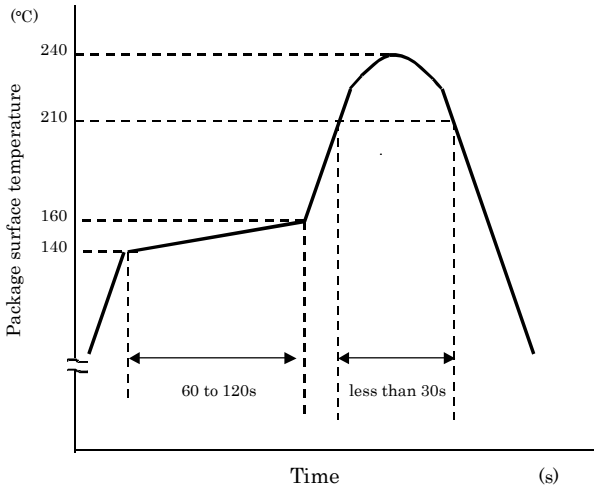
1. Soldering

1.1 Soldering

When using a soldering iron or medium infrared ray/hot air reflow, avoid a rise in device temperature as much as possible by observing the following conditions.

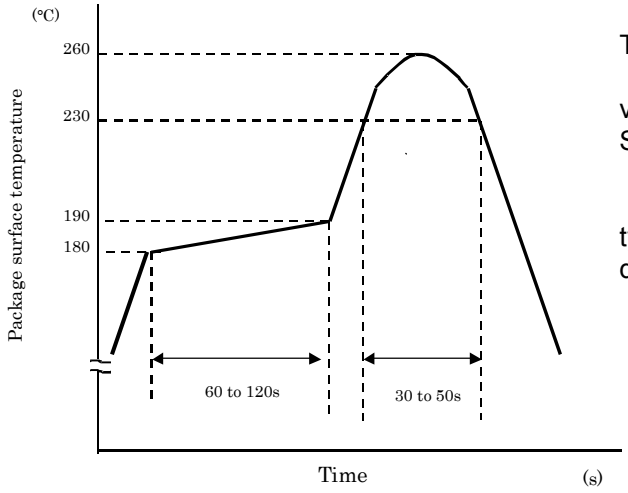
1) Using solder reflow

·Temperature profile example of lead (Pb) solder



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

·Temperature profile example of using lead (Pb)-free solder



This profile is based on the device's maximum heat resistance guaranteed value.
Set the preheat temperature/heating temperature to the optimum temperature corresponding to the solder paste type used by the customer within the described profile.

Reflow soldering must be performed once or twice.

The mounting should be completed with the interval from the first to the last mountings being 2 weeks.

2) Using solder flow (for lead (Pb) solder, or lead (Pb)-free solder)

・Please preheat it at 150°C between 60 and 120 seconds.

・Complete soldering within 10 seconds below 260°C. Each pin may be heated at most once.

3) Using a soldering iron

Complete soldering within 10 seconds below 260°C, or within 3 seconds at 350°C. Each pin may be heated at most once.

2. Storage

1) Avoid storage locations where devices may be exposed to moisture or direct sunlight.
2) Follow the precautions printed on the packing label of the device for transportation and storage.
3) Keep the storage location temperature and humidity within a range of 5°C to 35°C and 45% to 75%, respectively.
4) Do not store the products in locations with poisonous gases (especially corrosive gases) or in dusty conditions.
5) Store the products in locations with minimal temperature fluctuations. Rapid temperature changes during storage can cause condensation, resulting in lead oxidation or corrosion, which will deteriorate the solderability of the leads.
6) When restoring devices after removal from their packing, use anti-static containers.
7) Do not allow loads to be applied directly to devices while they are in storage.
8) If devices have been stored for more than two years under normal storage conditions, it is recommended that you check the leads for ease of soldering prior to use.

Option:Specification for Embossed-Tape Packing (TP) for Mini-Flat Coupler

1. Applicable Package

Package Name	Product Type
SO16	Mini-Flat Coupler

2. Product Naming System

Type of package used for shipment is denoted by a symbol suffix after a product number. The method of classification is as below.

(Example)

TLP290－4 (GB－TP, E

- [[G]]/RoHS COMPATIBLE (Note6)
- Tape type
- CTR rank
- Device name

3. Tape Dimensions

3.1 Orientation of Device in Relation to Direction of Tape Movement

Device orientation in the recesses is as shown in Figure 1.

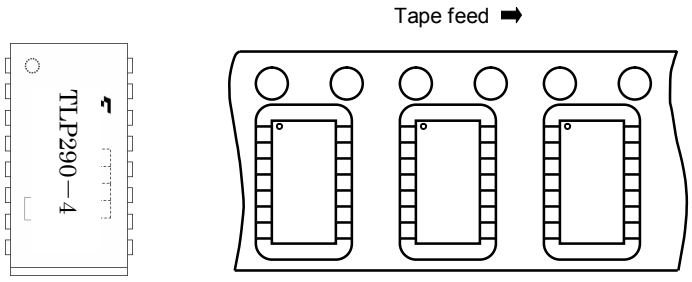


Figure 1 Device Orientation

3.2 Tape Packing Quantity : 2000 devices per reel

3.3 Empty Device Recesses are as Shown in Table 1.

Table 1 Empty Device Recesses

	Standard	Remarks
Occurrences of 2 or more successive empty device recesses	0	Within any given 40-mm section of tape, not including leader and trailer
Single empty device recesses	6 device (max) per reel	Not including leader and trailer

3.4 Start and End of Tape

The start of the tape has 50 or more empty holes. The end of tape has 50 or more empty holes and two empty turns only for a cover tape.

3.5 Tape Specification

(1) Tape material: Plastic (protection against electrostatics)
(2) Dimensions: The tape dimensions are as shown in Figure 2 and table 2.

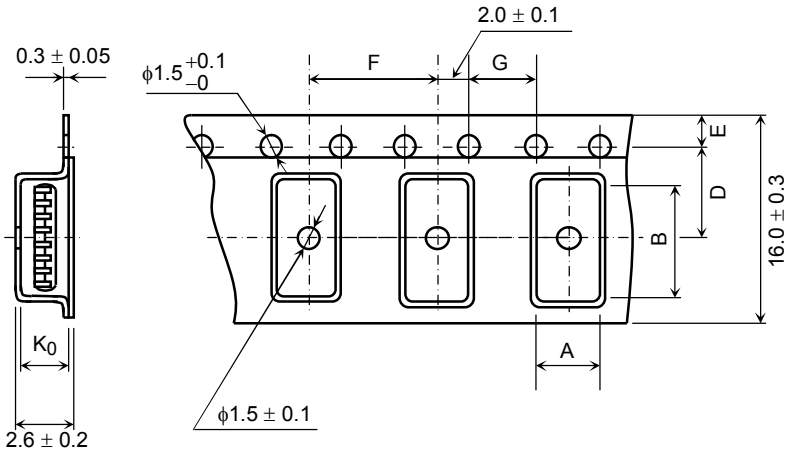


Figure2 Tape Forms

Table2 Tape Dimensions

Unit: mm
Unless otherwise specified: ±0.1

Symbol	Dimension	Remark
A	7.5	—
B	10.5	—
D	7.5	Center line of indented square hole and sprocket hole
E	1.75	Distance between tape edge and hole center
F	12.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
G	4.0	Cumulative error $^{+0.1}_{-0.3}$ (max) per 10 feed holes
K_0	2.2	Internal space

3.6 Reel

(1) Material: Plastic

(2) Dimensions: The reel dimensions are as shown in Figure 3 and Table 3.

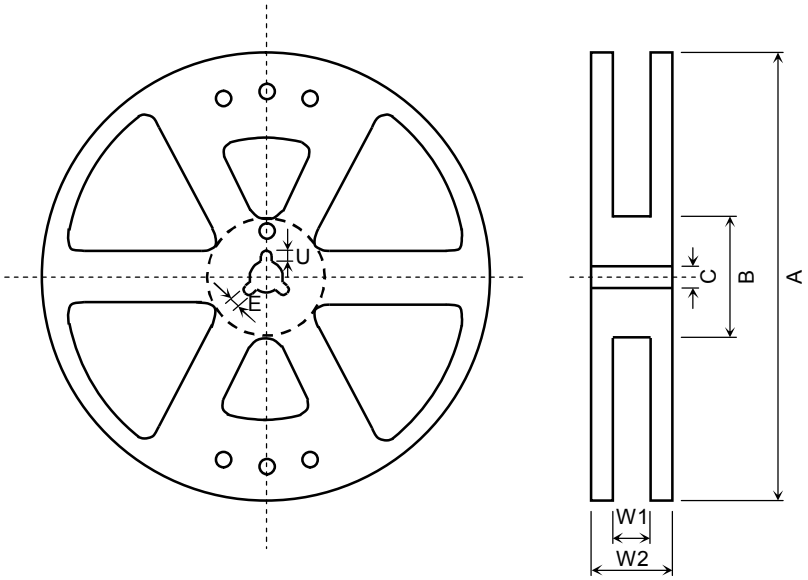


Figure 3 Reel Forms

Table 3 Reel Dimensions

Unit: mm

Symbol	Dimension
A	ϕ330 ± 2
B	ϕ80 ± 1
C	ϕ13 ± 0.5
E	2.0 ± 0.5
U	4.0 ± 0.5
W1	17.5 ± 0.5
W2	21.5 ± 1.0

4. Packing

Either one reel or five reels of photocouplers are packed in a shipping carton.

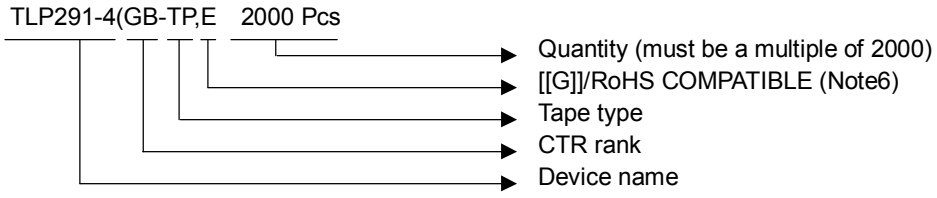
5. Label Indication

The carton bears a label indicating the product number, the symbol representing classification of standard, the quantity, the lot number and the Toshiba company name.

6. Ordering Method

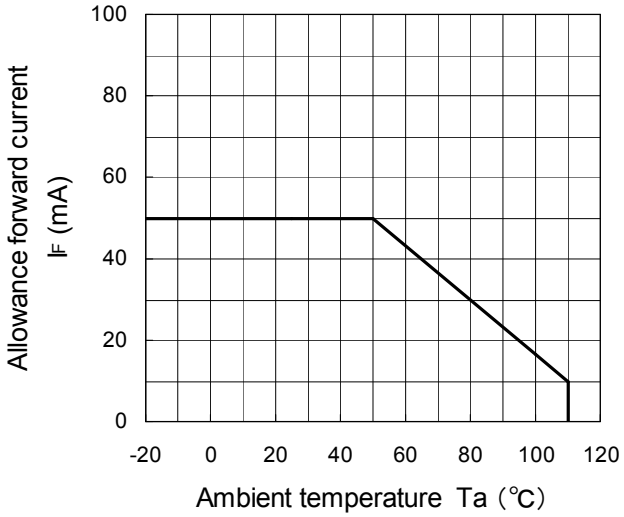
When placing an order, please specify the product number, the CTR rank, the tape type and the quantity as shown in the following example.

(Example)

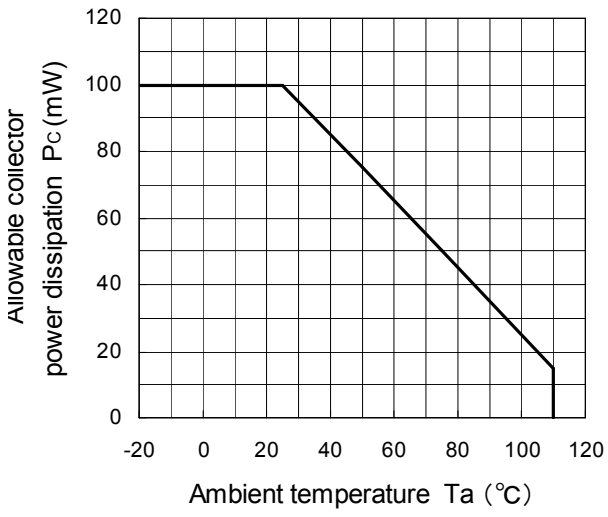


Note6: Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
The RoHS is the Directive 2002/95/EC of the European Parliament and of the Council of 27 January 2003 on the restriction of the use of certain hazardous substances in electrical and electronics equipment.

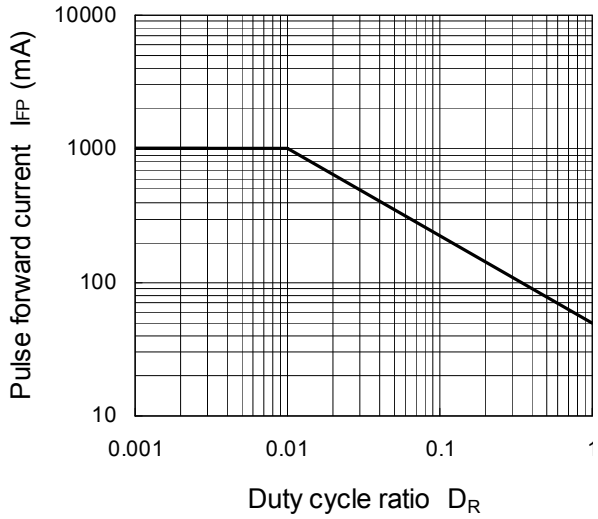
I_F – Ta



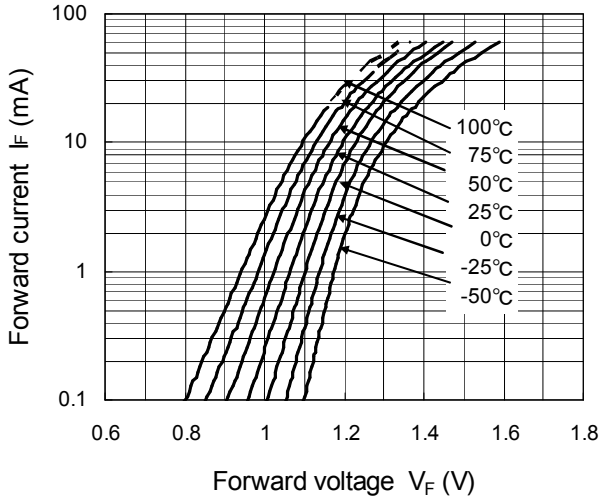
P_C - Ta



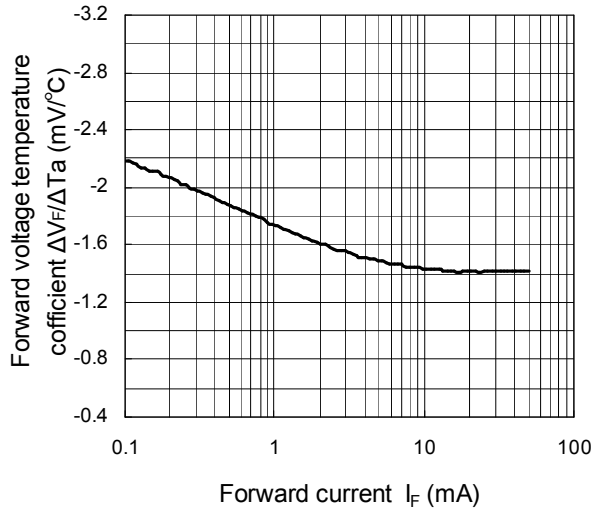
I_{FP} – D_R



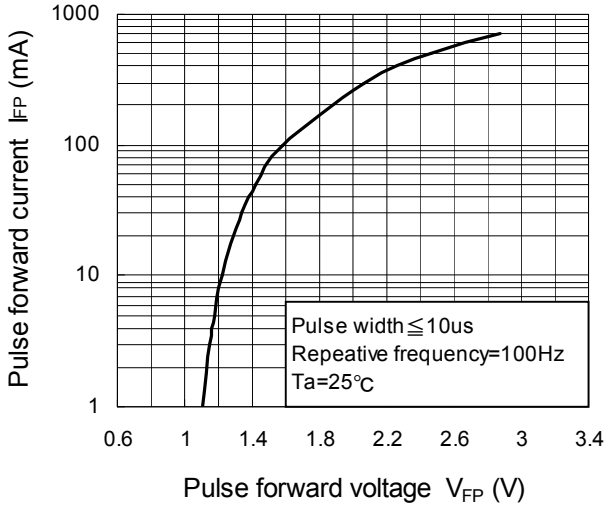
I_F - V_F



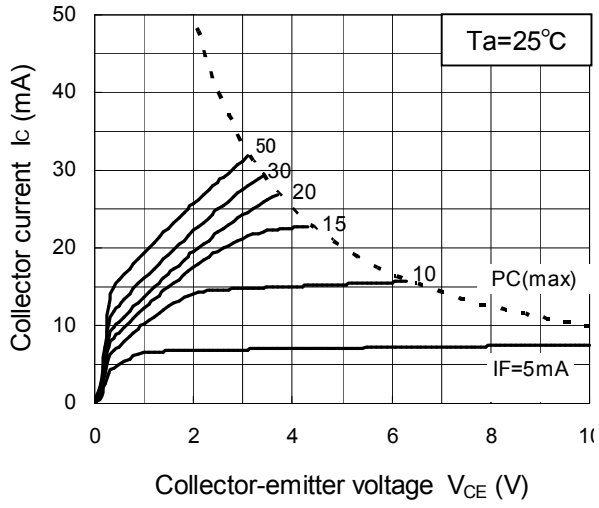
$\Delta V_F/\Delta Ta$ – IF



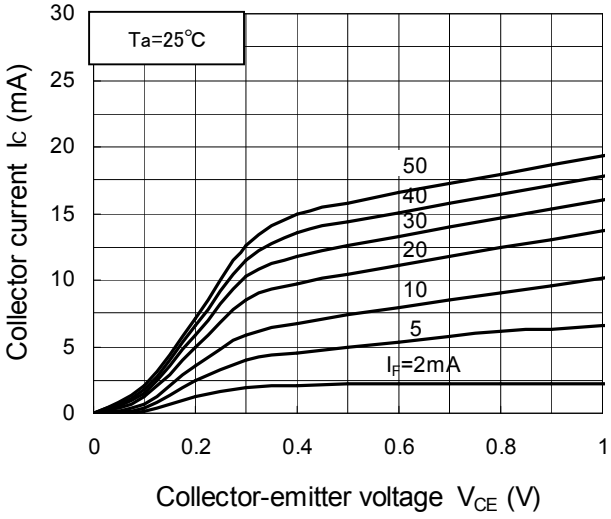
I_{FP} – V_{FP}



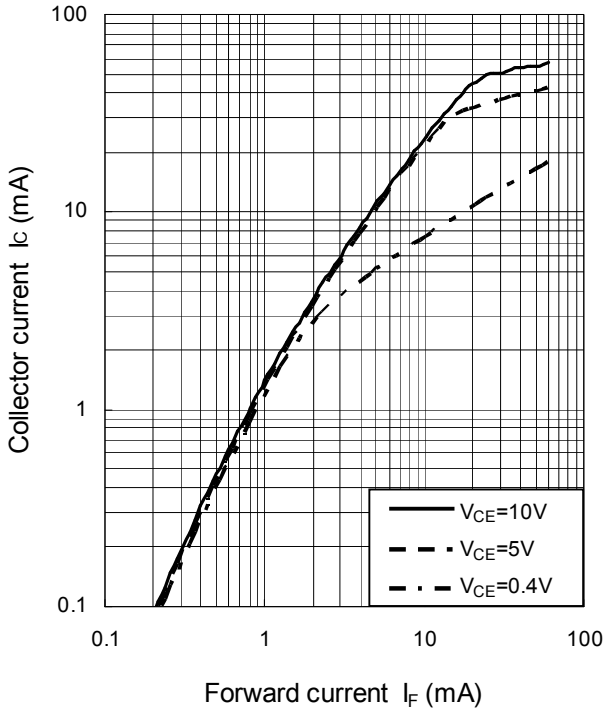
$I_C - V_{CE}$



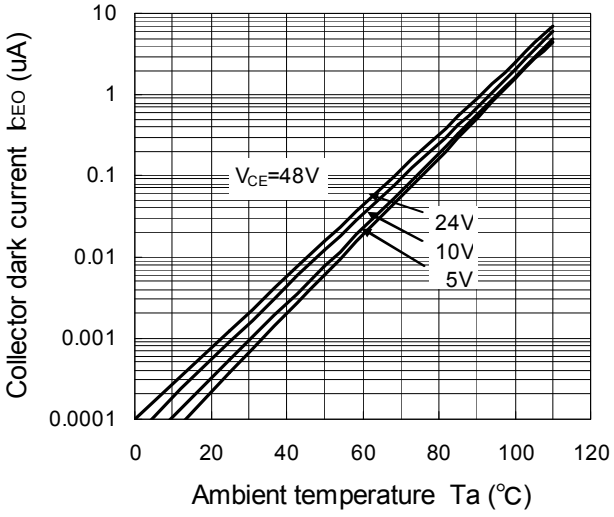
$I_C - V_{CE}$



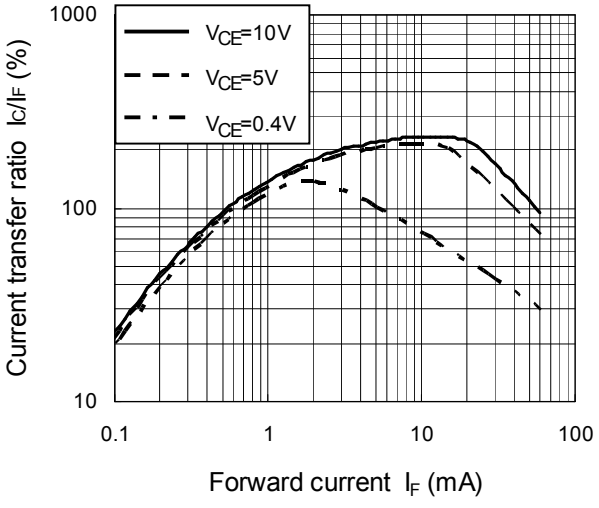
$I_C - I_F$



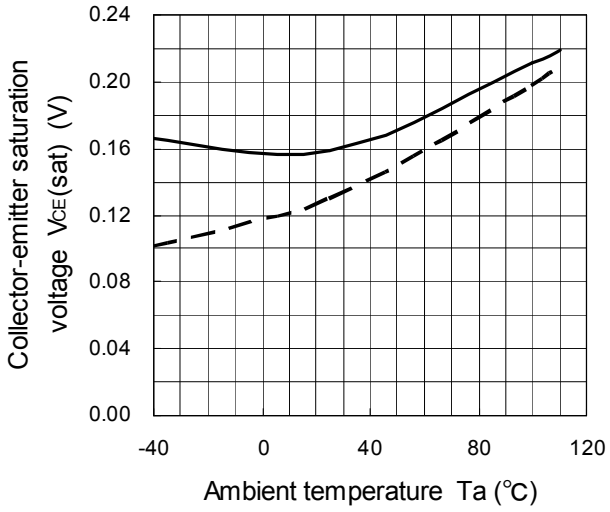
$I_{CEO} - Ta$



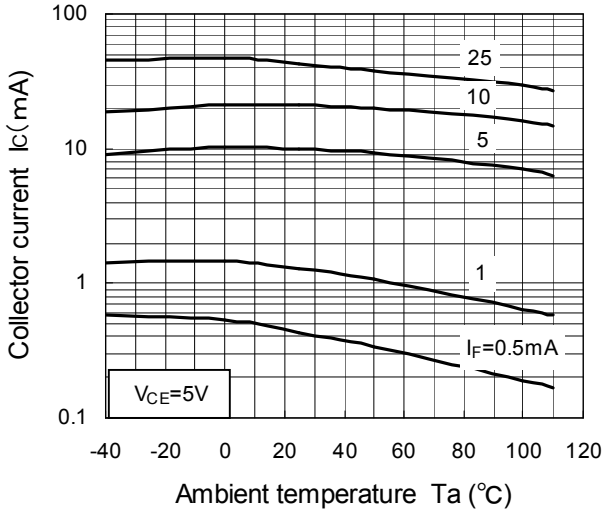
I_C/I_F - I_F



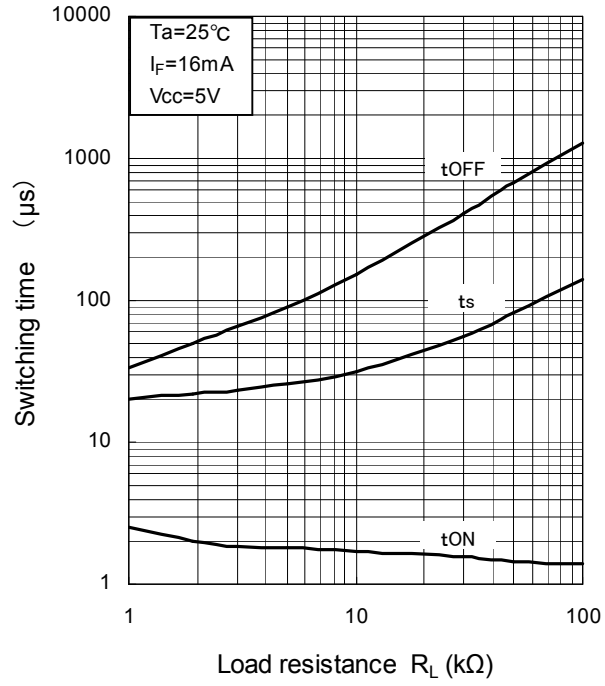
V_{CE}(sat) - Ta



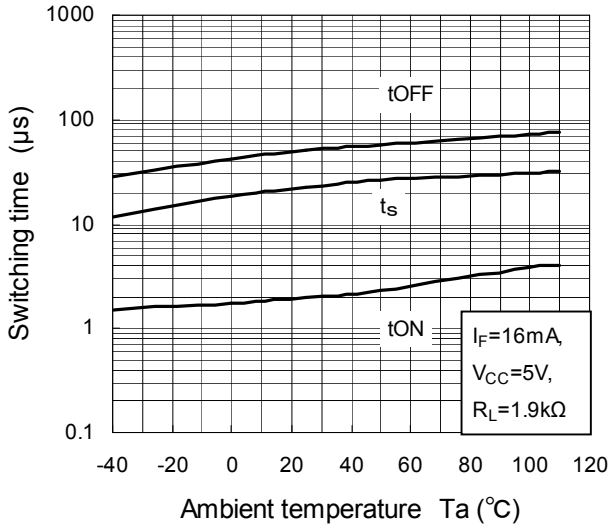
I_C-Ta



Switchingtime − R_L



Switching time−Ta



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