

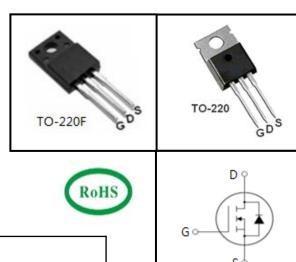
650V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information				
Device	Package	Marking		
TMA10N65H	TO-220F	A10N65H		
TMP10N65H	TO-220	P10N65H		

Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Parameter		Complete	Value		
Parameter		Symbol	TO-220F	TO-220	Unit
Drain-Source Voltage (V _{GS} = 0V)		V _{DSS}	65	50	V
Continuous Drain Current		I _D	10		А
Pulsed Drain Current	(note1)	I _{DM}	38		А
Gate-Source Voltage		V _{GSS}	±30		V
Single Pulse Avalanche Energy	(note2)	E _{AS}	562		mJ
Avalanche Current	(note1)	I _{AR}	7.5		А
Repetitive Avalanche Energy	(note1)	E _{AR}	45		mJ
Power Dissipation (T _C = 25°C)		P _D	65	147	W
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55~	+150	∘C

Thermal Resistance					
Borometer	Cumbal	Value		l lm!4	
Parameter	Symbol	TO-220F	TO-220	Unit	
Thermal Resistance, Junction-to-Case	R_{thJC}	1.92	0.85	00.00	
Thermal Resistance, Junction-to-Ambient	R_{thJA}	62.5	60	°C/W	



D			Value					
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit		
Static								
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	650			V		
Zara Cata Valtara Drain Current		$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	1 100 μΑ		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 520V, V_{GS} = 0V, T_{J} = 125^{\circ}C$	1		100			
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA		
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V		
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_{D} = 5A$	-	0.65	0.8	Ω		
Dynamic								
Input Capacitance	C _{iss}	$V_{GS} = 0V$,		1264				
Output Capacitance	C _{oss}	$V_{DS} = 25V$,		149		pF		
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		18				
Total Gate Charge	Q_g			35				
Gate-Source Charge	Q_{gs}	$V_{DD} = 520V, I_{D} = 10.0A,$ $V_{GS} = 10V$		7		nC		
Gate-Drain Charge	Q_{gd}			18				
Turn-on Delay Time	t _{d(on)}			23				
Turn-on Rise Time	t _r	$V_{DD} = 325V, I_{D} = 10A,$		15		20		
Turn-off Delay Time	t _{d(off)}	$V_{DD} = 325V, I_{D} = 10A,$ $R_{G} = 25 \Omega$		90		ns -		
Turn-off Fall Time	t _f			30				
Drain-Source Body Diode Character	istics							
Continuous Body Diode Current	Is	T 05.00			10	۸		
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			38	Α		
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 10A$, $V_{GS} = 0V$			1.4	V		
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_{S} = 10A,$		320		ns		
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /μs		4.2		μC		

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} = 7.5A, V_{DD} = 50V, R_{G} = 25 Ω , Starting T_{J} = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted

Figure 1. Output Characteristics ($T_J = 25^{\circ}C$)

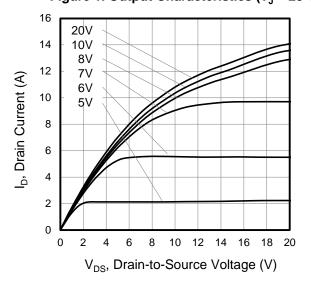


Figure 3. Drain Current vs. Temperature

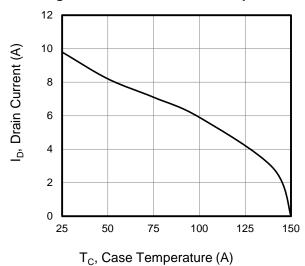


Figure 5. Transfer Characteristics

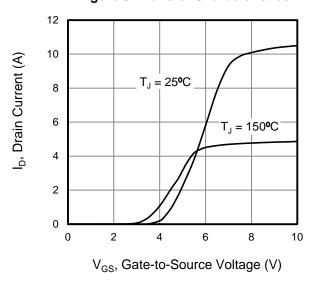


Figure 2. Body Diode Forward Voltage

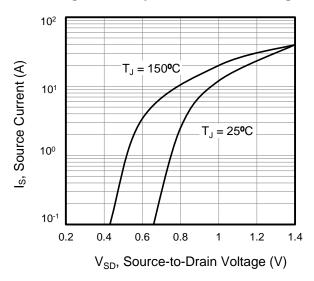
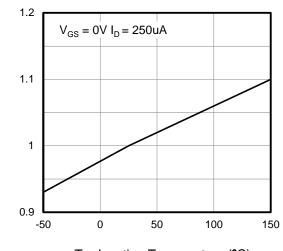
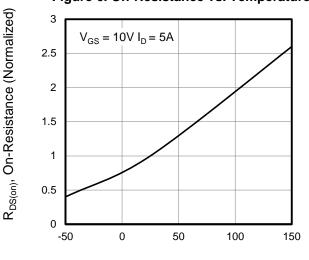


Figure 4. BV_{DSS} Variation vs. Temperature



T_J, Junction Temperature (°C)

Figure 6. On-Resistance vs. Temperature



T_J, Junction Temperature (°C)

BV_{DSS} (Normalized)



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

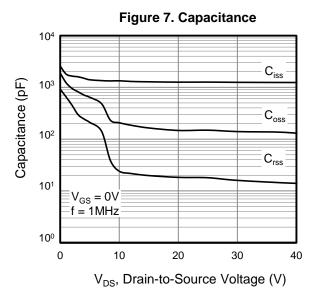
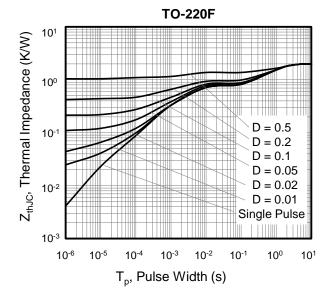


Figure 9. Transient Thermal Impedance



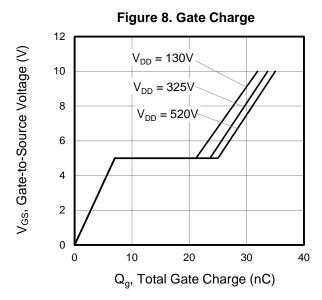


Figure 9. Transient Thermal Impedance

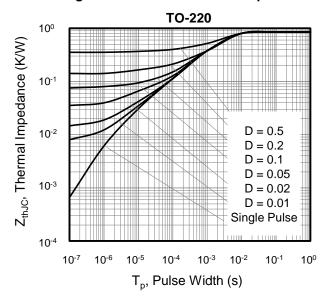




Figure A: Gate Charge Test Circuit and Waveform

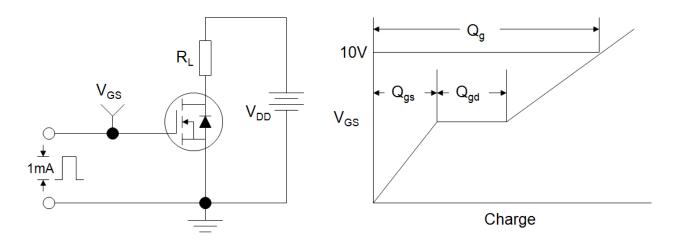


Figure B: Resistive Switching Test Circuit and Waveform

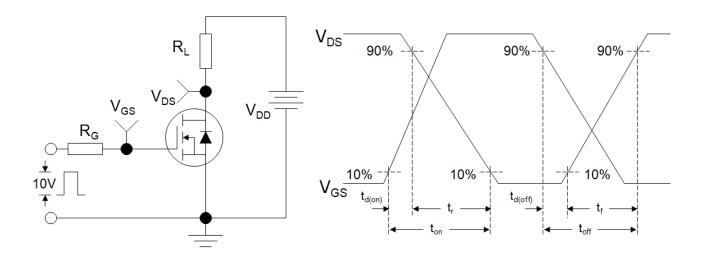
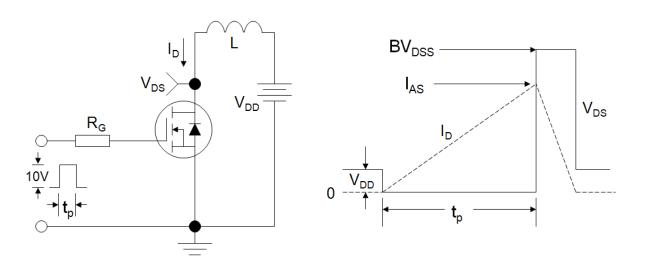
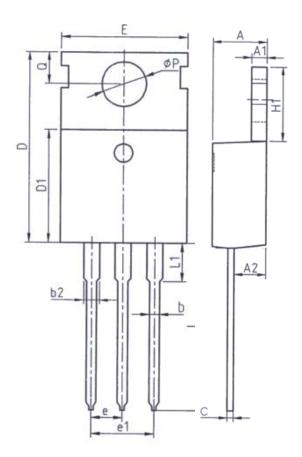
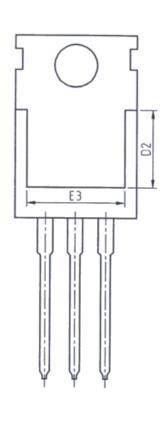


Figure C: Unclamped Inductive Switching Test Circuit and Waveform



TO-220



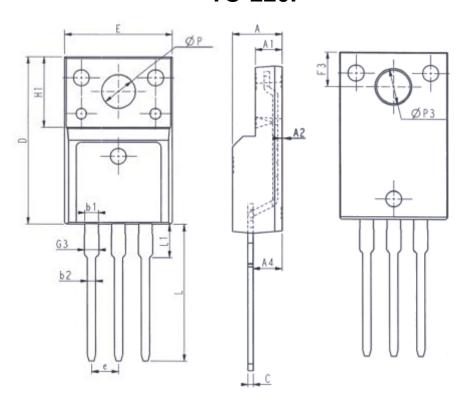


Unit: mm				
Symbol	Min.	Max.		
Α	4. 37	4. 77		
A1	1. 25	1. 45		
A2	2. 20	2. 60		
ь	0. 70	0. 95		
b2	1. 17	1. 47		
С	0. 40	0. 65		
D	15. 10	16. 10		
D1	8. 80	9. 40		
D2	5. 50	_		

Unit: mm				
Symbol	Min.	Max.		
E	9. 70	10. 30		
E3	7. 00	-		
е	2. 54BSC			
e1	5. 08BSC			
H1	6. 25	6. 85		
L	12. 75	13.80		
L1	_	3. 40		
P	3. 40	3. 80		
Q	2. 60	3. 00		



TO-220F



Unit: mm			Unit: mm			
Symbol	Min.	Max.	Symbol	Min.	Max.	
E	9.96	10.36	L	12. 68	13. 28	
Α	4. 50	4. 90	L1	2. 93	3. 13	
A1	2. 34	2. 74	Р	3. 03	3. 38	
A2	0. 30	0.60	Р3	3. 15	3. 65	
A4	2. 56	2. 96	F3	3. 15	3. 45	
С	0.40	0. 65	G3	1. 25	1. 55	
D	15. 57	16. 17	b1	1. 18	1. 43	
H1	6. 70REF		b2	0. 70	0. 95	
e	2. 54BSC					



Disclaimer

All product specifications and data are subject to change without notice.

For documents and material available from this datasheet, Wuxi Tongfang does not warrant or assume any legal liability or responsibility for the accuracy, completeness of any product or technology disclosed hereunder.

No license, express or implied, by estoppels or otherwise, to any intellectual property rights is granted by this document or by any conduct of Wuxi Tongfang.

The products shown herein are not designed for use in medical, life-saving, or life-sustaining applications unless. Customers using or selling Wuxi Tongfang products not expressly indicated for use in such applications do so entirely at their own risk and agree to fully indemnify Wuxi Tongfang for any damages arising or resulting from such use or sale.

Wuxi Tongfang disclaims any and all liability arising out of the use or application of any product described herein or of any information provided herein to the maximum extent permitted by law. The product specifications do not expand or otherwise modify Wuxi Tongfang's terms and conditions of purchase, including but not limited to the warranty expressed therein, which apply to these products.

Wuxi Tongfang Microelectronics CO., LTD. strives to supply high-quality high-reliability products. However, any and all semiconductor products fail with some probability. It is possible that these probabilistic failures could give rise to accidents or events that could endanger human lives that could give rise to smoke or fire, or that could cause damage to other property. When designing equipment, adopt safety measures so that these kinds of accidents or events cannot occur. Such measures include but are not limited to protective circuits and error prevention circuits for safe design, redundant design, and structural design.

In the event that any or all Wuxi Tongfang products (including technical data, services) described or contained herein are controlled under any of applicable local export control laws and regulations, such products must not be exported without obtaining the export license from the authorities concerned in accordance with the above law.

Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. Wuxi Tongfang believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.