TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSVI)

TPC8123

Lithium Ion Battery Applications Power Management Switch Applications

- Small footprint due to small and thin package
- Low drain-source ON-resistance: $R_{DS (ON)} = 7.0 \text{ m}\Omega$ (typ.)
- High forward transfer admittance: |Y_{fS}| = 36 S (typ.)
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement mode: V_{th} = -0.8 to -2.0 V (V_{DS} = -10 V, I_D = -0.5 mA)

Absolute Maximum Ratings (Ta = 25°C)

Characteris	stics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-30	> ∨
Drain-gate voltage (Ro	$_{\rm SS} = 20 \; \rm k\Omega)$	V_{DGR}	-30	V
Gate-source voltage		V _{GSS}	-25/+20	V
Drain current	DC (Note 1)	ID	-11	A
Drain current	Pulse (Note 1)	I _{DP}	44	NA.
Drain power dissipation	n (t = 10 s) (Note 2a)	PD	1.9	w
Drain power dissipation	n (t = 10 s) (Note 2b)	PD	1.0	/w
Single pulse avalanche	e energy (Note 3)	Eas	79	mJ
Avalanche current	(Note 1)) _{AR}	(-11)/	Α
Repetitive avalanche e	energy ote 2a) (Note 4)	E _{AR}	0.04	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature ra	ange	T _{stg}	-55 to 150	°C

Note 1, Note 2, Note 3 and Note 4: See the next page.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly

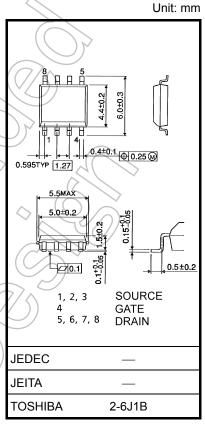
ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling

even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum

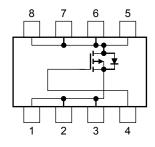
Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

This transistor is an electrostatic-sensitive device. Handle with care.



Weight: 0.080 g (typ.)

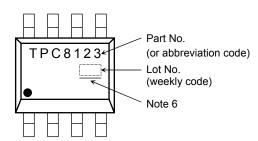
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

Marking (Note 5)

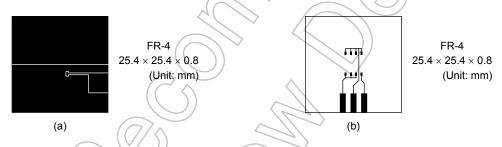


Note 6: A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product. The RoHS is Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)



Note 3: $V_{DD} = -24 \text{ V}$, $T_{ch} = 25 ^{\circ}\text{C}$ (initial), $L = 500 \mu\text{H}$, $R_G \neq 25 \Omega$, $I_{AR} = -11 \text{ A}$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.

Weekly code: (Three digits)

Week of manufacture

(01 for the first week of a year: sequential number up to 52 or 53)

Year of manufacture (The last digit of a year)

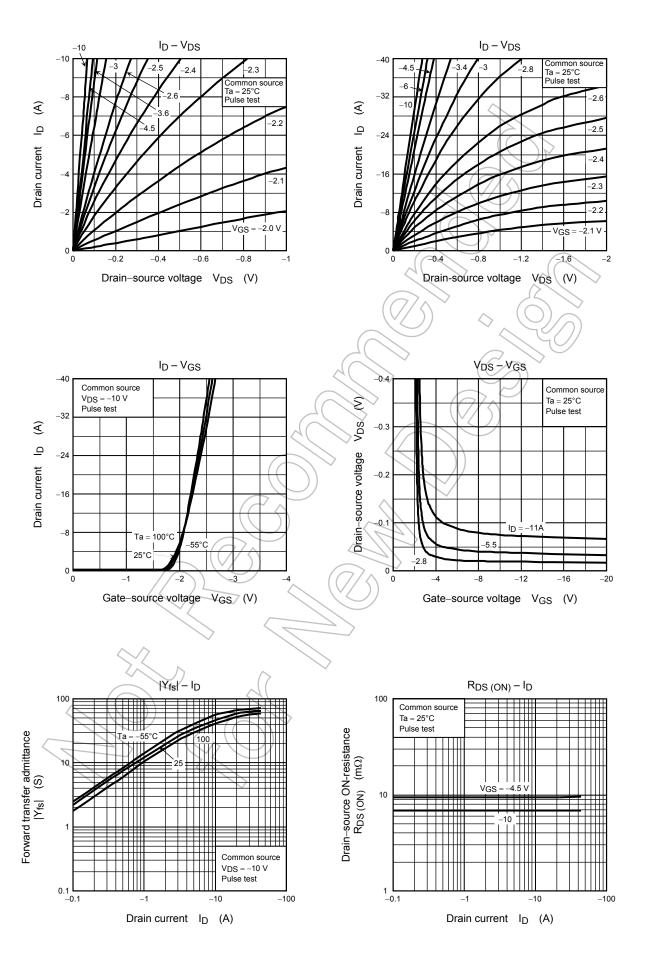
Electrical Characteristics (Ta = 25°C)

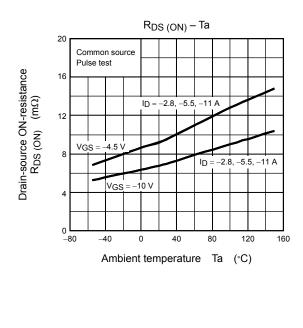
Chara	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage currer	nt	I _{GSS}	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±100	nA	
Drain cut-OFF curre	ent	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА	
Drain gourge brookdown voltage		V (BR) DSS	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30			V	
Dialii-Source break	Drain-source breakdown voltage		$I_D = -10 \text{ mA}, V_{GS} = 10 \text{ V (Note 7)}$	-21	1			
Gate threshold volta	ige	V_{th}	$V_{DS} = -10 \text{ V}, I_D = -0.5 \text{ mA}$	0.8) >_	-2.0	>	
Drain-source ON-resistance		R _{DS} (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -5.5 \text{ A}$) 	9.5	12.5	- mΩ	
			$V_{GS} = -10 \text{ V}, I_D = -5.5 \text{ A}$	\supset	7.0	9.0		
Forward transfer ad	Forward transfer admittance		$V_{DS} = -10 \text{ V}, I_D = -5.5 \text{ A}$	18	36		S	
Input capacitance		C _{iss}		_	2940			
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		460		pF	
Output capacitance		Coss		_	520	/		
	Rise time	t _r	V _{GS} 0 V	-	10			
Conitabilita e tima e	Turn-ON time	t _{on}			18	_		
Switching time	Fall time	t _f	4, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1, 1,	(\mathcal{E})	80	_	ns	
	Turn-OFF time	t _{off}	V _{DD} ≈ −15 V Duty ≤ 1%, t _w = 10 μs) —	250	_		
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ -24 V, V _{GS} = -10 V,	_	68	_		
Gate-source charge 1		Q _{gs1}	I _D = -11 A	_	7	_	nC	
Gate-drain ("miller") charge		Q_{gd}		_	18	_		

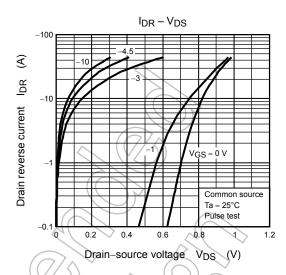
Source-Drain Ratings and Characteristics (Ta = 25°C)

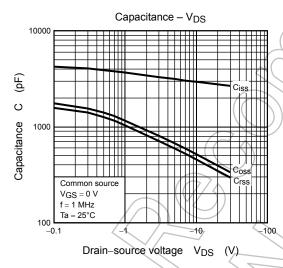
Charac	eteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	_	_	_	-44	Α
Forward voltage (dio	de)	VDSF	$I_{DR} = -11 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

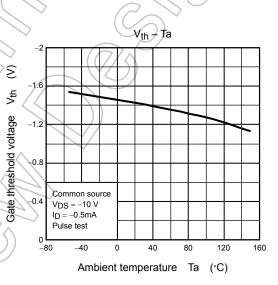
Note 7: VDSX mode (the application of a plus voltage between gate and source) may cause decrease in maximum rating of drain-source voltage.

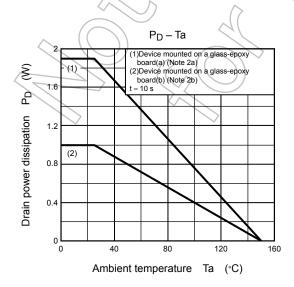


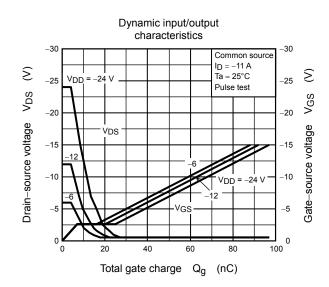




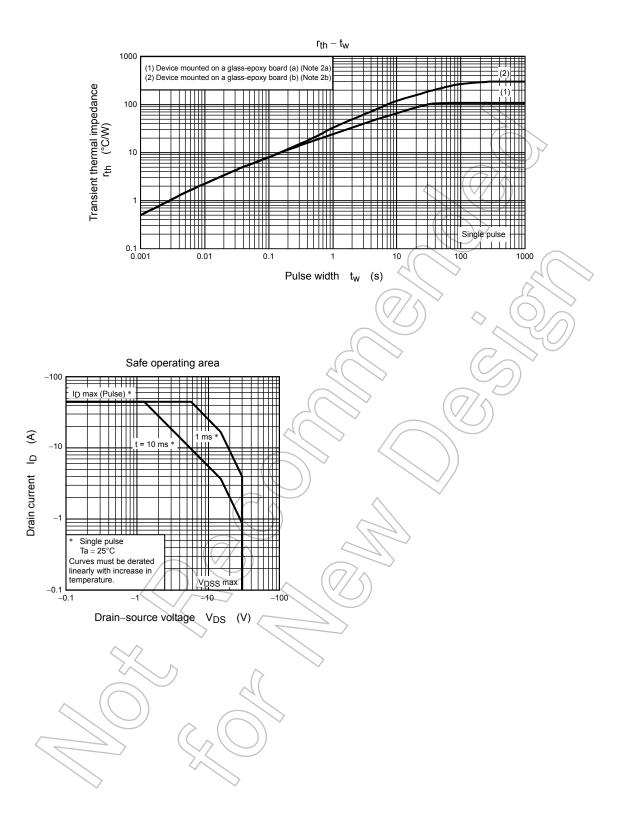








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