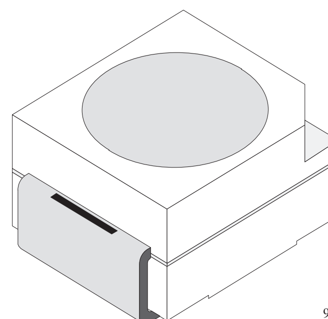
GaAs/GaAlAs Infrared Emitting Diode in SMT Package

Description

TSML3710 is an infrared emitting diode in GaAlAs on GaAs technology in miniature PLCC2 SMD package.



Features

- SMT IRED with extra high radiant power
- Low forward voltage
- Compatible with automatic placement equipment
- EIA and ICE standard package
- Suitable for infrared, vapor phase and wavesolder process
- Packed in 8 mm tape
- Suitable for pulse current operation
- Extra wide angle of half intensity $\varphi = \pm 60°$
- Peak wavelength $\lambda_p = 950$ nm
- Matched with TEMT3700 phototransistor

Applications

IR emitter in photointerrupters, transmissive sensors and reflective sensors

Household appliance

IR emitter in low space applications

Tactile keyboards

Parts Table

Part	Ordering code	Remarks
TSML3710	TSML3710-GS08	MOQ 7500pcs (1500 pcs per reel)

Absolute Maximum Ratings

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit
Reverse Voltage		V_R	5	V
Forward Current		I_F	100	mA
Peak Forward Current	$t_p/T = 0.5$, $t_p = 100$ µs	I_{FM}	200	mA
Surge Forward Current	$t_p = 100$ µs	I_{FSM}	1	A
Power Dissipation		P_V	170	mW
Junction Temperature		T_j	100	°C
Operating Temperature Range		T_{amb}	- 40 to + 85	°C
Storage Temperature Range		T_{stg}	- 40 to +100	°C
Soldering Temperature	t ≤10sec	T_{sd}	260	°C
Thermal Resistance Junction/Ambient		R_{thJA}	450	K/W

Basic Characteristics

T_{amb} = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Min	Typ.	Max	Unit
Forward Voltage	I_F = 100 mA, t_p = 20 ms	V_F		1.35	1.7	V
	I_F = 1 A, t_p = 100 µs	V_F		2.6	3.2	V
Temp. Coefficient of V_F	I_F = 1 mA	TK_{VF}		-1.85		mV/K
Reverse Current	V_R = 5 V	I_R			100	µA
Junction Capacitance	V_R = 0 V, f = 1 MHz, E = 0	C_j		25		pF
Radiant Intensity	I_F = 100 mA, t_p = 20 ms	I_e	4	8		mW/sr
	I_F = 1 A, t_p = 100 µs	I_e		60		mW/sr
Radiant Power	I_F = 100 mA, t_p = 20 ms	ϕ_e		35		mW
Temp. Coefficient of ϕ_e	I_F = 100 mA	$TK_{\phi e}$		-0.6		%/K
Angle of Half Intensity		φ		±60		deg
Peak Wavelength	I_F = 100 mA	λ_p		950		nm
Spectral Bandwidth	I_F = 100 mA	$\Delta\lambda$		50		nm
Temp. Coefficient of λ_p	I_F = 100 mA	$TK_{\lambda p}$		0.2		nm/K
Rise Time	I_F = 20 mA	t_r		800		ns
	I_F = 1 A	t_r		500		ns
Fall Time	I_F = 20 mA	t_f		800		ns
	I_F = 1 A	t_f		500		ns

Typical Characteristics (T_{amb} = 25 °C unless otherwise specified)

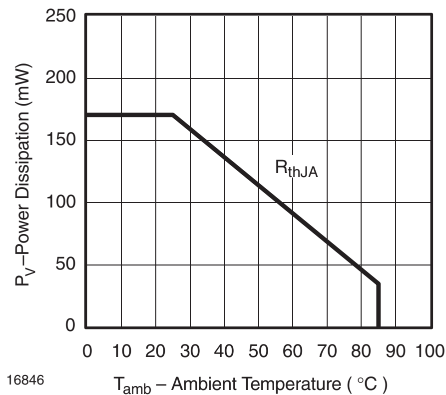


Figure 1. Power Dissipation vs. Ambient Temperature

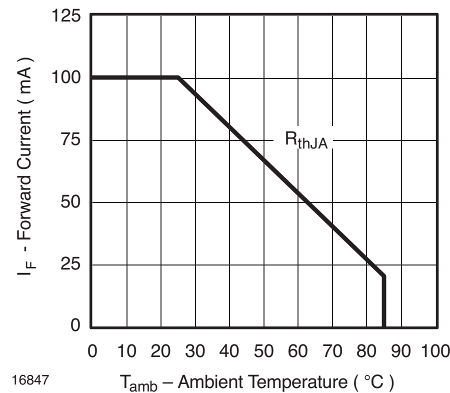


Figure 2. Forward Current vs. Ambient Temperature

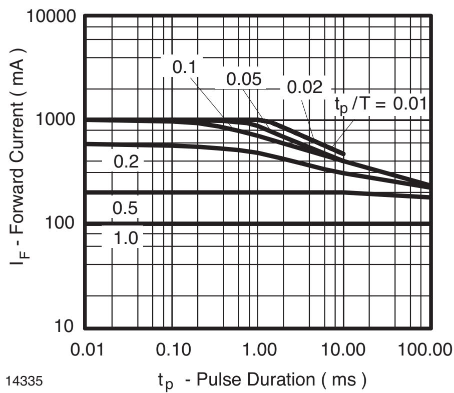


Figure 3. Pulse Forward Current vs. Pulse Duration

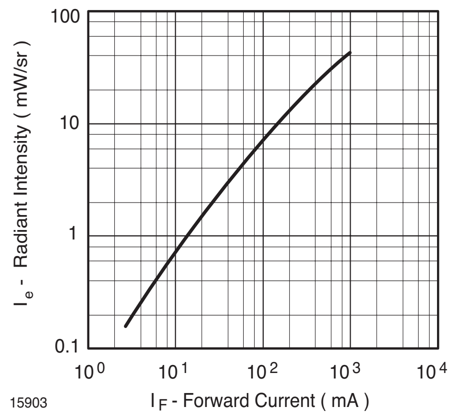


Figure 6. Radiant Intensity vs. Forward Current

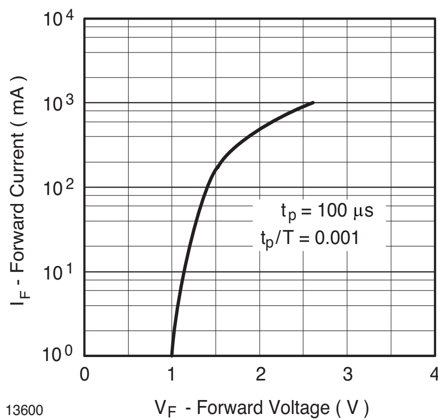


Figure 4. Forward Current vs. Forward Voltage

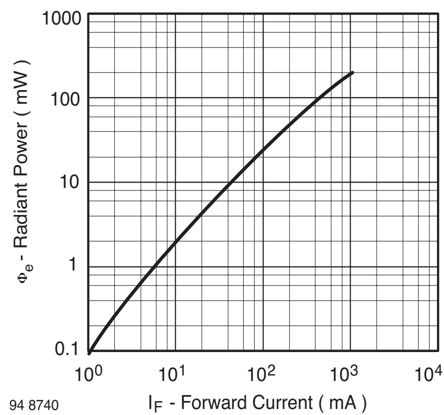


Figure 7. Radiant Power vs. Forward Current

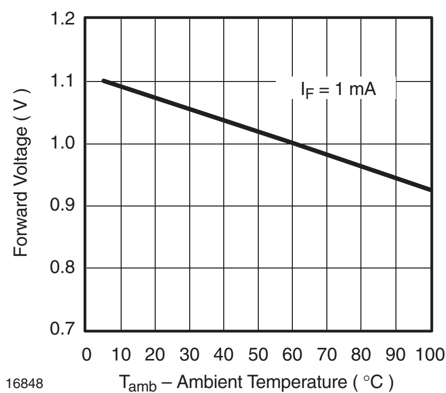


Figure 5. Forward Voltage vs. Ambient Temperature

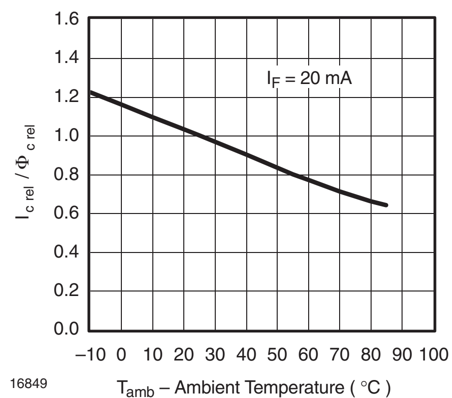


Figure 8. Rel. Radiant Intensity/Power vs. Ambient Temperature

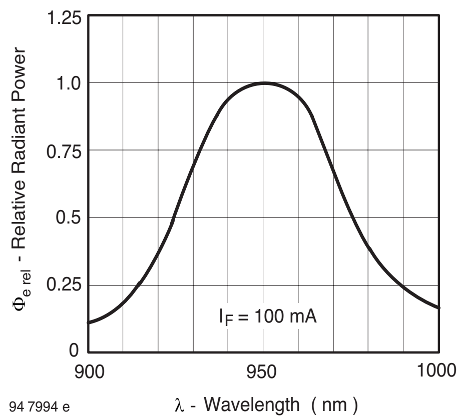


Figure 9. Relative Radiant Power vs. Wavelength

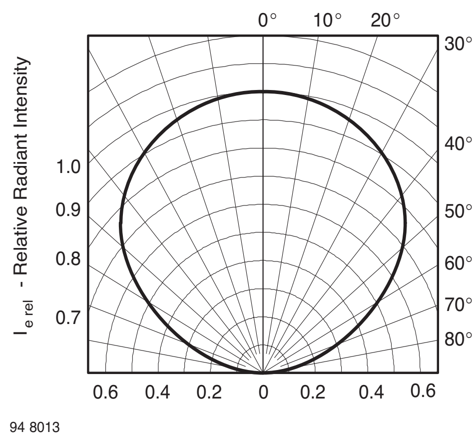
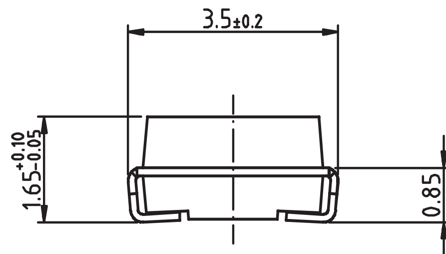
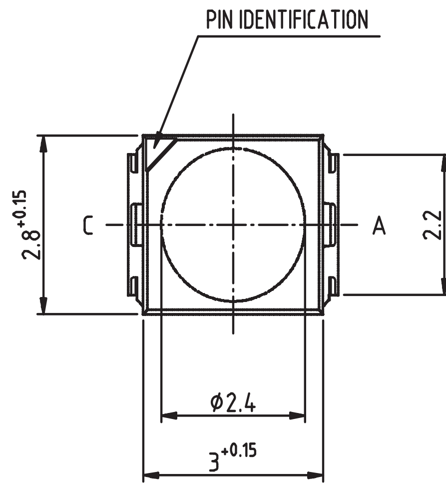


Figure 10. Relative Radiant Intensity vs. Angular Displacement

Package Dimensions in mm





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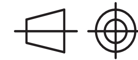
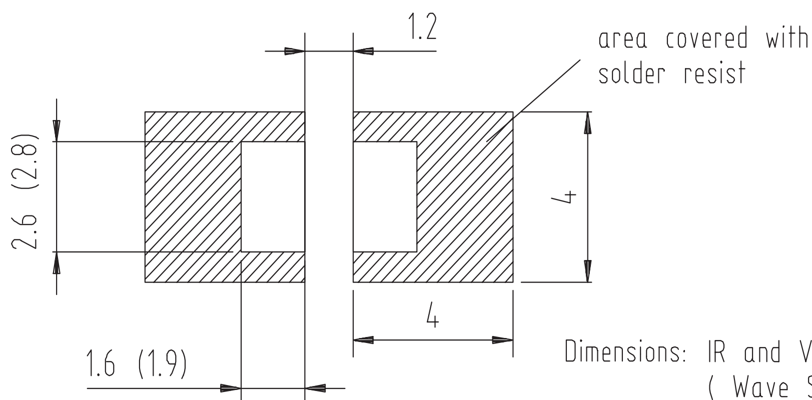


Pad Layout



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Temperature - Time Profile

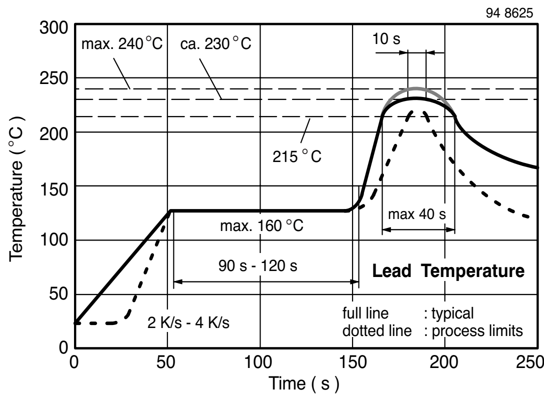

Figure 11. Infrared Reflow Soldering Optodevices (SMD Package)

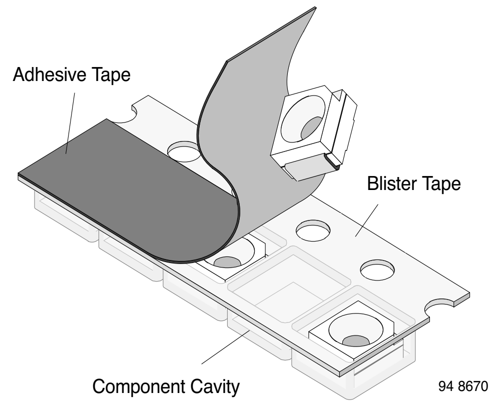

Figure 12. Blister Tape

Drypack

Devices are packed in moisture barrier bags (MBB) to prevent the products from moisture absorption during transportation and storage. Each bag contains a desiccant.

Floor Life

Floor life (time between soldering and removing from MBB) must not exceed the time indicated in J-STD-020.
TSML 3710 is released for:
Moisture Sensitivity Level 2, according to JEDEC, J-STD-020
Floor Life: 1 year
Conditions: Tamb < 30°C, RH < 60%

Drying

In case of moisture absorption devices should be baked before soldering. Conditions see J-STD-020 or Label.
Devices taped on reel dry using recommended conditions 192 h @ 40°C (+ 5°C), RH < 5%

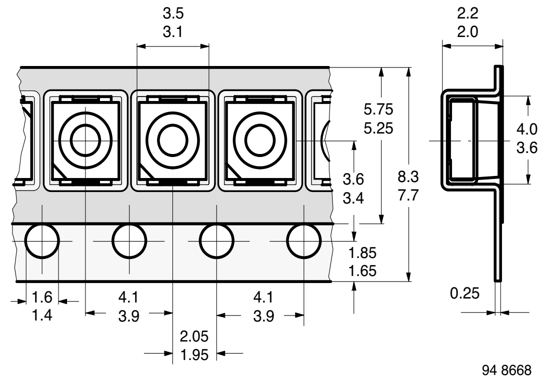

Figure 13. Tape Dimensions in mm for PLCC-2

Missing Devices

A maximum of 0.5% of the total number of components per reel may be missing, exclusively missing components at the beginning and at the end of the reel. A maximum of three consecutive components may be missing, provided this gap is followed by six consecutive components.

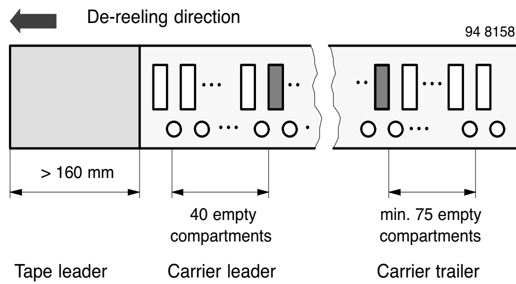

Figure 14. Beginning and End of Reel

The tape leader is at least 160 mm and is followed by a carrier tape leader with at least 40 empty compartements. The tape leader may include the carrier tape as long as the cover tape is not connected to the carrier tape.
The least comoponent is followed by a carrier tape trailer with a least 75 empty compartements and sealed with cover tape.

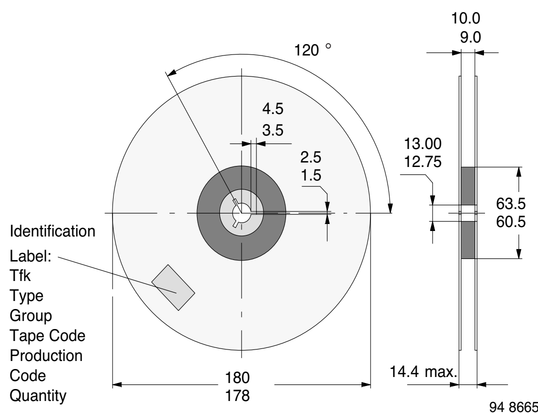

Figure 15. Dimensions of Reel

Cover Tape Removal Force

The removal force lies between 0.1 N and 1.0 N at a removal speed of 5 mm/s.
In order to prevent components from popping out of the bliesters, the cover tape must be pulled off at an angle of 180° with regard to the feed direction.

Ozone Depleting Substances Policy Statement

It is the policy of **Vishay Semiconductor GmbH** to

1. Meet all present and future national and international statutory requirements.
2. Regularly and continuously improve the performance of our products, processes, distribution and operatingsystems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

Parameters can vary in different applications. All operating parameters must be validated for each customer application by the customer. Should the buyer use Vishay Semiconductors products for any unintended or unauthorized application, the buyer shall indemnify Vishay Semiconductors against all claims, costs, damages, and expenses, arising out of, directly or indirectly, any claim of personal damage, injury or death associated with such unintended or unauthorized use.

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