Pb Free Plating Product

UF1600CT thru UF1608CT

16Ampere Heat Sink Dual Common Cathode High Efficiency Rectifiers

Features

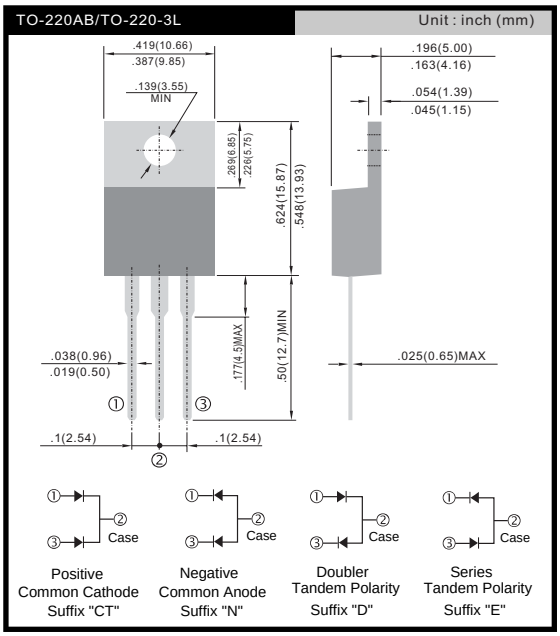
- ✶ Fast switching for high efficiency
- ✶ Low forward voltage drop
- ✶ High current capability
- ✶ Low reverse leakage current
- ✶ High surge current capability

Application

- ✶ Automotive Inverters and Solar Inverters
- ✶ Plating Power Supply,SMPS and UPS
- ✶ Car Audio Amplifiers and Sound Device Systems

Mechanical Data

- ✶ Case: Heatsink TO-220AB open metal package
- ✶ Epoxy: UL 94V-0 rate flame retardant
- ✶ Terminals: Solderable per MIL-STD-202 method 208
- ✶ Polarity: As marked on diode body
- ✶ Mounting position: Any
- ✶ Weight: 2.0 gram approximately



Maximum Ratings and Electrical Characteristics @T_A=25°C unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	UF1600CT	UF1601CT	UF1602CT	UF1603CT	UF1604CT	UF1606CT	UF1608CT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	50	100	200	300	400	600	800	V
RMS Reverse Voltage	$V_{R(RMS)}$	35	70	140	210	280	420	560	V
Average Rectified Output Current @T_C = 105°C	I_O	16							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	125							A
Forward Voltage @I_F = 8.0A	V_{FM}	1.0			1.3		1.7		V
Peak Reverse Current @T_A = 25°C At Rated DC Blocking Voltage @T_A = 125°C	I_{RM}	10 500							μA
Reverse Recovery Time (Note 1)	t_{rr}	50					100		nS
Typical Junction Capacitance (Note 2)	C_j	80					50		pF
Operating and Storage Temperature Range	T_j, T_{STG}	-65 to +150							°C

Note: 1. Measured with IF = 0.5A, IR = 1.0A, IRR = 0.25A.
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

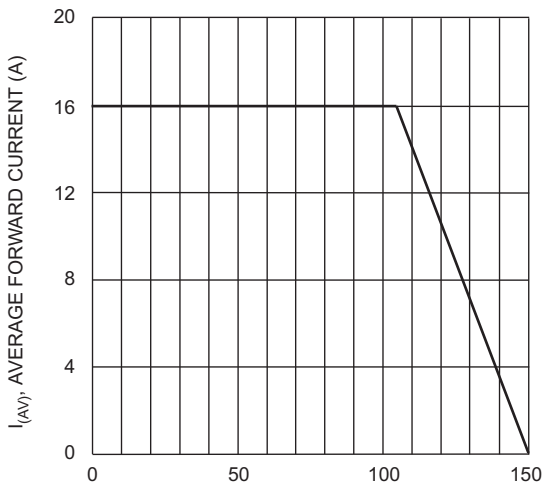


Fig. 1 Forward Current Derating Curve

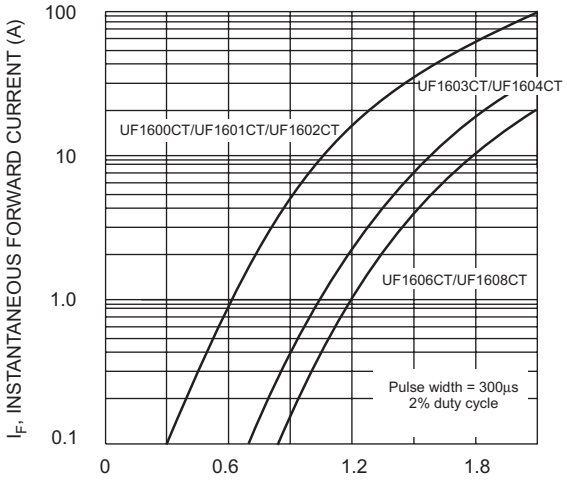


Fig. 2 Typical Forward Characteristics

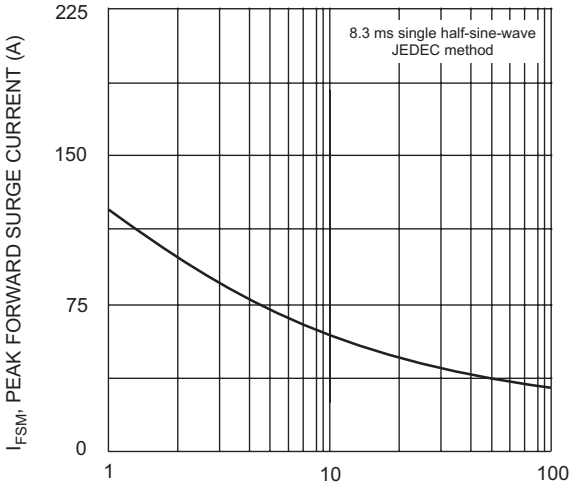


Fig. 3 Maximum Non-Repetitive Surge Current

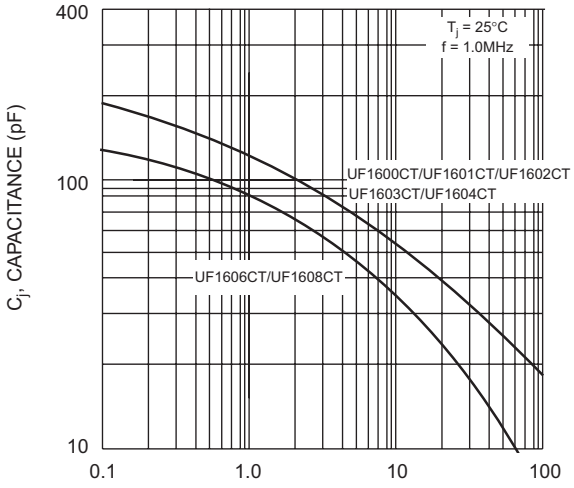
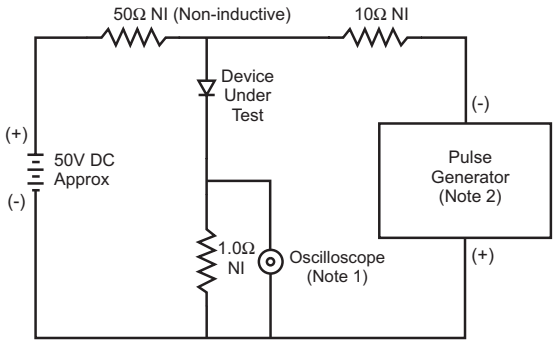


Fig. 4 Typical Junction Capacitance



Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
2. Rise Time = 10ns max. Input Impedance = 50Ω.

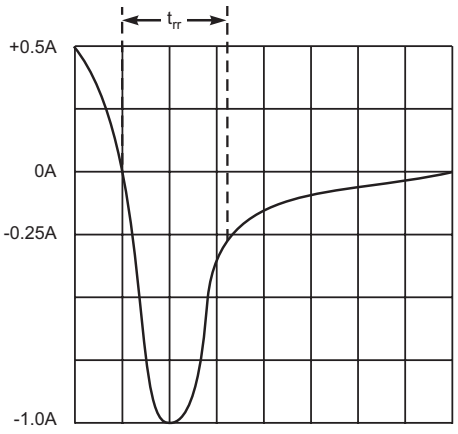


Fig. 5 Reverse Recovery Time Characteristic and Test Circuit