

Surface Mount Schottky Barrier Diode

(P/b) Lead(Pb)-Free

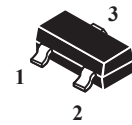
Features:

- *High Reliability
- *Low Voltage
- *Small Surface Mounting Type

Mechanical Data:

- *Case : Molded Plastic
- *Terminals : Solderable per MIL-STD-202,Method 208
- *Polarity : See Diagrams Below
- *Weight : 0.008 grams (approx.)
- *Mounting Position : Any

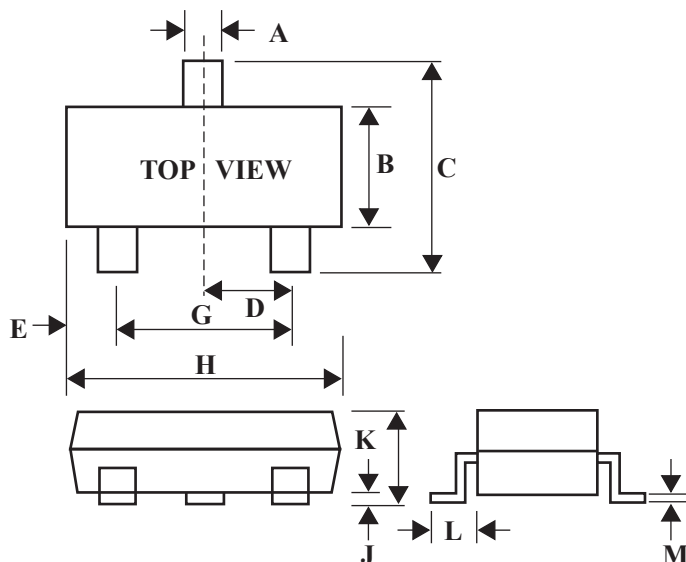
**SCHOTTKY BARRIER
RECTIFIERS
100mAMPERES
40VOLTS**



SOT-23

SOT-23 Outline Dimensions

Unit:mm



Dim	Min	Max
A	0.35	0.51
B	1.19	1.40
C	2.10	3.00
D	0.85	1.05
E	0.46	1.00
G	1.70	2.10
H	2.70	3.10
J	0.01	0.13
K	0.89	1.10
L	0.30	0.61
M	0.076	0.25


Maximum Ratings ($T_J=25^{\circ}\text{C}$ Unless otherwise noted)

Characteristic	Symbol	Limits	Unit
Peak Reverse Voltage	V_{RM}	40	Volts
DC Reverse Voltage	V_R	40	Volts
Mean Rectifying Current	I_o	0.1	A
Peak Forward Surge Current ⁽²⁾	I_{FSM}	1.0	A
Operating Junction Temperature Range	T_J	125	$^{\circ}\text{C}$
Storage Temperature Range	T_{stg}	-40 to +125	$^{\circ}\text{C}$

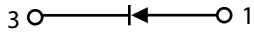
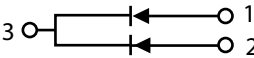
Electrical Characteristics ($T_A=25^{\circ}\text{C}$ Unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
Forward Voltage($I_F=100\text{mA}$)	V_{F1}	-	-	0.55	Volts
Forward Voltage($I_F=10\text{mA}$)	V_{F2}	-	-	0.34	Volts
Reverse Current($V_F=10\text{V}$)	I_R	-	-	30	μA
Capacitance Between Terminals ($V_R=10\text{V}, f=1\text{MHz}$)	C_T	-	6.0	-	pF

NOTE:

2.60HE for 1 

Device Marking

Item	Marking	Equivalent Circuit diagram
WSD421	LV3	
WSD425	KL3	

Electrical Characteristic Curves (Ta=25°C)

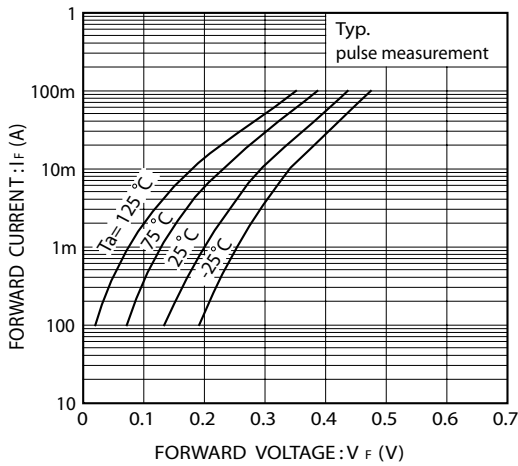


FIG.1 Forward Characteristics

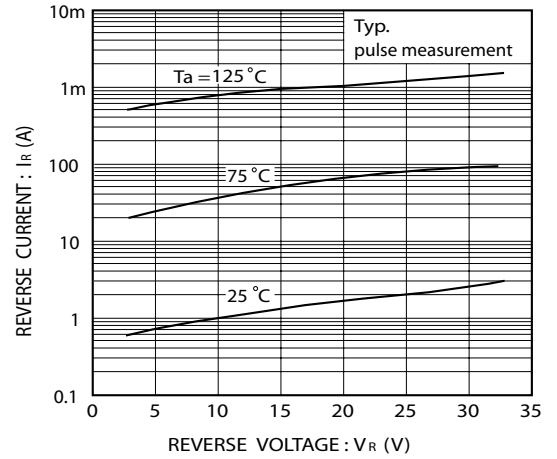


FIG.2 Reverse Characteristics

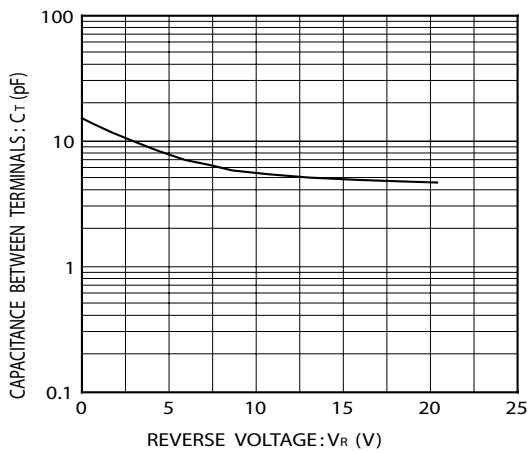


FIG.3 Capacitance Between Terminals Characteristics

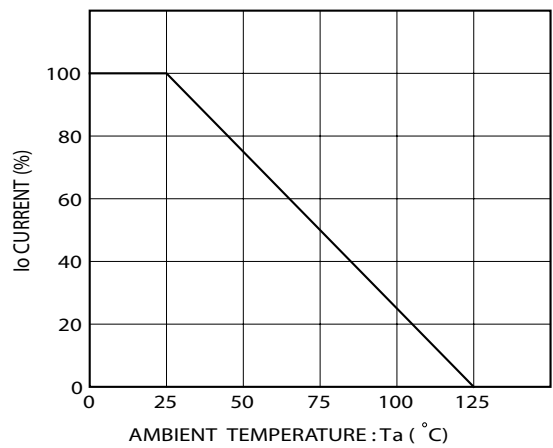


FIG.4 Derating Curve (Mounting on Glass Epoxy PCBs)