

## 2W DUAL CHANNEL AUDIO POWER AMPLIFIER—YD1316

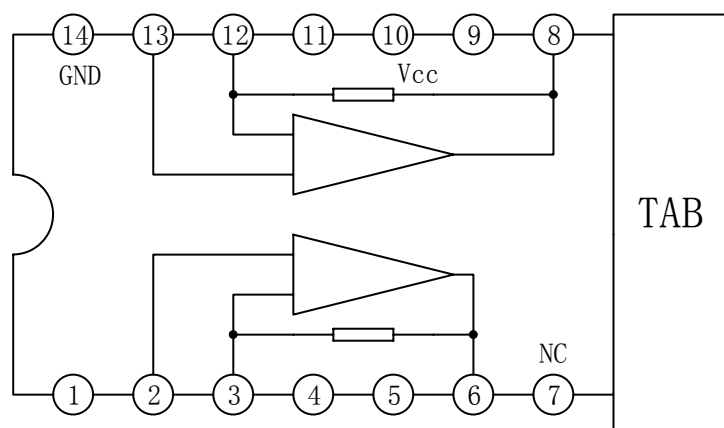
### DESCRIPTION

The YD1316 is a monolithic integrated circuit designed for the audio amplifier in tape recorders and radio.

### FEATURES

- \*Wide operating voltage ( $V_{CC}=3V$  to  $16V$ );
- \*Low quiescent current;
- \*Low Harmonic distortion;
- \*Large output power ( $P_o=2W \times 2$ , maximum,  $V_{CC}=12V$ ,  $R_L=8\Omega$ , THD=10%);
- \*Fine ripple rejection characteristic.

### BLOCK DIAGRAM



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**ABSOLUTE MAXIMUM RATINGS** (Tamb=25°C)

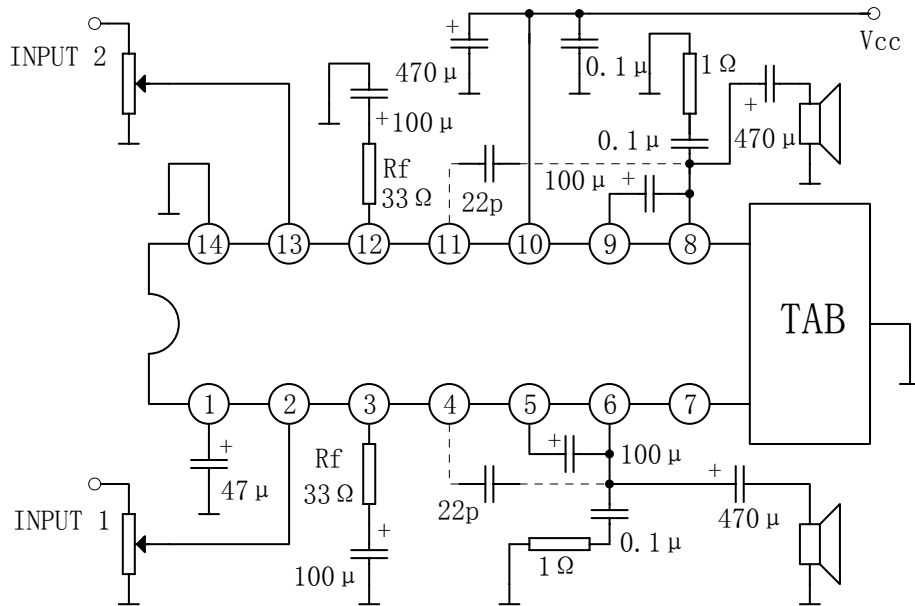
PARAMETER		SYMBOL	VALUE	UNIT
Supply Voltage (No signal)		V <sub>CC</sub>	18	V
Supply Voltage (operating)		V <sub>CC</sub>	16	V
Peak Output Current		I <sub>op</sub>	1.25	A
Power Dissipation	80*60*1.2mm <sup>3</sup> PCB	P <sub>D1</sub>	3.0	W
	No Heat Sink	P <sub>D2</sub>	1.5	W
Operating Temperature		T <sub>opr</sub>	-20 to +75	°C
Storage Temperature		T <sub>stg</sub>	-55 to 150	°C

**ELECTRICAL CHARACTERISTICS**

(Tamb=25°C, V<sub>CC</sub>=9V, R<sub>L</sub>=8Ω, R<sub>f</sub>=120Ω, f=1kHz, Unless otherwise specified)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Quiescent Circuit Current	I <sub>ccq</sub>	No Signal		10		mA
Voltage Gain	G <sub>v</sub>	P <sub>o</sub> =0.25W, R <sub>f</sub> =33Ω		44		dB
		P <sub>o</sub> =0.25W, R <sub>f</sub> =120Ω		34		
Output Power	P <sub>o</sub>	V <sub>CC</sub> =12V, R <sub>L</sub> =8Ω, THD=10%		2		W
		V <sub>CC</sub> =9V, R <sub>L</sub> =4Ω, THD=10%		1.6		
		V <sub>CC</sub> =9V, R <sub>L</sub> =8Ω, THD=10%		1.2		
		V <sub>CC</sub> =6V, R <sub>L</sub> =4Ω, THD=10%		0.7		
		V <sub>CC</sub> =6V, R <sub>L</sub> =8Ω, THD=10%		0.5		
Total Harmonic Distortion	THD	P <sub>o</sub> = 0.5W, R <sub>f</sub> =33Ω		0.8	1.0	%
		P <sub>o</sub> =0.5W, R <sub>f</sub> =120Ω		0.4		
Noise Output Voltage	V <sub>NO</sub>	R <sub>g</sub> =10kΩ		0.6	4	mV
Ripple Rejection	RR	R <sub>g</sub> =0, f <sub>r</sub> =100Hz, V <sub>r</sub> =0.3V		50		dB
Cross Talk	CT	R <sub>g</sub> =0, P <sub>o</sub> =0.25W		55		dB
Channel Balance	Δ G <sub>v</sub>	Crip	-2	0	2	dB
Input Resistance	Z <sub>i</sub>			5		MΩ

APPLICATION CIRCUIT



OUTLINE DRAWING

