

#### 150V PNP SMALL SIGNAL TRANSISTOR IN SOT23

#### **Features**

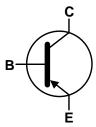
- Epitaxial Planar Die Construction
- Complementary NPN Type MMBT5551
- Ideal for Low Power Amplification and Switching
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

#### **Mechanical Data**

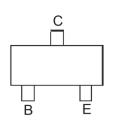
- Case: SOT23
- Case Material: Molded Plastic, "Green" Molding Compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208
- Weight: 0.008 grams (Approximate)







Device Symbol



Top View Pin-Out

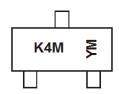
#### Ordering Information (Notes 4 & 5)

Part Number	Compliance	Marking	Reel Size (inches)	Tape Width (mm)	Quantity Per Reel
MMBT5401-7-F	AEC-Q101	K4M	7	8	3,000
MMBT5401-13-F	AEC-Q101	K4M	13	8	10,000
MMBT5401Q-7-F	Automotive	K4M	7	8	3.000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
- See https://www.diodes.com/quality/lead-free/ for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to https://www.diodes.com/quality/.
- 5. For packaging details, go to our website at https://www.diodes.com/design/support/packaging/diodes-packaging/.

### **Marking Information**



K4M = Product Type Marking Code YM = Date Code Marking Y = Year (ex: F = 2018) M = Month (ex: 3 = March)

Date Code Key

Year	2017	201	8 20	019	2020	2021	2022	2023	2024	20	25	2026
Code	E	F		G	Н	I	J	K	L	N	Л	N
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	7	Λ	5	6	7	8	a	0	N	ם



### Absolute Maximum Ratings (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	$V_{CBO}$	-160	V
Collector-Emitter Voltage	V <sub>CEO</sub>	-150	V
Emitter-Base Voltage	$V_{EBO}$	-5	V
Collector Current	lc	-600	mA

# Thermal Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit		
Power Dissipation	(Note 6)	D-	310	mW	
Power dissipation	(Note 7)	$ P_D$	350		
Thermal Resistance, Junction to Ambient	(Note 6)	)	403	°C/W	
Thermal Resistance, Junction to Ambient	(Note 7)	R <sub>0JA</sub>	357	-C/VV	
Thermal Resistance, Junction to Leads (Note 8)		$R_{ heta JL}$	350	°C/W	
Operating and Storage Temperature Range		$T_{J,}T_{STG}$	-55 to +150	°C	

## ESD Ratings (Note 9)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes:

- 6. For a device mounted on minimum recommended pad layout 1oz copper that is on a single-sided FR-4 PCB; device is measured under still air conditions whilst operating in a steady-state.

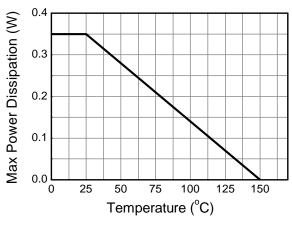
  7. Same as note (6), except the device is mounted on 15 mm x 15mm 1oz copper.

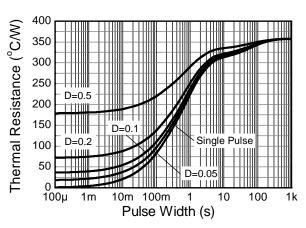
  8. Thermal resistance from junction to solder-point (at the end of the leads).

  9. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



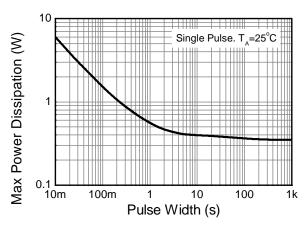
# **Thermal Characteristics and Derating Information**





### **Derating Curve**

**Transient Thermal Impedance** 



**Pulse Power Dissipation** 



# Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 10)					
Collector-Base Breakdown Voltage	BV <sub>CBO</sub>	-160	_	V	$I_C = -100\mu A, I_E = 0$
Collector-Emitter Breakdown Voltage	BV <sub>CEO</sub>	-150		V	$I_C = -1 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV <sub>EBO</sub>	-5		V	$I_E = -100 \mu A, I_C = 0$
Collector Cutoff Current	I <sub>CBO</sub>	_	-50	nA	$V_{CB} = -120V, I_{E} = 0$
	.080	<u> </u>	-50	μΑ	$V_{CB} = -120V, I_E = 0, T_A = +100^{\circ}C$
Emitter Cutoff Current	I <sub>EBO</sub>	_	-50	nA	$V_{EB} = -4V, I_{C} = 0$
ON CHARACTERISTICS (Note 10)				,	<del>_</del>
		50	_		$I_C = -1 \text{mA}$ , $V_{CE} = -5 \text{V}$
DC Current Gain	h <sub>FE</sub>	60	240	_	$I_C = -10 \text{mA}, V_{CE} = -5 \text{V}$
		50	_		$I_C = -50 \text{mA}, V_{CE} = -5 \text{V}$
Collector-Emitter Saturation Voltage	V	_	-0.2	V	$I_C = -10mA$ , $I_B = -1mA$
Collector-Emitter Saturation Voltage	V <sub>CE(SAT)</sub>		-0.5	V	$I_C = -50 \text{mA}, I_B = -5 \text{mA}$
Base-Emitter Saturation Voltage	V		-1	V	$I_C = -10mA$ , $I_B = -1mA$
ŭ	V <sub>BE</sub> (SAT)		- 1	V	$I_C = -50 \text{mA}, I_B = -5 \text{mA}$
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	$C_{obo}$		6	pF	$V_{CB} = -10V$ , $f = 1MHz$ , $I_{E} = 0$
Small Signal Current Gain	h <sub>fe</sub>	40	260	_	$V_{CE} = -10V, I_{C} = -1mA,$ f = 1kHz
Current Gain-Bandwidth Product	f⊤	100	300	MHz	$V_{CE} = -10V, I_{C} = -10mA,$ f = 100MHz
Noise Figure	NF	_	8.0	dB	$V_{CE} = -5V$ , $I_{C} = -200\mu A$ , $R_{S} = 10\Omega$ , $f = 1kHz$

Notes: 10. Measured under pulsed conditions. Pulse width  $\leq$  300 $\mu$ s. Duty cycle  $\leq$  2%.



# Typical Electrical Characteristics (@T<sub>A</sub> = +25°C, unless otherwise specified.)

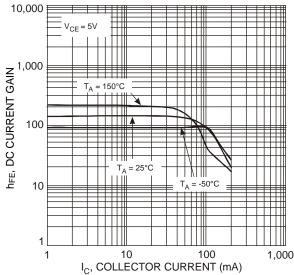
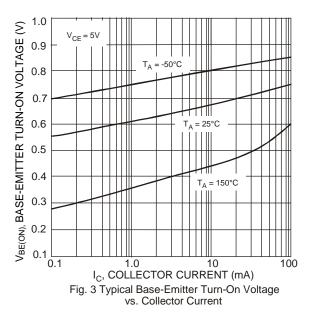


Fig. 1 Typical DC Current Gain vs. Collector Current



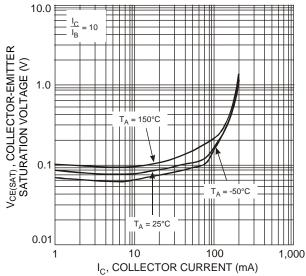


Fig. 2 Typical Collector-Emitter Saturation Voltage vs. Collector Current

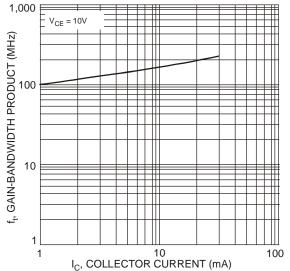


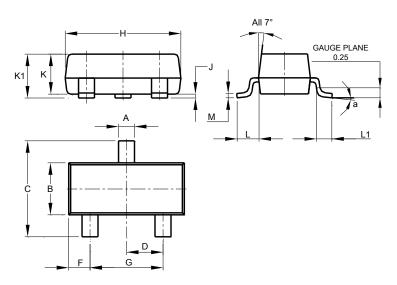
Fig.4 Typical Gain-Bandwidth Product vs. Collector Current



#### **Package Outline Dimensions**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23

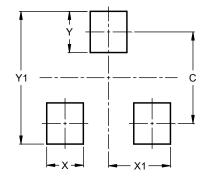


	SOT23						
Dim	Min	Max	Тур				
Α	0.37	0.51	0.40				
В	1.20	1.40	1.30				
С	2.30	2.50	2.40				
D	0.89	1.03	0.915				
F	0.45	0.60	0.535				
G	1.78	2.05	1.83				
Н	2.80	3.00	2.90				
J	0.013	0.10	0.05				
K	0.890	1.00	0.975				
K1	0.903	1.10	1.025				
L	0.45	0.61	0.55				
L1	0.25	0.55	0.40				
M	0.085	0.150	0.110				
а	0°	8°	-				
All	All Dimensions in mm						

# **Suggested Pad Layout**

Please see http://www.diodes.com/package-outlines.html for the latest version.

#### SOT23



Dimensions	Value (in mm)
С	2.0
Х	0.8
X1	1.35
Υ	0.9
Y1	2.9

Note: For high voltage applications, the appropriate industry sector guidelines should be considered with regards to creepage and clearance distances between device terminals and PCB tracking.



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