

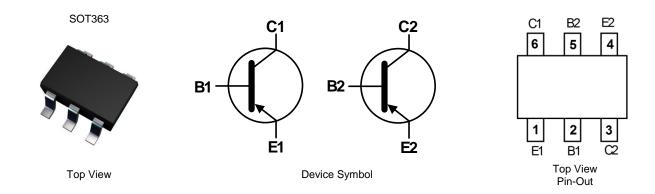
60V DUAL PNP SMALL SIGNAL TRANSISTOR IN SOT363

Features

- Ultra-Small Surface Mount Package
- Epitaxial Planar Die Construction
- Ideal for Low Power Amplification and Switching
- Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability
- PPAP Capable (Note 4)

Mechanical Data

- Case: SOT363
- Case Material: Molded Plastic, "Green" Molding Compound.
 UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Finish. Solderable per MIL-STD-202, Method 208 (3)
- Weight: 0.006 grams (Approximate)



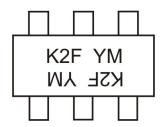
Ordering Information (Notes 4 & 5)

Product	Status	Compliance	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMDT2907A-7-F	Active	AEC-Q101	K2F	7	8	3,000
MMDT2907AQ-7-F	Active	Automotive	K2F	7	8	3,000

Notes:

- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- See http://www.diodes.com/quality/lead_free.html for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
- 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. Automotive products are AEC-Q101 qualified and are PPAP capable. Automotive, AEC-Q101 and standard products are electrically and thermally the same, except where specified. For more information, please refer to http://www.diodes.com/product_compliance_definitions.html.
- 5. For packaging details, go to our website at http://www.diodes.com/products/packages.html.

Marking Information



K2F = Product Type Marking Code YM = Date Code Marking Y = Year (ex: D = 2016) M = Month (ex: 9 = September)

Date Code Kev

Year	2016	2017	2018	2019	2020	2021	2022	2023	2024	2025	2026	2027
Code	D	Е	F	G	Н		J	K	L	М	N	0
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec



Absolute Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-60	V
Collector-Emitter Voltage	V _{CEO}	-60	V
Emitter-Base Voltage	V_{EBO}	-5.0	V
Collector Current	Ic	-600	mA

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

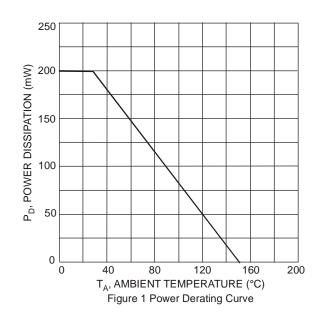
Characteristic	Symbol	Value	Unit
Power Dissipation (Note 6)	P_{D}	200	mW
Thermal Resistance, Junction to Ambient Air (Note 6)	$R_{ heta JA}$	625	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

ESD Ratings (Note 7)

Characteristic	Symbol	Value	Unit	JEDEC Class
Electrostatic Discharge - Human Body Model	ESD HBM	4,000	V	3A
Electrostatic Discharge - Machine Model	ESD MM	400	V	С

Notes:

Thermal Characteristics and Derating Information



^{6.} For the device mounted on minimum recommended pad layout FR-4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
7. Refer to JEDEC specification JESD22-A114 and JESD22-A115.



Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV _{CBO}	-60	_	_	V	$I_C = -10\mu A, I_B = 0$
Collector-Emitter Breakdown Voltage (Note 8)	BV _{CEO}	-60	_		V	$I_C = -10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV_{EBO}	-5	-	1	V	$I_E = -10\mu A, I_C = 0$
Collector Cutoff Current	1	1	1	-10	nA	$V_{CB} = -50V, I_E = 0$
Collector Cutoff Current	I _{CBO}	_	_	-10	μΑ	$V_{CB} = -50V, I_E = 0, T_A = +125$ °C
Collector Cutoff Current	ICEX	_	_	-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -0.5V$
Base Cutoff Current	I_{BL}	_	_	-50	nA	$V_{CE} = -30V, V_{EB(OFF)} = -0.5V$
ON CHARACTERISTICS (Note 8)						
		75	_	_		$I_C = -100 \mu A$, $V_{CE} = -10 V$
		100	_	_		$I_C = -1.0 \text{mA}, V_{CE} = -10 \text{V}$
DC Current Gain	h_{FE}	100	_	_	_	$I_C = -10 \text{mA}, V_{CE} = -10 \text{V}$
		100	_	300		$I_C = -150 \text{mA}, V_{CE} = -10 \text{V}$
		50	_	-		$I_C = -500 \text{mA}, V_{CE} = -10 \text{V}$
Collector-Emitter Saturation Voltage	V05()		_	-0.4	V	$I_C = -150 \text{mA}, I_B = -15 \text{mA}$
Concetor Emitter Cataration Voltage	V _{CE(sat)}			-1.6	v	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
Base-Emitter Saturation Voltage	V _{BE(sat)}	_	_	-1.3	V	$I_C = -150 \text{mA}, I_B = -15 \text{mA}$
3	V BE(Sat)			-2.6	ľ	$I_C = -500 \text{mA}, I_B = -50 \text{mA}$
SMALL SIGNAL CHARACTERISTICS					1	
Output Capacitance	C _{OBO}	_	_	8.0	pF	$V_{CB} = -10V$, $f = 1.0MHz$, $I_E = 0$
Input Capacitance	C _{IBO}	_	_	30	pF	$V_{EB} = -2.0V$, $f = 1.0MHz$, $I_{C} = 0$
Current Gain Bandwidth Product	f_T	200	_	_	MHz	$V_{CE} = -20V, I_{C} = -50mA,$ f = 100MHz
SWITCHING CHARACTERISTICS						
Turn-On Time	t _{on}	-	1	45	ns	V 20V I 450m A
Delay Time	t _d	-	1	10	ns	$V_{CC} = -30V, I_C = -150mA,$
Rise Time	tr			40	ns	I _{B1} = -15mA
Turn-Off Time	t _{off}	_	_	100	ns	V 6V 1 450 A
Storage Time	ts	_	_	80	ns	$V_{CC} = -6V, I_C = -150mA,$
Fall Time	t _f	_		30	ns	$I_{B1} = -I_{B2} = -15\text{mA}$

Note:

8. Short duration pulse test used to minimize self-heating effect.



Typical Electrical Characteristics (@T_A = +25°C, unless otherwise specified.)

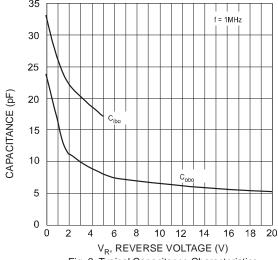


Fig. 2, Typical Capacitance Characteristics

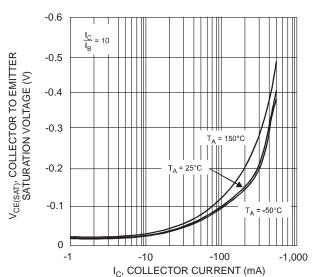


Fig. 4, Collector Emitter Saturation Voltage vs.
Collector Current

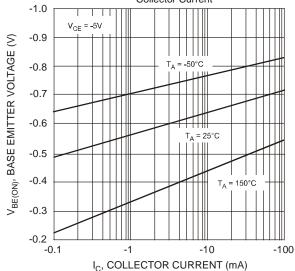


Fig. 6, Base Emitter Voltage vs. Collector Current

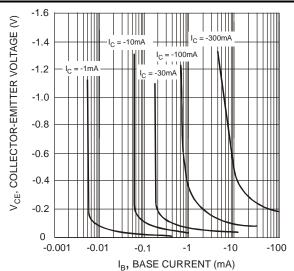


Fig. 3, Typical Collector Saturation Region

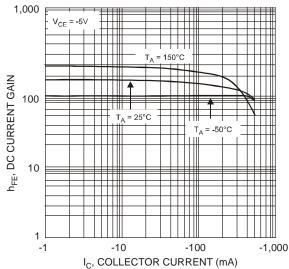


Fig. 5, DC Current Gain vs. Collector Current

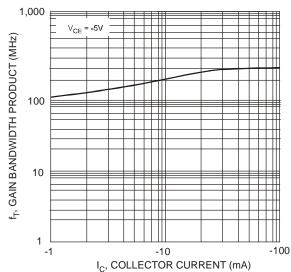


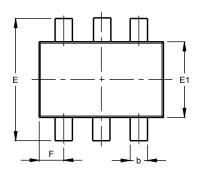
Fig. 7, Gain Bandwidth Product vs.
Collector Current

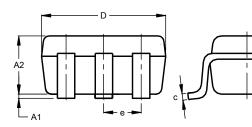


Package Outline Dimensions

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



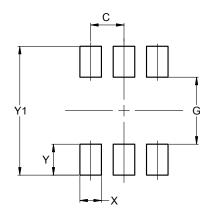


SOT363						
Dim	Min	Max	Тур			
A1	0.00	0.10	0.05			
A2	0.90	1.00	1.00			
b	0.10	0.30	0.25			
C	0.10	0.22	0.11			
D	1.80	2.20	2.15			
Е	2.00	2.20	2.10			
E1	1.15	1.35	1.30			
е	(0.650 BSC				
F	0.40	0.45	0.425			
١	0.25	0.40	0.30			
а	0°	8°				
All Dimensions in mm						

Suggested Pad Layout

Please see http://www.diodes.com/package-outlines.html for the latest version.

SOT363



Dimensions	Value		
Dillielisions	(in mm)		
С	0.650		
G	1.300		
Х	0.420		
Υ	0.600		
Y1	2.500		



IMPORTANT NOTICE

DIODES INCORPORATED MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARDS TO THIS DOCUMENT, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE (AND THEIR EQUIVALENTS UNDER THE LAWS OF ANY JURISDICTION).

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to this document and any product described herein. Diodes Incorporated does not assume any liability arising out of the application or use of this document or any product described herein; neither does Diodes Incorporated convey any license under its patent or trademark rights, nor the rights of others. Any Customer or user of this document or products described herein in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on Diodes Incorporated website, harmless against all damages.

Diodes Incorporated does not warrant or accept any liability whatsoever in respect of any products purchased through unauthorized sales channel. Should Customers purchase or use Diodes Incorporated products for any unintended or unauthorized application, Customers shall indemnify and hold Diodes Incorporated and its representatives harmless against all claims, damages, expenses, and attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized application.

Products described herein may be covered by one or more United States, international or foreign patents pending. Product names and markings noted herein may also be covered by one or more United States, international or foreign trademarks.

This document is written in English but may be translated into multiple languages for reference. Only the English version of this document is the final and determinative format released by Diodes Incorporated.

LIFE SUPPORT

Diodes Incorporated products are specifically not authorized for use as critical components in life support devices or systems without the express written approval of the Chief Executive Officer of Diodes Incorporated. As used herein:

- A. Life support devices or systems are devices or systems which:
 - 1. are intended to implant into the body, or
 - 2. support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in significant injury to the user.
- B. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or to affect its safety or effectiveness.

Customers represent that they have all necessary expertise in the safety and regulatory ramifications of their life support devices or systems, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of Diodes Incorporated products in such safety-critical, life support devices or systems, notwithstanding any devices- or systems-related information or support that may be provided by Diodes Incorporated. Further, Customers must fully indemnify Diodes Incorporated and its representatives against any damages arising out of the use of Diodes Incorporated products in such safety-critical, life support devices or systems.

Copyright © 2016, Diodes Incorporated

www.diodes.com